

FEATURES

Tiny 3.35 mm × 2.50 mm × 0.88 mm surface-mount package

High SNR of 62 dBA

High sensitivity of -38 dBV

Flat frequency response from 100 Hz to 15 kHz

Low current consumption: <250 μA

Single-ended analog output

High PSRR of 70 dB

Compatible with Sn/Pb and Pb-free solder processes

RoHS/WEEE compliant

APPLICATIONS

Smartphones and feature phones

Teleconferencing systems

Digital video cameras

Bluetooth headsets

Video phones

Tablets

GENERAL DESCRIPTION

The ADMP404 is a high quality, high performance, low power, analog output bottom-ported omnidirectional MEMS microphone. The ADMP404 consists of a MEMS microphone element, an impedance converter, and an output amplifier. The ADMP404 sensitivity specification makes it an excellent choice for both near field and far field applications. The ADMP404 has a high signal-to-noise ratio (SNR) and flat, wideband frequency response, resulting in natural sound with high intelligibility. Its low current consumption enables long battery life for portable applications. A built-in particle filter provides high reliability. The ADMP404 complies with the TIA-920 *Telecommunications Telephone Terminal Equipment Transmission Requirements for Wideband Digital Wireline Telephones* standard.

The ADMP404 is available in an ultraminiature 3.35 mm × 2.50 mm × 0.88 mm surface-mount package. It is reflow solder compatible with no sensitivity degradation. The ADMP404 is halide free.

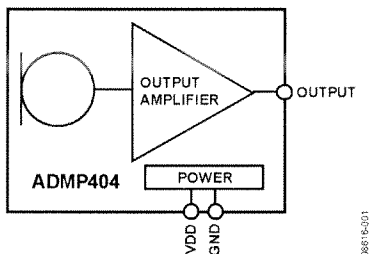
FUNCTIONAL BLOCK DIAGRAM

Figure 1.

088116-011

Rev. B

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Specifications subject to change without notice. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices. Trademarks and registered trademarks are the property of their respective owners.

One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106, U.S.A.
Tel: 781.329.4700 www.analog.com
Fax: 781.461.3113 ©2010–2011 Analog Devices, Inc. All rights reserved.

KE-825ITC-00006518

TABLE OF CONTENTS

Features	1	Connecting to Analog Devices, Inc., Audio Codecs	7
Applications	1	Supporting Documents	7
General Description	1	PCB Land Pattern Layout	8
Functional Block Diagram	1	Handling Instructions	9
Revision History	2	Pick and Place Equipment	9
Specifications	3	Reflow Solder	9
Absolute Maximum Ratings	4	Board Wash	9
ESD Caution	4	Reliability Specifications	10
Pin Configuration and Function Descriptions	5	Outline Dimensions	11
Typical Performance Characteristics	6	Ordering Guide	11
Applications Information	7		

REVISION HISTORY

8/11—Rev. A to Rev. B

Changes to Figure 1	1
Changes to Supply Voltage Parameter, Table 1	3
Changes to Table 3	4
Added Connecting to Analog Devices, Inc., Audio Codecs Section and Supporting Documents Section	7
Changes to Pick and Place Equipment Section (20 kg to 10 kg)	9
Added LGA_CAV Tape and Reel Outline Dimensions, Figure 12	11

12/10—Rev. 0 to Rev. A

Changes to Applications Section and General Description Section	1
Changes to Table 1	3
Changes to Table 2	4

7/10—Revision 0: Initial Version

SPECIFICATIONS

$T_A = 25^\circ\text{C}$, $V_{DD} = 1.8\text{ V}$, unless otherwise noted. All minimum and maximum specifications are guaranteed. Typical specifications are not guaranteed.

Table 1.

Parameter	Symbol	Test Conditions/Comments	Min	Typ	Max	Unit
PERFORMANCE						
Directionality				Omni		
Sensitivity		1 kHz, 94 dB SPL	-41	-38	-35	dBV
Signal-to-Noise Ratio	SNR			62		dB
Equivalent Input Noise	EIN			32		dB
Dynamic Range		Derived from EIN and maximum acoustic input		88		dB
Frequency Response ¹		Low frequency -3 dB point		100		Hz
		High frequency -3 dB point		15		kHz
Total Harmonic Distortion	THD	Deviation limits from flat response within pass band		-3/+2		dB
Power Supply Rejection Ratio	PSRR	105 dB SPL			3	%
Maximum Acoustic Input		217 Hz, 100 mV p-p square wave superimposed on $V_{DD} = 1.8\text{ V}$		70		dB
		Peak		120		dB SPL
POWER SUPPLY						
Supply Voltage	V_{DD}		1.5		3.3	V
Supply Current	I_S				250	μA
OUTPUT CHARACTERISTICS						
Output Impedance	Z_{OUT}			200		Ω
Output DC Offset				0.8		V
Output Current Limit				90		μA

¹ See Figure 4 and Figure 6.

ABSOLUTE MAXIMUM RATINGS

Table 2.

Parameter	Rating
Supply Voltage	-0.3 V to +3.6V
Sound Pressure Level (SPL)	160 dB
Mechanical Shock	10,000 g
Vibration	Per MIL-STD-883 Method 2007, Test Condition B
Temperature Range	-40°C to +70°C

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ESD CAUTION



ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

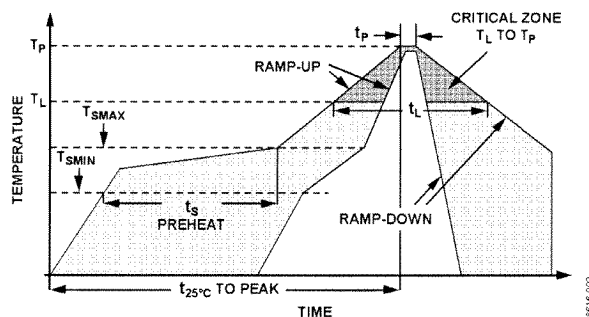
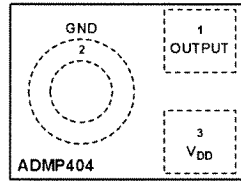


Figure 2. Recommended Soldering Profile Limits

Table 3. Recommended Soldering Profile Limits

Profile Feature	Sn/Pb	Pb-Free
Average Ramp Rate (T_L to T_p)	1.25°C/sec maximum	1.25°C/sec maximum
Preheat		
Minimum Temperature (T_{SMIN})	100°C	150°C
Maximum Temperature (T_{SMAX})	150°C	200°C
Time (T_{SMIN} to T_{SMAX}), t_s	60 sec to 75 sec	60 sec to 75 sec
Ramp-Up Rate (T_{SMAX} to T_L)	1.25°C/sec	1.25°C/sec
Time Maintained Above Liquidous (t_L)	45 sec to 75 sec	~50 sec
Liquidous Temperature (T_L)	183°C	217°C
Peak Temperature (T_p)	215°C + 3°C/-3°C	245°C + 0°C/-5°C
Time Within 5°C of Actual Peak Temperature (t_p)	20 sec to 30 sec	20 sec to 30 sec
Ramp-Down Rate	3°C/sec maximum	3°C/sec maximum
Time 25°C ($t_{25°C}$) to Peak Temperature	5 minute maximum	5 minute maximum

PIN CONFIGURATION AND FUNCTION DESCRIPTIONS



TOP VIEW
(TERMINAL SIDE DOWN)
Not to Scale

Figure 3. Pin Configuration

08/16/03

Table 4. Pin Function Descriptions

Pin No.	Mnemonic	Description
1	OUTPUT	Analog Output Signal
2	GND	Ground
3	V _{DD}	Power Supply

Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts



Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

Advanced Docket Research



With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

Analytics At Your Fingertips



Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS

Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS

Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS

Sync your system to PACER to automate legal marketing.