

United States Patent [19]

Mathers et al.

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[54]	SECURIT	Y SYSTEM FOR HARD DISK	5,282,247	1/1994	McLean et al
	DRIVE		5,375,243	12/1994	Parzych et al
			5,533,125	7/1996	Bensimon et al.
[75]	Inventors:	Stewart Mathers, Ayr; Alec Donald	5,535,409	7/1996	Larvoire et al
[]		Stewart, Fife, both of United Kingdom	5,551,033	8/1996	Foster et al
[73]	Assignee:	Calluna Technology Limited, Edinburgh, United Kingdom	FC	REIGN	PATENT DOC
		Zamouigh, Omita imgaoni	0 428 528 B1	5/1991	European Pat.
[*]	Notice:	This patent issued on a continued pros-	0 432 333	6/1991	European Pat.
ГЛ	rioties.	ecution application filed under 37 CFR	90 00771	1/1990	WIPO .
		1.53(d), and is subject to the twenty year	94/11969	5/1994	WIPO .
		patent term provisions of 35 U.S.C. 154(a)(2).		ОТНЕ	R PUBLICATI
[21]	Appl. No.:	08/640,919	Electronics In	ternation	al vol 55 No

[22] PCT Filed: Nov. 14, 1994 PCT/GB94/02508 [86] PCT No.: § 371 Date: Jul. 10, 1996 § 102(e) Date: Jul. 10, 1996 [87] PCT Pub. No.: WO95/14265 PCT Pub. Date: May 26, 1995

Foreign Application Priority Data [30]

Nov.	13, 1993	[GB]	United Kir	ıgdom .		. 9323	3453
[51]	Int. Cl. ⁷			G06F	11/00; H	04L 9	9/00
[52]	U.S. Cl.				713/202	; 711/	164
[58]	Field of	Search			. 395/186	6, 187	.01,
		305/	188 N1 · 38	0/4 - 25	3.713/	1 2	100

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710/200; 707/9; 705/18; 711/164

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5,375,243	12/1994	Parzych et al 395/725
5,533,125	7/1996	Bensimon et al
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0 428 528 B1	5/1991	European Pat. Off
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04/11060	5/1004	WIDO

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Electronics International-vol. 55, No. 3, Feb. 1982, New York, US, pp. 121-125.

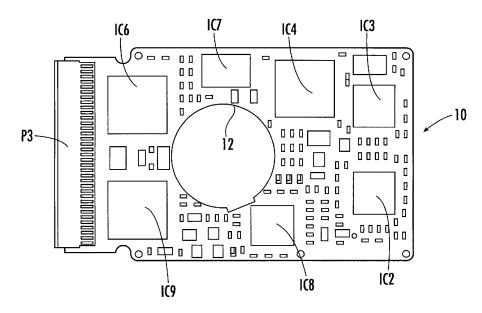
Maxtor Corporation—product literature—copyright 1993— "PCMCIA Hard Disk Drive Security".

Primary Examiner—Robert W. Beausoliel, Jr. Assistant Examiner—Scott T. Baderman Attorney, Agent, or Firm-Alston & Bird LLP

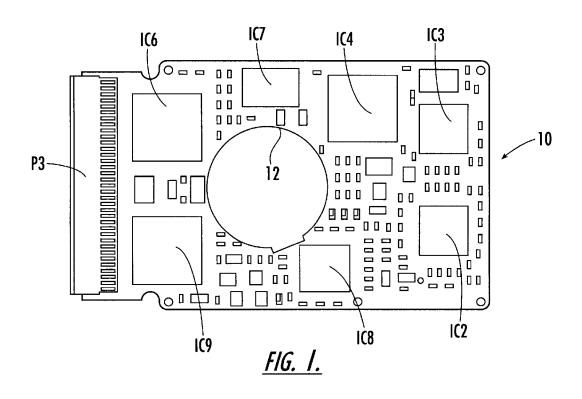
[57] **ABSTRACT**

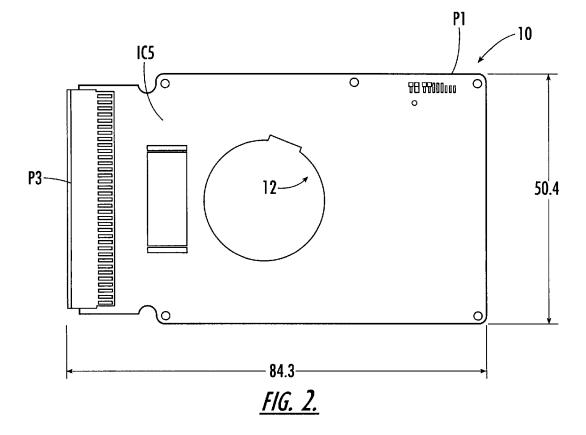
A portable hard disk drive has an electrically erasable programmable read-only-memory (EEPROM) for storing a first password for allowing a user access to the disk and a random access memory (RAM) for temporarily storing a password entered by a user. A microprocessor is arranged to compare the user-entered passed with the password stored in the EEPROM and to generate a signal to allow a user access to the disk if a valid match is found and to prohibit access if there is no match.

16 Claims, 4 Drawing Sheets

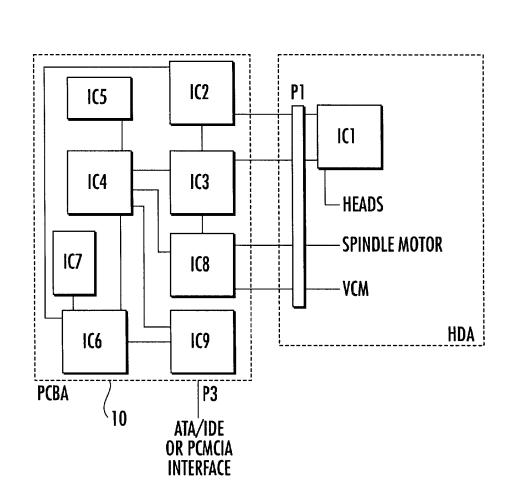












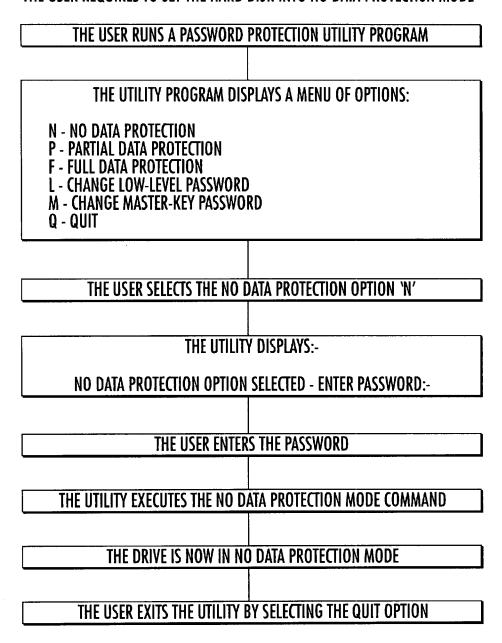
BLOCK NO	FUNCTION
ICI	PRE-AMPLIFIER
IC2	DATA CHANNEL
IC3	MIXED SIGNAL ASIC
IC4	MICRO-PROCESSOR
IC5	FLASH EEPROM
1C6	INTERFACE
IC7	RAM
IC8	MOTOR/VCM DRIVER/CONTROLLER
109	INTERFACE (PCMCIA)
Pl	ELASTOMERIC CONNECTOR
P3	68 WAY CONNECTOR

FIG. 3.



PASSWORD PROTECTION EXAMPLE

THE USER REQUIRES TO SET THE HARD DISK INTO NO DATA PROTECTION MODE



TTHE OTHER COMMANDS ARE EXECUTED IN A SIMILAR FASHION EXCEPT THAT CHANGING THE PASSWORDS OPTIONS WOULD PROMPT THE USER FOR BOTH THE OLD AND THE NEW PASSWORDS.

FIG. 4.



PASSWORD PROTECTION HARD DRIVE COMMAND HANDLING SEQUENCE

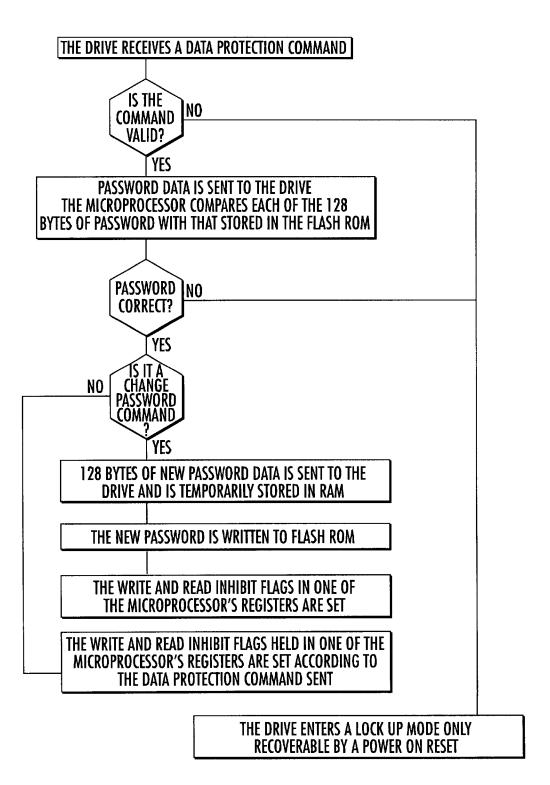


FIG. 5.



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