PAUL D. FRANZON

B. BRIEF RESUME

1. Education

- Doctor of Philosophy, Electrical and Electronic Engineering 1989, University of Adelaide, Australia . Advisor: Kamran Eshraghian.
- Bachelor of Engineering with First Class Honours, Electrical and Electronic Engineering: 1984, University of Adelaide, Australia
- Bachelor of Science, Physics and Mathematics: 1983, University of Adelaide, Australia.

2. Professional Experience

- Assistant Professor, Associate Professor, Professor, and Distinguished Professor North Carolina State University, Department of Electrical and Computer Engineering, Raleigh, North Carolina, January 1989 – present
- Technical Director, Rambus (part time/consulting), 2009-11, Chapel Hill, NC.
- Cofounder, LightSpin Technologies Inc., 2001-. Vice-President of Engineering, 2001-2002, Raleigh NC.
- PhD Candidate, University of Adelaide, Department of Electrical and Electronic Engineering, Adelaide, South Australia, Australia, April 1987 December 1988.
- Director and Co-Founder, Network Communications Pty. Ltd., Adelaide, South Australia, Australia, April 1987 April 1989.
- Consultant, AT&T Bell Laboratories, Holmdel NJ, January 1986 April 1987.
- PhD Candidate, University of Adelaide, Department of Electrical and Electronic Engineering, Adelaide, South Australia, Australia, August 1984-December 1985.
- Engineer, Defence Science and Technology Organization, Salisbury, South Australia, Australia, January 1984--July 1984
- Intern, Defense Science and Technology Organization, Salisbury, South Australia, Australia, December 1982--March 1983.
- Intern, Telecom Australia, Adelaide, South Australia, Australia, December 1981-March 1982.
- Infantry Soldier and Officer, (Ranks held: Private Captain), Royal Australian Infantry Corps, Australian Army Reserve, December 1979 December 1991.

3. Scholarly and Creative activities

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Type	Number
Books	3
Solution Manuals	1
Edited Book chapter	15
Refereed Journal article	88

Conference Paper (refereed)220Patents granted10

4. Membership in professional organizations

Fellow, Institute of Electrical and Electronic Engineers, 1984-Member, IMAPS Member, SPIE Member, Association of Computing Machinery

5. Scholarly and professional honors

- NCSU Innovator of the Year Award, 2015
- College of Engineering Board of Governor's Award, 2014
- NRL Alan Berman Research Publication Award, 2008
- Babbage Award, Synopsys, 2008
- Fellow of the IEEE, 2006
- Alcoa Research Award, 2005
- ECE Graduate Teacher of the year award, 2007
- ECE Most Helpful Teacher of the year award, 2007
- ECE Teacher of the year award, 2006
- ECE Graduate Advisor of the year award, 2006
- Alumni Undergraduate Distinguished Professor, 2003-2005.
- Graduate teacher of the year, ECE department, 2005
- NSW Australia Expatriate Scientist Award, 2003
- Selected to the NCSU Academy of Outstanding Teachers, 2001
- First round prize winner, SRC copper challenge, 2000.
- Teacher of the Year Award, presented by the IEEE Student Branch, 1997
- National Science Foundation Young Investigator's Award, 1993.
- 13 prizes while a student at the University of Adelaide

6. <u>Professional service on campus</u>

- Member, STRAG 2003-2005
- Instructor, PE preparation course 1995-2000

7. Professional service off campus

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- Consultant to DARPA, Thermal issues, 2012-14
- Consultant to Paul Hastings, OMM, KL Gates, Samsung, Micron and SK Hynix, 2015-, Patent issues including IPR, (District of Delaware, CA N. 14-cv-01432-LPS-CJB and related cases)
- Consultant to LDKM and AVT, Patent issues, 2013- (New York Southern District, 1:11-cv-06604-CM, with 08908 and 00918)
- Consultant to Skiermont Puckett and Spherix, Patent issues including IPR, 2014-2015, (Texas Northern District, 3:13-cv-3494-M and 3496)

- Consultant to Haliburton, 2014
- Consultant to DARPA, Exascale Computing Study, 2006-9.
- Consultant to Rambus, Semiconductors, 2009-12
- Consultant to Techsearch, 2008.
- Consultant, NTU, 2004-9. ASIC Design.
- Consultant to Tessera, 2009. 3DIC advising.
- Consultant, Irvine Sensors, 2006. Secure chip design.
- Consultant, Cisco Systems, 2006, Signal Integrity.
- Consultant, Talon Logic, 2005. Secure system Design.
- Consultant to O'Malveny and Meyers, 2000-2002, Patent issues.
- Consultant to Venture 2000, 2000, Due Diligance.
- Consultant to CAPPS, 1999-2000, IP Development.
- Consultant to Sofrent, 1999-2000, IP Development.
- Consultant to Ericsson, 1997, Synthesis Methodology.
- Consultant to Cadence, 1996. Evaluated possible company acquisition.
- Consultant to Polychip, 1994 2000. Circuit Design.
- Consultant to Square-D, 1996. Interconnect Design.
- Consultant to Mentor Graphics, 1995, 1996. Technical advisory board.
- Consultant to Cadence Design Systems, 1992, 1996. Technical advisory board.
- Consultant to DCT, 1995-1996. ASIC Design.
- Consultant to Techsearch International, 1989-1991. Report Preparation.
- Consultant to BNR, HP, Sun. 1992-4. Interconnect Design.
- Consultant to MCNC, 1989. CAD

II. TEACHING AND MENTORING OF UNDERGRADUATE AND GRADUATE STUDENTS

A. TEACHING EFFECTIVENESS

1. Courses Taught

DOCKET

Course	When	Enrollment	Instructor	Course
			effectiveness	excellence
ECE 464-001	S 15	19	4.3	4.3
ECE 520-001	S 15	119	4.3	4.3
ECE 520-601	S 15	11	4.8	5.0
ECE 520-603	S 15	3		
ECE 520-651	SuI 15	16	4.6	4.6
ECE 733-001	S 15	50	4.5	4.3
ECE 464-002	F 15	21	4.6	4.5
ECE 520-002	F 15	129	4.6	4.5
ECE 520-601	F 15	7	5.0	5.0
ECE 634-602 ASIC Design	F 2014	2		

ECE 520-601	S1 2014	9		
ECE 464-001	S 2014	10	4.5	4.5
ECE 520-001	S 2014	111	4.5	4.5
ECE 520-601	S 2014	9	4.5	4.5
ECE 733-001	S 2014	49	4.7	4.4
ECE 520-601	F 2013	2		
ECE 520-651 ASIC Design	SII '13	7	4.3	4.3
ECE 464-051 ASIC Design	SII '13	6	4.5	4.5
ASIC Design OOC	SII '13	635		
ECE 733 Digital Electronics	S 2013	62	4.5	4.3
ECE 520-001 ASIC Design	S 2013	136	4.6	4.5
ECE 464 ASIC Design	S 2013	(with	4.3	4.3
		above)		
ECE 520-651 ASIC Design	Sum	18		
FCF 733 Digital Electronics	S 2012	52	4.2	4 2
ECE 755 Digital Electronics	S 2012	12	4.2	4.2
ECE 520-001 ASIC Design	S 2012	116	4.3	4.2
ECE 320-001 ASIC Design	S 2012	110	4.3	4.3
ECE 404 ASIC Design	5 2012 F 2011	58	4.5	4.3
Systems	1 2011	58	4.0	4.0
ECE 520 651 ASIC Design	Sum	13	4.2	12
LEE 520-051 ASIC Design	'11	15	4.2	4.2
FCF 733 Digital Electronics	S 2011	18	4 5	45
FCF 520-601	S 2011	12	43	4.2
ECE 520-001 ASIC Design	S 2011	75	4 5	4.2
FCF 464 ASIC Design	S 2011	25	4 5	4.5
FCF 406 Des Complex	F 2010	75	4.6	4.5
Systems	1 2010	15	4.0	4.0
ECE 733 Digital Circuits 001	S 2010	36	43	42
ECE 733 Digital Circuits 601	S 2010	3	5.0	5.0
ECE 520 ASIC Design 001	S 2010	57	43	<u> </u>
FCF 520 ASIC Design 601	S 2010	17	4.2	
ECE 464 ASIC Design	S 2010	9	4 3	4 4
ECE 733 Digital Circuits 001	S 2009	57	4.6	4 5
ECE 733 Digital Circuits 601	S 2009	7	4.4	4 2
ECE 520 ASIC Design 001	S 2009	128	4.6	4.4
ECE 520 ASIC Design 601	S 2009	17	4.5	4.20
ECE 520 ASIC Design 620	S 2009	1		1.20
ECE 464 ASIC Design	S 2009	16	4.6	44
ECE 733 Digital Circuits 001	S 2008	67	4 64	4 64
ECE 733 Digital Circuits 601	S 2008	2	1.01	
ECE 520 ASIC Design 001	S 2008	101	4 76	4 59
ECE 520 ASIC Design 601	S 2008	15	4 5	4 20
ECE 464 ASIC Design 001	S 2008	27	4 76	4 59
LCL TOT ADIC DUSIEII	5 2000	<i>4</i> I	7.70	т.J/

ECE 733	S 2007	58	4.64	4.46
ECE 520 ASIC Design 001	S 2007	129	4.75	4.68
ECE 520 ASIC Design 601	S 2007	6		
ECE 464 ASIC Design	S 2007	17	4.75	4.68
ECE 745 ASIC Verification	F 2006	20		4.6
ECE 733 Digital Circuits 001	S 2006	43	4.5	4.6
ECE 520 ASIC Design 001	S 2006	65		
ECE 520 ASIC Design 601	S 2006	8		
ECE 464 ASIC Design 001	S 2006	17	4.4	4.1
ECE 733 Digital Circuits 001	S 2005	30	4.5	4.6
ECE 733 Digital Circuits 601	S 2005	5		
ECE 520 ASIC Design 001	S 2005	44	4.6	4.3
ECE 520 ASIC Design 002	S 2005	34	4.6	4.5
ECE 520 ASIC Design 601	S 2005	2		
ECE 464 ASIC Design 001	S 2005	15	4.9	4.1
ECE 464 ASIC Design 002	S 2005	13	4.9	4.1
ECE 733 Digital circuits	S 2004	45	4.2 (4.1)	4.2 (3.9)
ECE 520 ASIC Design	S 2004	76	4.4 (4.1)	4.3 (3.9)
ECE 520 ASIC Design 601	S 2004	17		
ECE 464 ASIC Design	S 2004	69	4.5 (4.1)	4.0 (3.9)
ECE 733 Digital Circuits	S 2003	84	4.3 (4.0)	4.2 (3.8)
ECE 520 ASIC Design	S 2003	81	4.2 (4.0)	3.9 (3.8)
ECE 520 ASIC Design 601	S 2003	30		
ECE 464 ASIC Design	S 2003	63	3.9 (4.0)	3.5 (3.8)
ECE 406 Design Complex DS	F 2002	167	4.2 (4.1)	3.8 (3.9)
ECE 520 ASIC Design	S 2002	239	4.4 (4.1)	4.2 (3.9)
ECE 520 ASIC Design 601	S 2002			
ECE 464 ASIC Design	S 2002	39	4.1 (4.1)	3.6 (3.9)
ECE 406 Des. Complex Dig	F 2000		4.7 (4.0)	4.5 (3.7)
Sys				
ECE 704 Design For Test	F 2000		4.6 (4.0)	4 (3.8)
ECE 520 ASIC Design 001	S 2000		4.5 (4.1)	4.4 (3.9)
ECE 520 ASIC Design 002	S 2000		4.6 (4.1)	4.3 (3.9)
ECE 520 ASIC Design 601	S 2000			
ECE 492B ASIC Design	S 2000		4.5 (4.1)	4.3 (3.9)
ECE 342 Des. Complex Dig	F 1999		4.4 (4.1)	4 (3.9)
Sys				
ECE 520 ASIC Design 005	S 1999		4.6 (4.2)	4.5 (4.0)
ECE 520 ASIC Design 006	S 1999		4.5 (4.2)	4.3 (4.0)
ECE 342 Des. Complex Dig	F 1998		4.3 (4.1)	4.1 (3.9)
Sys				
ECE 520 ASIC Design	S 1998		4.6 (4.1)	4.4 (3.9)
ECE 492B ASIC Design	S 1998		4.7 (4.1)	4.8 (4.1)
ECE 342 Des. Complex Dig	F 1997		4.6 (4.0)	4.4 (4.0)
Sys				

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