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Kimura

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[54]	PORTABL MEMORY	E SECURE SEMICONDUCTOR DEVICE
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[73]	Assignee:	Mitsubishi Denki Kabushiki Kaisha, Tokyo, Japan
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	: 31, 1989 [JI : 31, 1989 [JI	
[51] [52]	U.S. Cl	
[58]	Field of Sea	arch 380/3, 23, 25, 49; 235/380, 441, 382, 379, 492, 487
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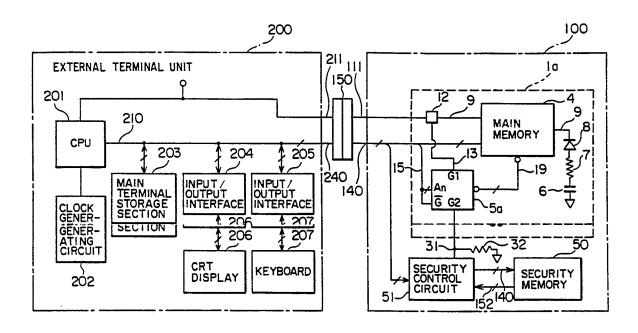
Primary Examiner-David Cain

Attorney, Agent, or Firm-Leydig, Voit & Mayer

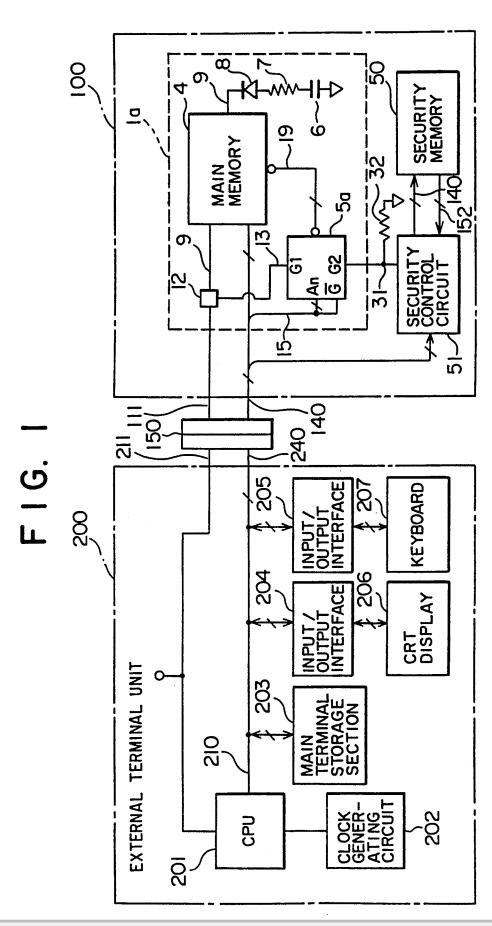
ABSTRACT

A portable semiconductor memory device for interfacing with and exchanging information with an external terminal, said portable device having a security function for controlling access to a main memory. The main memory is adapted to exchange data with the external terminal by means of an interface bus which includes data lines, address lines and control lines. Access controls means in the portable unit is utilized to control access to the main memory. The unit also has a security memory comprising two sections. One section stores enciphered data which is read out to the external terminal, deciphered and returned to the unit as the first element used in a comparison. The second section of the security memory stores internal identification information. The two elements of identification information are provided to a comparator means which enables access to the main memory via the interface bus after a match is detected.

33 Claims, 6 Drawing Sheets

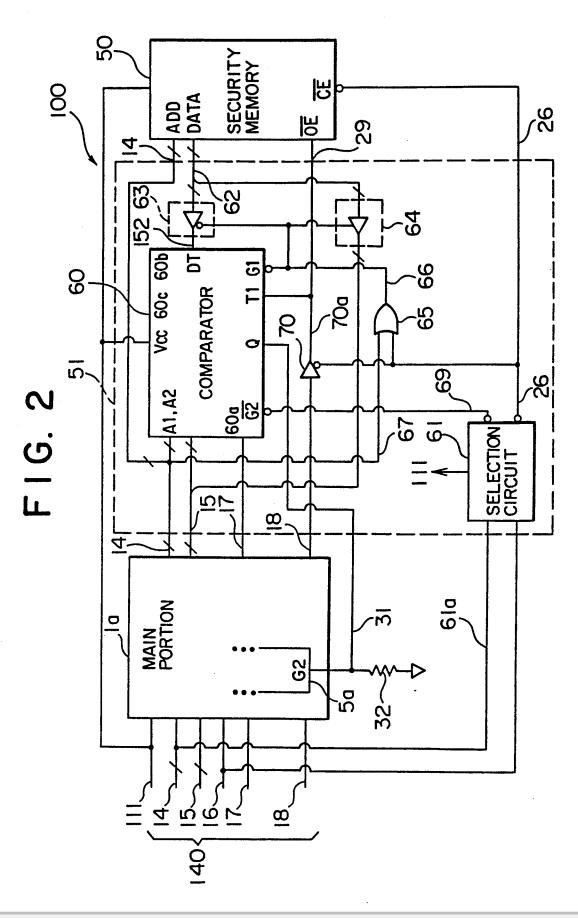








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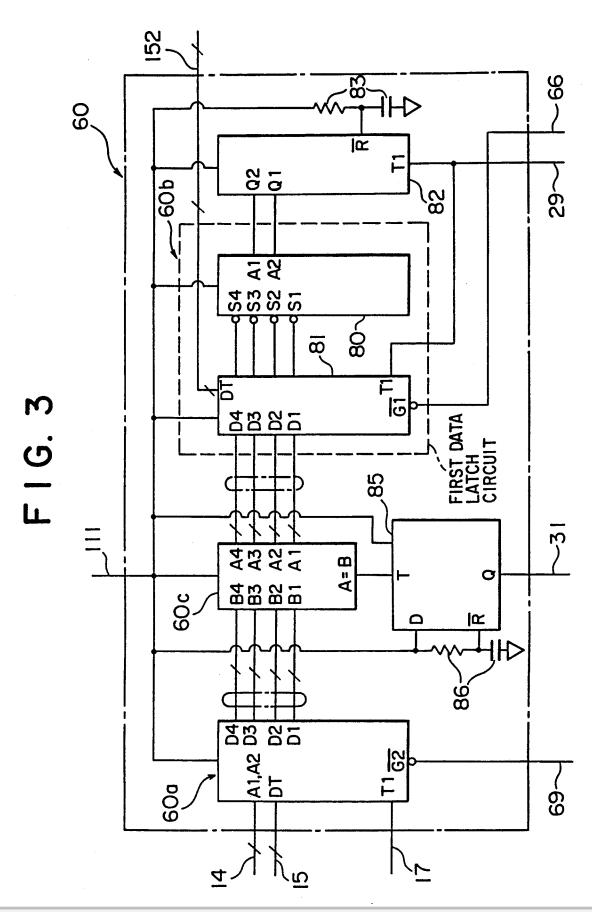
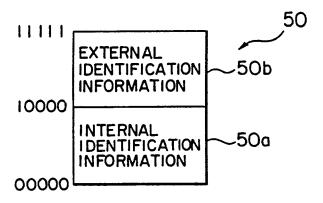


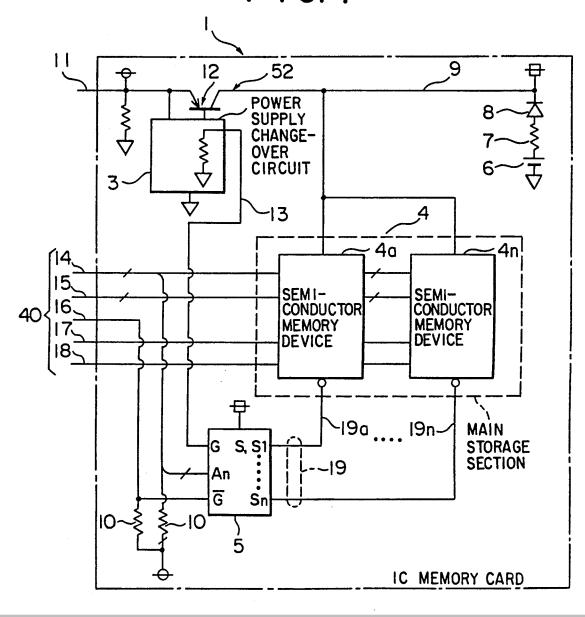


FIG. 4

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