



[54] SEMICONDUCTOR MEMORY DEVICE

[56] References Cited

[75] Inventors: Chul-soo Kim, Suwon; Ho-cheol Lee, Seoul, both of Rep. of Korea

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[57] ABSTRACT

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There is provided a semiconductor memory device which does not require an additional input pad to apply a signal for discriminating between a normal cell and a redundant cell. The semiconductor memory device has (claim 1). Therefore, the normal cell array or the redundant cell array is sequentially selected and tested by using the same input pad to which the bank select bit is input, without an additional pad.

[30] Foreign Application Priority Data

Dec. 30, 1995 [KR] Rep. of Korea ..... 95/69724

[51] Int. Cl.<sup>6</sup> ..... G11C 7/00

[52] U.S. Cl. .... 365/201; 365/200; 365/230.03

[58] Field of Search ..... 365/200, 201, 365/230.03

11 Claims, 4 Drawing Sheets

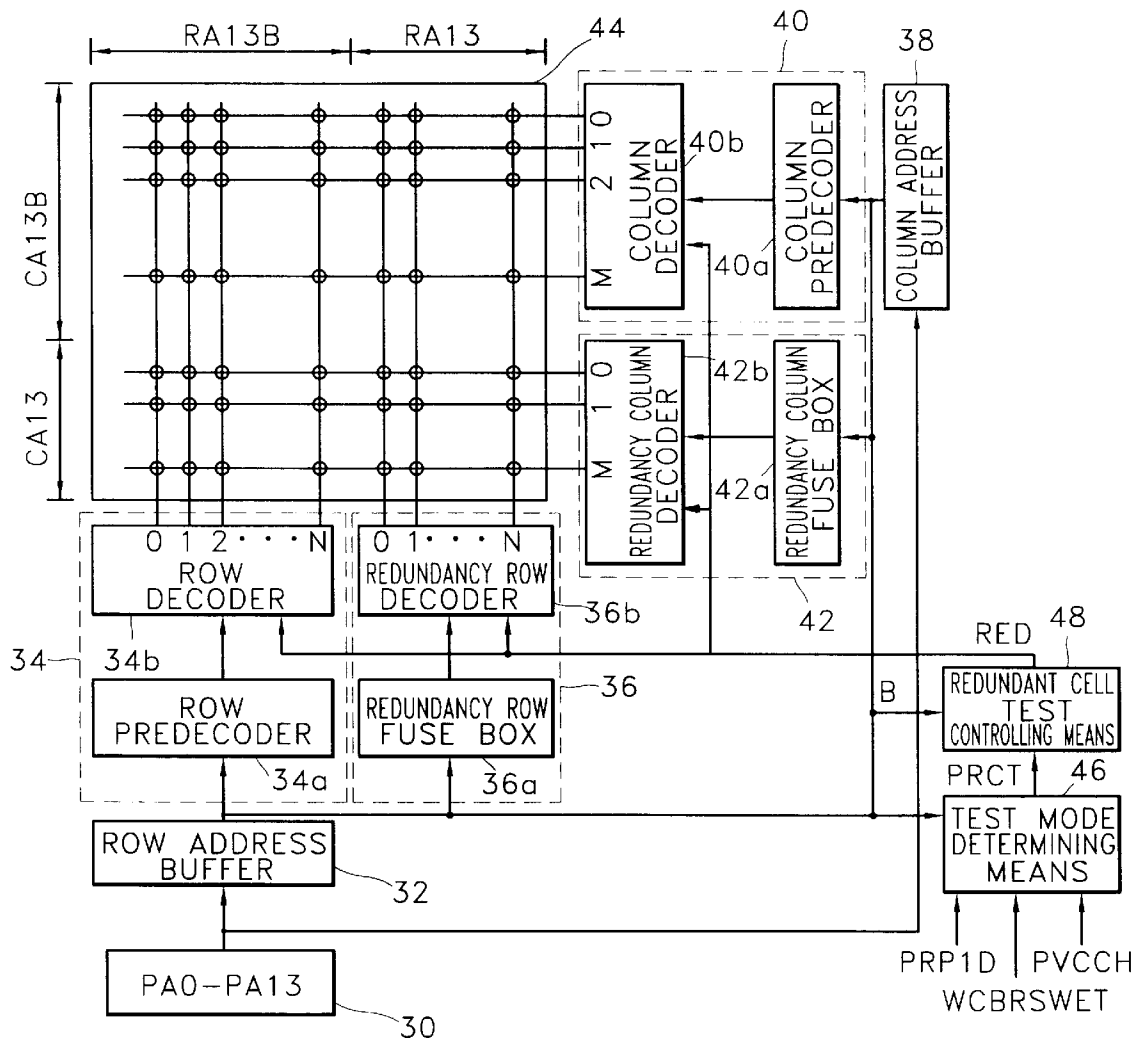


FIG. 1 (PRIOR ART)

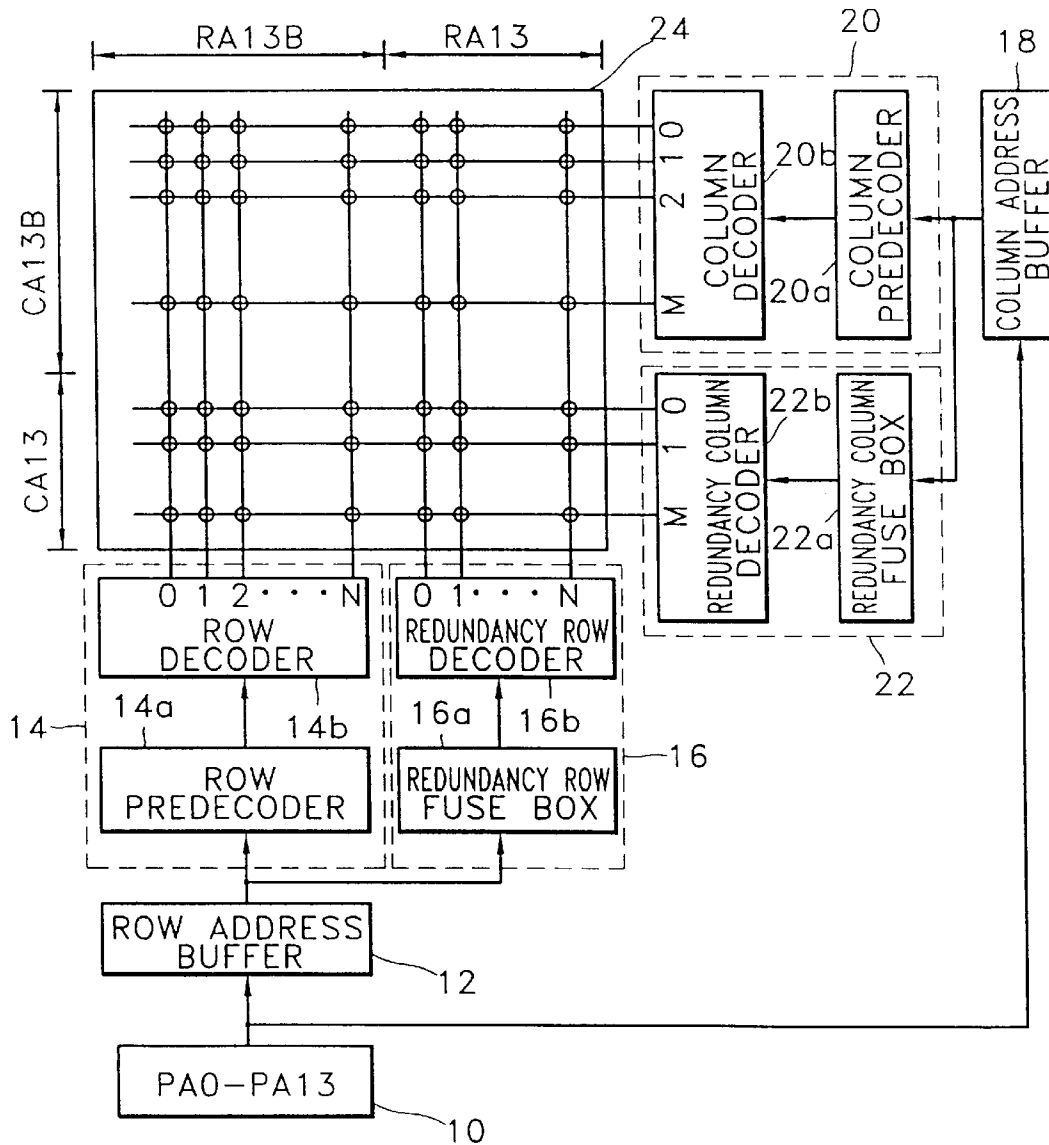


FIG. 2

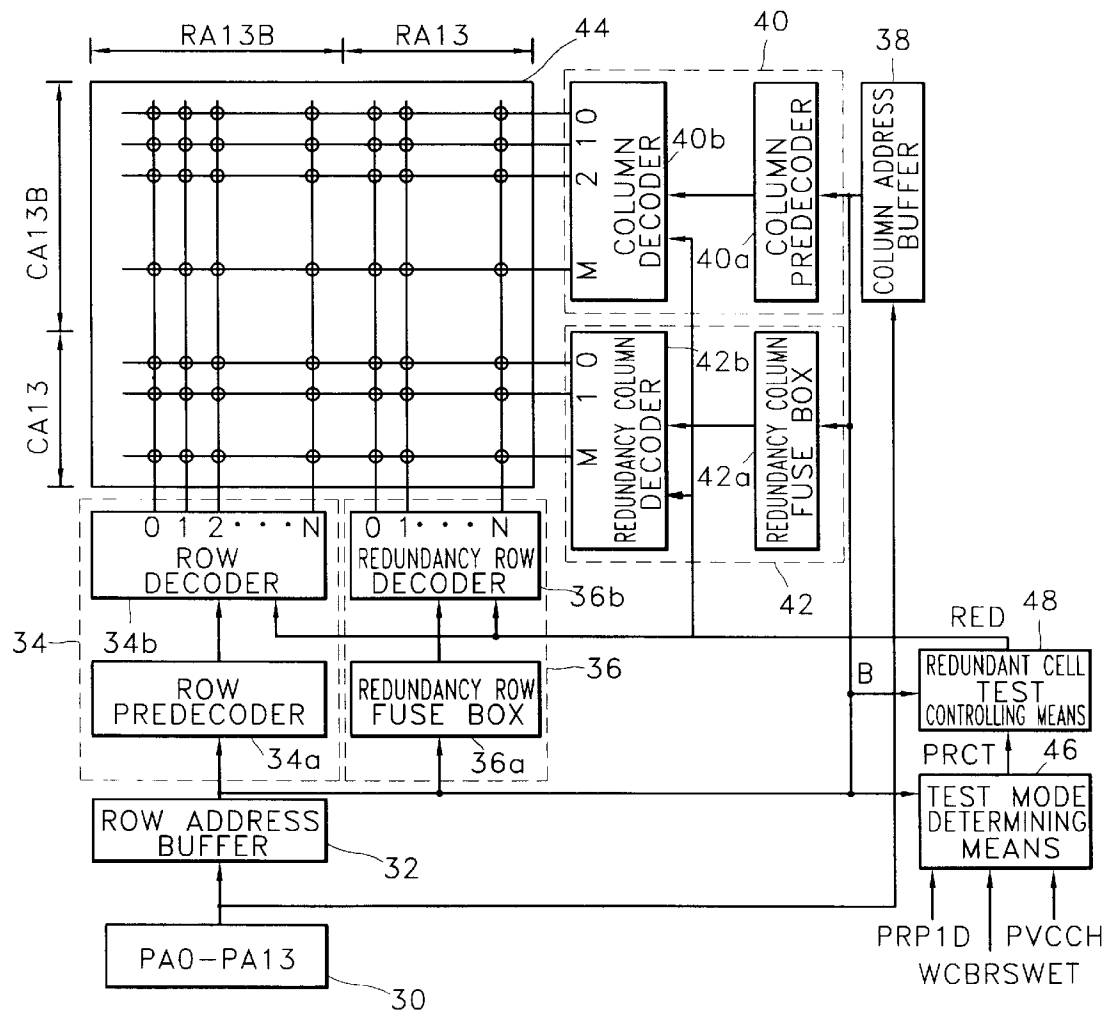


FIG. 3

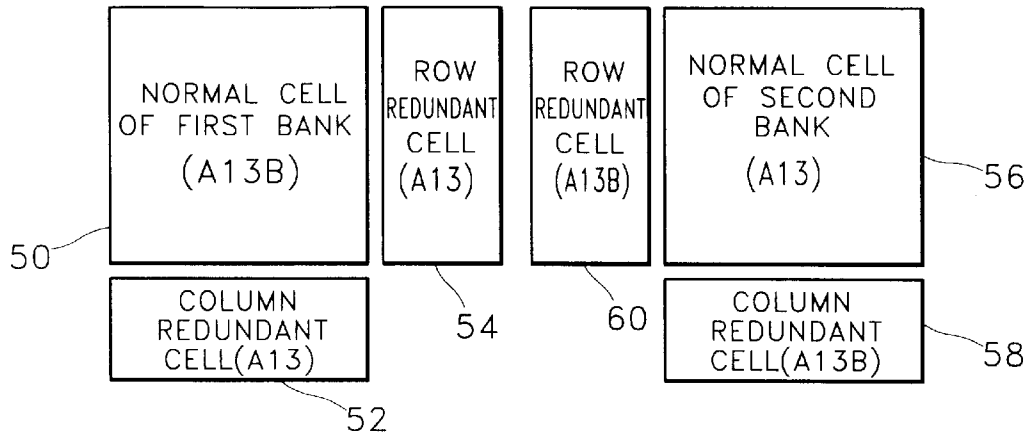


FIG. 4

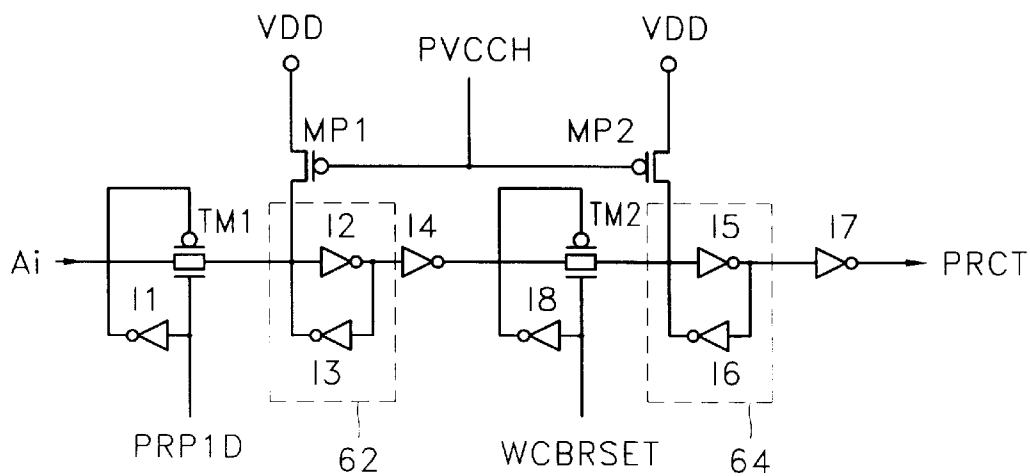
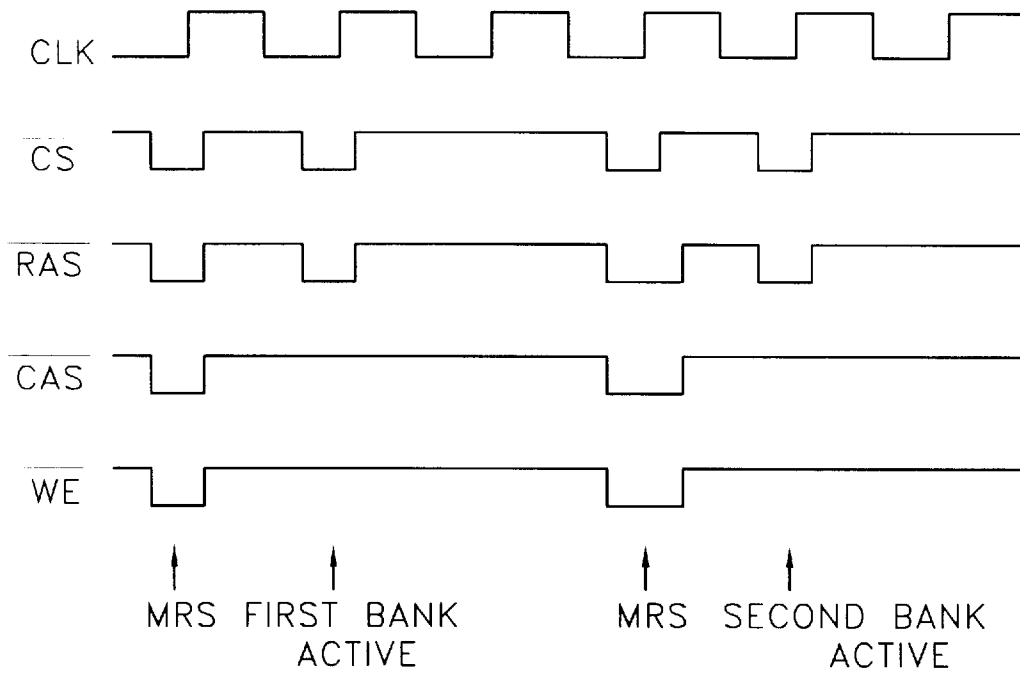


FIG. 5



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