

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

AGILTRON, INC.
Petitioner

v.

MEMSCAP S.A.
Patent Owner

Patent No. 6,262,512

Issued: July 17, 2001

Filed: November 8, 1999

Title: THERMALLY ACTUATED MICROELECTROMECHANICAL
SYSTEMS INCLUDING THERMAL ISOLATION STRUCTURES

Inter Partes Review No. IPR2016-01683

**PETITION FOR *INTER PARTES* REVIEW
UNDER 35 USC §§ 311-319 AND 37 CFR § 42.100 *ET. SEQ.***

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| <u>EXHIBIT LIST</u> | |
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| Exhibit No. | Description |
| 1001 | US Patent No. 6,262,512 (“512 Pat.”) |
| 1002 | File History of US Patent No. 6,262,512 |
| 1005 | US Patent No. 5,050,838 <i>Beatty et al.</i> (“Beatty”) |
| 1006 | Erno H. Klassen <i>et al.</i> , “Silicon Fusion Bonding and Deep Reactive Ion Etching; A New Technology for Microstructures” (“ <i>Klaassen</i> ”) |
| 1007 | US Patent No. 6,070,851 <i>Tsai et al.</i> (“ <i>Tsai</i> ”) |
| 1008 | US Patent No. 5,741,740 <i>Jang et al.</i> (“ <i>Jang</i> ”) |
| 1009 | US Patent No. 5,682,053 <i>Wiszniowski</i> (“ <i>Wiszneiwski</i> ”) |
| 1010 | T. Lisec <i>et al.</i> , “Thermally Driven Microvalve with Buckling Behaviour for Pneumatic Applications” (“ <i>Lisec</i> ”) |
| 1011 | J. Mark Noworolski <i>et al.</i> , “Fabrication of SOI Wafers With Buried Cavities Using Silicon Fusion Bonding and Electrochemical Etchback” (“ <i>Noworoloski</i> ”) |
| 1012 | Semiannual Progress Report for the Reporting Period January 1995 to July 1995 On “Single Crystal Silicon Actuators and Sensors Based On Silicon Fusion Bonding Technology” (“ <i>Maluf</i> Report”) |
| 1013 | US Patent No. 5,534,111 <i>Hocker et al.</i> (“ <i>Hocker</i> ”) |
| 1014 | US Patent No. 4,771,016 <i>Bajor et al.</i> (“ <i>Bajor</i> ”) |
| 1015 | US Patent No. 5,783,854 <i>Dries et al.</i> (“ <i>Dries</i> ”) |
| 1016 | Excerpts from N. Maluf, “An Introduction to Microelectromechanical Systems Engineering” (“ <i>Maluf</i> Text”) |
| 1017 | US Patent No. 5,909,078 <i>Wood et al.</i> (“ <i>Wood</i> ”) |

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| 1018 | M. Asheghi <i>et al</i> , “Temperature-Dependent Thermal Conductivity of Single-Crystal Silicon Layers in SOI Substrates” (“ <i>Ashegi</i> ”). |
| 1019 | O. Paul <i>et al</i> , “Thermal Conductivity of CMOS Materials for the Optimization of Microsensors” (“ <i>Paul</i> ”). |

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