

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

K/S HIMPP
Petitioner

v.

III Holdings 7 LLC
Patent Owner.

Case No. IPR2017-00929

**DECLARATION OF LES ATLAS, PH.D.
REGARDING U.S. PATENT NO. 7,929,722**

HIMDB 1002

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3. [1.3] “comparing a functionality of a hearing assist device to a functionality of the coprocessor device to determine if: a signal processing functionality absent from the hearing assist device is available on the coprocessor device or a signal processing functionality absent from the coprocessor device is available on the hearing assist device; or a signal processing functionality present on the hearing assist device is enhanced on the	

coprocessor device or a signal processing functionality present on the coprocessor device is enhanced on the hearing assist device”21

4. [1.4] “directing a signal obtained at the hearing assist device for at least partial processing to at least one the hearing assist device or the coprocessor device”24

B. Claim 226

1. [2.1] “One or more processor-readable storage media of claim 1, further comprising: detecting an additional coprocessor device” 26

2. [2.2] “comparing the functionality of at least one of the hearing assist device or the coprocessor device to the additional coprocessor device to determine if: a signal processing functionality absent from one of the hearing assist device, the coprocessor device, or the additional coprocessor device is available on the hearing assist device, the coprocessor device, or the additional coprocessor device; or a signal processing functionality present on one of the hearing assist device, the coprocessor device, or the additional coprocessor device is enhanced on the hearing assist device, the coprocessor device, or the additional coprocessor device”26

3. [2.3] “directing the signal obtained at the hearing assist device for at least partial processing to at least one the hearing assist device or the coprocessor device, or the additional coprocessor device”28

C. Claim 3 – “one or more processor-readable storage media of claim 1, wherein the signal processing functionality present on the hearing assist device is enhanced on the coprocessor device or the [sic, an] additional coprocessor device, the enhancement comprising at least one of an enhanced signal processing algorithm or an enhanced processing capability of the coprocessor device or the additional coprocessor device”28

D. Claim 7 – “one or more processor-readable storage media of claim 1, wherein the signal is processed at least partially in series by a plurality of the hearing assist device, the coprocessor device, or the [sic] one or more additional coprocessor devices”29

E. Claim 830

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