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APPLICATION NO.	ISSUE DATE	PATENT NO.	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	01/06/2015	8930719	SCOT0014-6	5811

31518 7590 12/17/2014

NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

ISSUE NOTIFICATION

The projected patent number and issue date are specified above.

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(application filed on or after May 29, 2000)

The Patent Term Adjustment is 0 day(s). Any patent to issue from the above-identified application will include an indication of the adjustment on the front page.

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Application Assistance Unit (AAU) of the Office of Data Management (ODM) at (571)-272-4200.

APPLICANT(s) (Please see PAIR WEB site http://pair.uspto.gov for additional applicants):

Scott A. Moskowitz, Sunny Isles Beach, FL;

Change(s) applied
to document,
/M.H.E./
10/29/2014

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Path: Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ReferenceCitationList_SCOT0014-6_10-3-2012.wpd
37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET: 13/556,420 / SCOT0014-6
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	P 069	20100005308	January 2010	Moskowit	X
	P 070	20100098251	Apr 2010	Moskowitz	X
	P 071	20100220861	Sept 2010	Moskowitz	X
	P 072	20100202607	Aug 2010	Moskowitz	X
April 25, 2002	P 073	20020047873	~~June 2002~~	Imanaka et al. ~~Petrovic~~	X
	P 074	20020009208	Jan 2002	Alattar	X
	P 075	20010029580	October 2001	Moskowitz	X
Matsumoto	P 076	20100182570	July 2010	~~Chota~~	X
	P 077	20100077220	March 2010	Moskowitz	X
	P 078	20100077219	March 2010	Moskowitz	X
	P 079	20100064140	March 2010	Moskowitz	X
	P 080	20100153734	June 2010	Moskowitz	X
	P 081	20100106736	April 2010	Moskowitz	X
	P 082	20060251291	November 2006	Rhoads	X
Change(s) applied	P 083	20030002862	January 2003	Rodriguez	
to document,	P 084	~~2003005780~~	May 2003	Hansen	20030055780
/J.H./	P 085	20020152179	Oct 2002	Racov	
12/9/2014	P 086	20030027549	Feb 2003	Kiel	
	P 087	20020057651	May 2002	Roberts	

DATE: 03/21/2013	EXAMINER'S SIGNATURE: /Izunna Okeke/

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Issue Classification	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	**Art Unit**
	IZUNNA OKEKE	2432

☐ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original
1	58	17	74	33	90	49	106								
18	59	2	75	34	91	50	107								
3	60	19	76	35	92										
4	61	20	77	36	93										
5	62	21	78	37	94										
6	63	22	79	38	95										
7	64	23	80	39	96										
8	65	24	81	40	97										
9	66	25	82	41	98										
10	67	26	83	42	99										
11	68	27	84	43	100										
12	69	28	85	44	101										
13	70	29	86	45	102										
14	71	30	87	46	103										
15	72	31	88	47	104										
16	73	32	89	48	105										

Change(s) applied
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/N.D.W./
10/17/2014

NONE (Assistant Examiner)	(Date)	**Total Claims Allowed:** 50	
/IZUNNA OKEKE/ Primary Examiner.Art Unit 2432 (Primary Examiner)	09/06/2014 (Date)	O.G. Print Claim(s) 93	O.G. Print Figure 1

U.S. Patent and Trademark Office Part of Paper No. 20140905

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	F 023-	WO0203385	Jan 2002	WO		X
	F 024-	WO9701892	~~June 1995~~	WO	1997-01-16	X
	F 025-	WO9726732	July 1997	WO		X
	F 026-	WO9802864	Jan 1998	WO		X
	F 027-	EP1547337	Mar 2006	EP		X
	F 028-	EP0581317A2	Feb 1994	EP		X
	F 029-	WO023385A1	Oct 2002	WO		X

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LISTING OF FOREIGN AND INTERNATIONAL PATENT DOCUMENTS - F Series

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	F 01-	EP0372601	Jun., 1990	EP		X
	F 02-	EP0565947	Oct., 1993	EP		X
	F 03-	EP0581317	Feb., 1994	EP		X
	F 04-	EP0649261	Apr., 1995	EP		X
	F 05-	EP0651554	May., 1995	EP		X
	F 06-	EP1354276	Dec., 2007	EP		X
	F 07-	NL 1005523	Sep., 1998	NL		X
	F 08-	WO 9514289	May., 1995	WO		X
	F 09-	WO 9629795	Sep., 1996	WO		X
	F 010-	WO 9724833	Jul., 1997	WO		X
	F 011-	WO 9744736	Nov., 1997	WO		X
	F 012-	WO9837513	Aug., 1998	WO		X
	F 013-	WO 9952271	Oct., 1999	WO		X
	F 014-	WO 9962044	Dec., 1999	WO		X
	F 015-	WO 9963443	Dec., 1999	WO		X
	F 016-	WO9726733	Jan. 1997	WO		X
	F 017-	WO98002864	~~Jul. 1997~~	WO	1998-01-22	X
	F 018-	WO 0057643	Sept 2000	WO		X
	F 019-	WO 9642151	Dec 1996	WO		X
	F 020-	EP0872073	July 1996	EP		X
	F 021-	WO0118628	March 2001	WO		X
	F 022-	WO0143026	June 2001	WO		X

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	P 069	20100005308	January 2010	Moskowitz	X
	P 070	20100098251	Apr 2010	Moskowitz	X
	P 071	20100220861	Sept 2010	Moskowitz	X
	P 072	20100202607	Aug 2010	Moskowitz	X
April 25, 2002	P 073	20020047873	~~June 2002~~	~~Petrovic~~	X
	P 074	20020009208	Jan 2002	Alattar	X
	P 075	20010029580	October 2001	Moskowitz	X
Matsumoto	P 076	20100182570	July 2010	~~Chota~~	X
	P 077	20100077220	March 2010	Moskowitz	X
	P 078	20100077219	March 2010	Moskowitz	X
	P 079	20100064140	March 2010	Moskowitz	X
	P 080	20100153734	June 2010	Moskowitz	X
	P 081	20100106736	April 2010	Moskowitz	X
	P 082	20060251291	November 2006	Rhoads	X
	P 083	20030002862	January 2003	Rodriguez	X
January 9, 2003	P 084	2003005780	~~May 2003~~	Hansen	X
	P 085	20020152179	Oct 2002	Racov	X
	P 086	20030027549	Feb 2003	Kiel	X
	P 087	20020057651	May 2002	Roberts	X
	P 088	20110069864	March 2011	Moskowitz	
	P 089	20100313033	Dec 2010	Moskowitz	
	P 090	20110019691	Jan 2011	Moskowitz	
	P 091	20030023852	Jan. 2003	Wold	

Imanaka

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	U 0367	6556976	~~Aug 1987~~	Callen	April 29, 2003
	U 0368	6574608	Jun 2003	Dahod	
	U 0369	6601044	Jul 2003	Wallman	
	U 0370	6594643	Jul 2003	Freeny	
	U 0371	6618188	Sep 2003	Haga	
	U 0372	6778968	Aug 2004	Gulati	
	U 0373	6839686	Jan 2005	Galant	
	U 0374	~~6856867~~	Feb 2005	Woolston	6,856,967
	U 0375	6876982	Apr 2005	Lancaster	
	U 0376	7003480	Feb 2006	Fox	
	U 0377	5822436	Oct 1998	Rhoads	

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July 8, 2003	U 0298	6590996	~~Jun 2003~~	Reed	X
	U 0299	5949055	Sept 1999	Fleet	X
	U 0300	6067622	May 2000	Moore	X
July 20, 2010	U 0301	7761712	~~Jun 2010~~	Moskowitz	X
	U 0302	7743001	Jun 2010	Vermeulen	X
	U 0303	6865747	Mar 2005	Mercier	X
	U 0304	6611599	Aug 2003	Natarajan	X
	U 0305	6480937	Nov 2002	Vorbach	X
	U 0306	6398245	Jun 2002	Gruse	X
	U 0307	6950941	Sept 2005	Lee	X
	U 0308	6983058	Jan 2006	Fukuoka	X
	U 0309	5675653	Oct 1997	Nelson	X
	U 0310	6804453	Oct 2004	Sasamoto	X
	U 0311	6178405	Jan 2001	Ouyang	X
	U 0312	5839100	Nov 1998	Wegener	X
	U 0313	5781184	Jul 1998	Wasserman	X
	U 0314	5617506	Apr 1997	Burk	X
	U 0315	5327520	Jul 1994	Chen	X
	U 0316	5111530	May 1992	Kutaragi	X
	U 0317	7095715	Aug 2006	Buckman	X
	U 0318	6173322	Jan 2001	Hu	X
	U 0319	5754938	May 1998	Herz	X
	U 0320	6035398	Mar 2000	Bjorn	

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	U 0275	7457962	November 2008	Moskowitz	X
	U 0276	7460994	December 2008	Herre et al.	X
	U 0277	7475246	January 2009	Moskowitz	X
	U 0278	7530102	May 2009	Moskowitz	X
	U 0279	7532725	May 2009	Moskowitz et al.	X
	U 0280	7568100	July 2009	Moskowitz et al.	X
	U 0281	7647502	January 2010	Moskowitz	X
	U 0282	7647503	January 2010	Moskowitz	X
	U 0283	7779261	August 2010	Moskowitz	X
	U 0284	6990453	January 2006	Wang	X
	U 0285	6081597	June 2000	Hoffstein	X
	U 0286	7035049	Apr 2006	Yamamoto	X
	U 0287	7664263	Feb 2010	Moskowitz	X
	U 0288	7286451	Oct 2007	Wirtz	X
	U 0289	6385324	May 2002	Koppen	X
	U 0290	6674858	Jan 2004	Kimura	X
	U 0291	6148333	Nov 2000	Guedalia	X
July 9, 2002	U 0292	6418421	~~Jun 2002~~	Hurtado	X
	U 0293	6385596	May 2002	Wiser	X
	U 0294	6226618	May 2001	Downs	X
	U 0295	6957330	Oct 2005	Hughes	X
	U 0296	5842213	Nov 1998	Odom	X
	U 0297	5818818	Oct 1998	Soumiya	X

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	U 0229	6799277	September 2004	Colvin	X
	U 0230	6813717	November 2004	Colvin	X
	U 0231	6813718	November 2004	Colvin	X
	U 0232	6823455	November 2004	Macy et al.	X
	U 0233	6834308	December 2004	Ikezoye et al.	X
	U 0234	6842862	January 2005	Chow et al.	X
	U 0235	6853726	February 2005	Moskowitz et al.	X
	U 0236	6857078	February 2005	Colvin	X
	U 0237	6931534	August 2005	Jandel et al.	X
	U 0238	6966002	November 2005	Torrubia-Saez	X
6,968,337	U 0239	~~6983337~~	November 2005	Wold	X
	U 0240	6977894	December 2005	Achilles et al.	X
	U 0241	6978370	December 2005	Kocher	X
	U 0242	6986063	January 2006	Colvin	X
	U 0243	7007166	February 2006	Moskowitz et al.	X
	U 0244	7020285	March 2006	Kirovski et al.	X
	U 0245	7035409	April 2006	Moskowitz	X
	U 0246	7043050	May 2006	Yuval	X
	U 0247	7046808	May 2006	Metois et al.	X
	U 0248	7050396	May 2006	Cohen et al.	X
	U 0249	7051208	May 2006	Venkatesan et al.	X
	U 0250	7058570	June 2006	Yu et al.	X
	U 0251	7093295	August 2006	Saito	X

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July 20, 2010	U 0301	7761712	~~Jun 2010~~	Moskowitz	X
	U 0302	7743001	Jun 2010	Vermeulen	X
	U 0303	6865747	Mar 2005	Mercier	
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	U 0308	6983058	Jan 2006	Fukuoka	
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	U 0311	6178405	Jan 2001	Ouyang	
	U 0312	5839100	Nov 1998	Wegener	
	U 0313	5781184	Jul 1998	Wasserman	
	U 0314	5617506	Apr 1997	Burk	
	U 0315	5327520	Jul 1994	Chen	
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	U 0321	5901178	May 1999	Lee	
	U 0322	8214175	July 2012	Moskowitz	
	U 0323	8265278	Sept 2012	Moskowitz	
	U 0324	8161286	~~Nov 2010~~	Moskowitz	April 17, 2012
	U 0325	8307213	~~Jan 2011~~	Moskowitz	November 6, 2012
	U 0326	8121343	~~May 2012~~	Moskowitz	February 21, 2012
	U 0327	5437050	Jul 1995	Lamb	
	U 0328	5123045	Jun 1992	Ostrovsky	X
	U 0329	7310815	Dec 2007	Yanovsky	
	U 0330	8179846	May 2012	Dolganow	
	U 0331	7719966	May 2010	Luft	
	U 0332	7630379	Dec 2009	Morishita	
	U 0333	5949973	Sept 1999	Yarom	
	U 0334	8400566	Mar. 2013	Terry	
	U 0335	5649284	July 1997	Yoshinobu	
	U 0336	~~744506~~	Oct 2008	Datta	7,444,506
	U 0337	6480963	~~Oct 2002~~	Tachibana	November 12, 2002
	U 0338	6510513	Jan 2003	Darrow	
	U 0339	5189411	Feb 1993	Collar	
	U 0340	5293633	Mar 1994	Robbins	
	U 0341	4633462	Dec 1986	Stifle	
	U 0342	~~5103461~~	Mar 1992	Cain	5,097,584
	U 0343	6272535	Aug 2001	Iwamura	

Change(s) applied to document, /M.H.E./ 10/29/2014

Change(s) applied to document, /M.H.E./ 10/30/2014

DATE:	09/06/2014	EXAMINER'S SIGNATURE:	/Izunna Okeke/

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail **Mail Stop ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

31518 7590 09/11/2014
NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission
I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

TITLE OF INVENTION: Data protection method and device

APPLN. TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	UNDISCOUNTED	$960	$0	$0	$960	12/11/2014

EXAMINER	ART UNIT	CLASS-SUBCLASS
OKEKE, IZUNNA	2432	713-193000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list
(1) The names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) The name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.
1 Neifeld IP Law, PC
2
3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent) : ☐ Individual ☐ Corporation or other private group entity ☐ Government

4a. The following fee(s) are submitted:
☒ Issue Fee
☐ Publication Fee (No small entity discount permitted)
☒ Advance Order - # of Copies three

4b. Payment of Fee(s): (**Please first reapply any previously paid issue fee shown above**)
☐ A check is enclosed.
☒ Payment by credit card. Form PTO-2038 is attached. XXXXXXXXXXXXXXXX efs authorization
☒ The Director is hereby authorized to charge the required fee(s), any deficiency, or credits any overpayment, to Deposit Account Number 50-2106 (enclose an extra copy of this form).

5. **Change in Entity Status** (from status indicated above)
☐ Applicant certifying micro entity status. See 37 CFR 1.29
NOTE: Absent a valid certification of Micro Entity Status (see forms PTO/SB/15A and 15B), issue fee payment in the micro entity amount will not be accepted at the risk of application abandonment.
☐ Applicant asserting small entity status. See 37 CFR 1.27
NOTE: If the application was previously under micro entity status, checking this box will be taken to be a notification of loss of entitlement to micro entity status.
☐ Applicant changing to regular undiscounted fee status.
NOTE: Checking this box will be taken to be a notification of loss of entitlement to small or micro entity status, as applicable.

NOTE: This form must be signed in accordance with 37 CFR 1.31 and 1.33. See 37 CFR 1.4 for signature requirements and certifications.

Authorized Signature /BruceMargulies/ Date 11/13/2014
Typed or printed name Bruce T. Margulies Registration No. 64175

PTOL-85 Part B (10-13) Approved for use through 10/31/2013. OMB 0651-0033 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Electronic Patent Application Fee Transmittal	
Application Number:	13556420
Filing Date:	24-Jul-2012
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Filer:	Bruce Talbot Margulies
Attorney Docket Number:	SCOT0014-6
Filed as Large Entity	

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl Issue Fee	1501	1	960	960
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Miscellaneous:				
Printed Copy of Patent - No Color	8001	3	3	9
		Total in USD ($)		**969**

Electronic Acknowledgement Receipt	
EFS ID:	20710231
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Bruce Talbot Margulies
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	17-NOV-2014
Filing Date:	24-JUL-2012
Time Stamp:	10:04:40
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	$969
RAM confirmation Number	8252
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)

1		FilingOfIssueFeeTransmittal_SCOT0014-6_11-13-2014c.pdf	196804 0baf7e82592a28ebe3dcea07bf3f84290b6d5c52	yes	2

	Multipart Description/PDF files in .zip description		
	Document Description	**Start**	**End**
	Transmittal Letter	1	1
	Issue Fee Payment (PTO-85B)	2	2

Warnings:					
Information:					
2	Fee Worksheet (SB06)	fee-info.pdf	32098 009711de3dfb8acb4be38c096f595a1c9efd8de5	no	2
Warnings:					
Information:					
		Total Files Size (in bytes):	228902		

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE

1. **37 CFR 1.25(b) SELECTED AUTHORIZATION TO CHARGE UNDERPAYMENT AND REFUND OVERPAYMENTS TO DEPOSIT ACCOUNT 50-2106.** The undersigned is an authorized signor for deposit account 50-2106 and authorizes charges for applications filed by Neifeld IP Law, PC, specified in 37 CFR 1.16 (national filing, search, exam fees); in 37 CFR 1.17 (processing, including petition fees); and 37 CFR 1.18 (post allowance, including issue fees) *except that*: the undersigned does not authorize charges for invention claims (specified in 1.16(h); (I); and (k)). The undersigned authorizes charges for a 35 USC 371 national stage entry of a PCT international application identified in 37 CFR 1.492(a)-(c) and (h)-(j), but not (d)-(g) (all fees other than invention claims fees).

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:** 969
2501 1.18(a)(1) Utility issue fee $960
Printed copy of patent w/o color, delivery by USPS, USPTO Box, or electronic means 3.00; 3 at 3.00 per or 9.00 total

3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
Part B - Fee(s) Transmittal (1 page)

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**

Disbursements: BankAcct#6, G/L 5010, check, amount, and entry date: 2180, 969, 11/13/2014
PClaw billing matter: [SCOT0001]
Service Fees: Amount/CreditAtty/entry date/Services:
$400/BTM/11/13/2014/Firm charge for paying gov. fees for issue
INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: BTM
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 11/13/2014 **SIGNATURE**: /BruceMargulies/
PRINTED NAME: BruceMargulies Reg. #64,175
Printed: November 13, 2014 (12:16pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\FilingOfIssueFeeTransmittal_SCOT0014-6_11-13-2014.wpd

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

31518 7590 10/17/2014

NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

EXAMINER	
OKEKE, IZUNNA	
ART UNIT	**PAPER NUMBER**
2432	

NOTIFICATION DATE	DELIVERY MODE
10/17/2014	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

general@neifeld.com
rneifeld@neifeld.com
rhahl@neifeld.com

Response to Rule 312 Communication	Application No.	Applicant(s)
	13/556,420	MOSKOWITZ, SCOTT A.
	Examiner	Art Unit
	IZUNNA OKEKE	2432

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address –

1. ☒ The amendment filed on *08 October 2014* under 37 CFR 1.312 has been considered, and has been:

a) ☐ entered.

b) ☒ entered as directed to matters of form not affecting the scope of the invention.

c) ☐ disapproved because the amendment was filed after the payment of the issue fee.
Any amendment filed after the date the issue fee is paid must be accompanied by a petition under 37 CFR 1.313(c)(1) and the required fee to withdraw the application from issue.

d) ☐ disapproved. See explanation below.

e) ☐ entered in part. See explanation below.

/IZUNNA OKEKE/
Primary Examiner, Art Unit 2432

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432

37 CFR 1.312 AMENDMENT AFTER ALLOWANCE

REMARKS

Copied below are the claims, further amended, in view of the examiner's amendment dated 9/11/2014. These amendments present editorial corrections to the allowed claims.

/RickNeifeld/
Richard Neifeld, Reg. No. 35,299
Attorney of Record
Date: 10/7/2014

OK TO ENTER: /I.O./
10/13/2014

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432

37 CFR 1.312 AMENDMENT AFTER ALLOWANCE

REMARKS

Copied below are the claims, further amended, in view of the examiner's amendment dated 9/11/2014. These amendments present editorial corrections to the allowed claims.

/RickNeifeld/
Richard Neifeld, Reg. No. 35,299
Attorney of Record
Date: 10/7/2014

IN THE CLAIMS

1-57. (Canceled)

58. (Currently Amended) A computing device for running application software, comprising:

memory designed to store data in non transitory form;

an operating system;

wherein said memory stores an application software;

wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;

wherein said application software is designed to call said memory scheduler code resource;

wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in said memory; and

wherein said memory scheduler code resource is designed to modify a stack frame in said memory.

59. (Currently Amended) The system of claim 75 wherein said first one of said plurality of executable code resources is a memory scheduler code resource, wherein, when said memory scheduler code resource is called, said memory scheduler code resource functions to shuffle said memory scheduler code resource randomly in said memory.

60. (Currently Amended) The device of claim 58 further comprising a program counter designed to store values and wherein said operating system is designed to enable application software to modify a value stored by said program counter.

61. (Previously Presented) The device of claim 58 further comprising a program counter designed to store values and wherein said memory scheduler code resource is designed to modify a value stored by said program counter.

62. (Previously Presented) The device of claim 58 wherein said memory scheduler code resource is designed to modify a calling address, and to copy itself to a memory location associated with said calling address.

63. (Currently Amended) The device of claim 58 ~~wherein said system comprises an operating system, and~~ wherein said operating system is designed to call said memory scheduler code resource.

64. (Currently Amended) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in said memory, wherein each one of said addresses is associated with one of the code resources.

65. (Currently Amended) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in said memory associated with said other code resources in said memory.

66. (Previously Presented) The device of claim 58 further comprising a processor for processing instructions defined by said application software.

67. (Currently Amended) A system for executing application software code, comprising:

a memory designed to store data in non transitory form, and storing executable code resources;

wherein said executable code resources comprise a memory scheduler and other executable code resources;

wherein said memory scheduler is designed to shuffle said other executable code resources in said memory; and

wherein said memory scheduler is designed to modify a stack frame in said memory.

68. (Previously Presented) The system of claim 67, wherein said memory scheduler is designed to shuffle said other executable code resources randomly in said memory.

69. (Currently Amended) The system of claim 67 wherein said memory scheduler is designed to shuffle said other executable code resources in said memory such that the shuffling results in a change in location in said memory of at least one of said other executable code resources.

70. (Previously Presented) The system of claim 67 further comprising a program counter designed to store values and wherein said memory scheduler is designed to modify values stored by said program counter.

71. (Currently Amended) The system of claim 67 wherein said memory scheduler maintains a list of addresses in said memory of at least one code resource.

72. (Currently Amended) The system of claim 67 wherein said memory scheduler maintains a list of addresses in said memory of said other executable code resources.

73. (Previously Presented) The system of claim 67 designed to call said memory scheduler.

74. (Previously Presented) The system of claim 67 wherein said application software code is designed to call said memory scheduler.

75. (Previously Presented) A system for executing an application on a computer system, comprising:

a computer system comprising a processor for processing instructions and memory

designed to store data in non transitory form;

a software application comprising a plurality of executable code resources loaded in said memory;

said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources;

wherein a first one of said plurality of executable code resources is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor; and

wherein said first one of said plurality of executable code resources is designed to modify a stack frame in said memory.

76. (Previously Presented) The system of claim 75 wherein said first one is also designed to relocate itself to a different address during execution of said software application.

77. (Currently Amended) The system of claim 75 wherein said system is designed to repeatedly relocate said at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application.

78. (Currently Amended) A system comprising:

a processor;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least one code resource into a different location in said memory after said program has been loaded into said memory; and

wherein one of said code resources is designed to modify a stack frame in said memory.

79. (Currently Amended) A system comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load an executable program comprising at least two code resources into said memory and to randomize the location of at least one of the at least two code resources in the said memory, using said processor; and

wherein one of the at least two code resources is designed to modify a stack frame in said

memory.

80. (Currently Amended) A system for executing a program comprising code, comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

a memory scheduler configured to randomize location of code defining a code resource, in said memory, using said processor; and

wherein said memory scheduler is configured to modify a stack frame in said memory.

81. (Currently Amended) The system of claim 80 wherein said memory scheduler is configured to repeatedly randomize the location of at least a portion of the code in said memory.

82. (Previously Presented) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that have sequential addresses in said memory.

83. (Previously Presented) The device of claim 82 wherein each one of said other code resources defines a function or procedure.

84. (Previously Presented) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that immediately follow one another in said memory.

85. (Previously Presented) The device of claim 84 wherein each one of said other code resources defines a function or procedure.

86. (Currently Amended) A system comprising:

a processor;

a memory coupled to said processor;

wherein said memory is designed to store data in non transitory form;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least a subset of said code resources into a different location in said memory after said program has been loaded into said memory; and

wherein one of said code resources is configured to modify a stack frame in said memory.

87. (Previously Presented) The system of claim 75 wherein each one of said plurality of executable code resources loaded in said memory comprises a sequence of instructions that have sequential addresses in said memory.

88. (Previously Presented) The system of claim 87 wherein each one of said plurality of executable code resources loaded in said memory defines a function or procedure.

89. (Previously Presented) The system of claim 88 wherein each one of said plurality

of executable code resources loaded in said memory is an essential code resource.

90. (Currently Amended) The system of claim 75 wherein each one of said plurality of executable code resources loaded in said memory comprises a sequence of instructions, and the sequence of instructions for one of said plurality of executable code resources loaded in said memory ~~have~~ has memory addresses that immediately follow one another.

91. (Previously Presented) The system of claim 90 wherein each one of said plurality of executable code resources loaded in said memory defines a function or procedure.

92. (Previously Presented) The system of claim 90 wherein said system comprises one of a set-top box; a cellular phone; a smart television; and a personal digital assistant (PDA).

93. (Previously Presented) A system for protecting a software application, said software application comprising a plurality of executable code resources, said system comprising:

a processor;

memory for storing data in non transitory form;

wherein said processor is coupled to said memory for transmitting data there between;

wherein, when executing said software application, said system is configured to store in said memory an original plurality of code resource addresses;

wherein, when executing said software application, said system is configured to store a plurality of executable code resources for executing said software application;

wherein each one of said original plurality of code resource addresses specifies an address in said memory of one of said plurality of executable code resources;

wherein, when executing said software application, said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses; and

wherein one of said plurality of executable code resources is configured to modify a stack frame in said memory.

94. (Previously Presented) The system of claim 93, wherein said system is configured to repeatedly (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

95. (Currently Amended) The system of claim 93, wherein each one of said plurality of executable code resources ~~are~~ is relocated to random addresses.

96. (Currently Amended) The system of claim 93, wherein each one said original plurality of code resource addresses specifies an entry point of an original location in said memory of each one of said plurality of executable code resources.

97. (Currently Amended) The system of claim 93, wherein each one said new plurality of code resource addresses specifies an entry point of a new location in said memory of each one of said plurality of executable code resources.

98. (Previously Presented) The system of claim 93 wherein each one of said plurality of executable code resources is stored in sequential locations in said memory.

99. (Previously Presented) The system of claim 98 wherein each one of said plurality of executable code resources executes a function or procedure when called.

100. (Previously Presented) The system of claim 93 wherein each one of said plurality of executable code resources executes a function or procedure

101. (Previously Presented) The system of claim 93 wherein said system is configured to relocate each one of said plurality of executable code resources in said memory in response to a call to an executable code resource of said plurality of executable code resources.

102. (Previously Presented) The system of claim 101 wherein said system is configured to update a reference to said executable code resource in said memory to reflect a new address in said memory of said executable code resource resulting from relocating said executable code resource.

103. (Previously Presented) The system of claim 102 wherein said executable code resource is a memory scheduler which is called periodically, or at random or pseudo random intervals.

104. (Currently Amended) A computer program product storing in non-transitory form instructions defining a software program, which instructions, when loaded into a computer system instruct the computer system to perform the following steps:

load said program into memory of said computer system;

wherein said software program, as loaded into memory, comprises a plurality of code resources;

shuffle locations in said memory of at least a subset of said plurality of code resources; and

wherein said shuffle comprises one of said plurality of code resources modifying a stack

frame in said memory.

105. (Previously Presented) The product of claim 104 wherein each one of said subset of said code resources comprises a sequence of instructions that have sequential addresses in said memory.

106. (Previously Presented) The product of claim 104 wherein each one of said subset of said code resources defines a function or procedure.

107. (Previously Presented) The product of claim 104 wherein said subset comprises at least two code resources.

ran
Date/Time: October 7, 2014 (5:44pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\37CFR312Amendment_AmendedClaims_SCOT0014-6_10-7-2014.wpd

Electronic Acknowledgement Receipt	
EFS ID:	20368520
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Bruce Talbot Margulies
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	08-OCT-2014
Filing Date:	24-JUL-2012
Time Stamp:	19:27:27
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		AmendmentAfterAllowance_SCOT0014-6_10-8-2014c.pdf	51351 9f3b9e116a77aec1bd2548bcfe54634bf1bf52bf	yes	10

	Multipart Description/PDF files in .zip description		
	Document Description	**Start**	**End**
	Transmittal Letter	1	1
	Amendment after Notice of Allowance (Rule 312)	2	2
	Claims	3	10
Warnings:			
Information:			
	Total Files Size (in bytes):	51351	

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NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

1. **37 CFR 1.25(b) SELECTED AUTHORIZATION TO CHARGE UNDERPAYMENT AND REFUND OVERPAYMENTS TO DEPOSIT ACCOUNT 50-2106.** The undersigned is an authorized signor for deposit account 50-2106 and authorizes charges for applications filed by Neifeld IP Law, PC, specified in 37 CFR 1.16 (national filing, search, exam fees); in 37 CFR 1.17 (processing, including petition fees); and 37 CFR 1.18 (post allowance, including issue fees) *except that*: the undersigned does not authorize charges for invention claims (specified in 1.16(h); (I); and (k)). The undersigned authorizes charges for a 35 USC 371 national stage entry of a PCT international application identified in 37 CFR 1.492(a)-(c) and (h)-(j), but not (d)-(g) (all fees other than invention claims fees).

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**0

3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
37 CFR 1.312 AMENDMENT AFTER ALLOWANCE (9 pages)

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: None.
Service Fees: None.

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: ran
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 10-8-2014 **SIGNATURE**: /BruceMargulies/
Printed: October 8, 2014 (12:04pm) BRUCE MARGULIES, REG. NO. 64175
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\AmendmentAfterAllowance_SCOT0014-6_10-8-2014.wpd

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
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NOTICE OF ALLOWANCE AND FEE(S) DUE

31518 7590 09/11/2014

NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

EXAMINER
OKEKE, IZUNNA

ART UNIT	PAPER NUMBER
2432	

DATE MAILED: 09/11/2014

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

TITLE OF INVENTION: Data protection method and device

APPLN. TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	UNDISCOUNTED	$960	$0	$0	$960	12/11/2014

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the ENTITY STATUS shown above. If the ENTITY STATUS is shown as SMALL or MICRO, verify whether entitlement to that entity status still applies.

If the ENTITY STATUS is the same as shown above, pay the TOTAL FEE(S) DUE shown above.

If the ENTITY STATUS is changed from that shown above, on PART B - FEE(S) TRANSMITTAL, complete section number 5 titled "Change in Entity Status (from status indicated above)".

For purposes of this notice, small entity fees are 1/2 the amount of undiscounted fees, and micro entity fees are 1/2 the amount of small entity fees.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail **Mail Stop ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

31518 7590 09/11/2014
NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission
I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

TITLE OF INVENTION: Data protection method and device

APPLN. TYPE	ENTITY STATUS	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	UNDISCOUNTED	$960	$0	$0	$960	12/11/2014

EXAMINER	ART UNIT	CLASS-SUBCLASS
OKEKE, IZUNNA	2432	713-193000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
- ❑ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
- ❑ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list
(1) The names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) The name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.
1 _____
2 _____
3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent) : ❑ Individual ❑ Corporation or other private group entity ❑ Government

4a. The following fee(s) are submitted:
- ❑ Issue Fee
- ❑ Publication Fee (No small entity discount permitted)
- ❑ Advance Order - # of Copies _____

4b. Payment of Fee(s): (**Please first reapply any previously paid issue fee shown above**)
- ❑ A check is enclosed.
- ❑ Payment by credit card. Form PTO-2038 is attached.
- ❑ The Director is hereby authorized to charge the required fee(s), any deficiency, or credits any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. **Change in Entity Status** (from status indicated above)
- ❑ Applicant certifying micro entity status. See 37 CFR 1.29 — NOTE: Absent a valid certification of Micro Entity Status (see forms PTO/SB/15A and 15B), issue fee payment in the micro entity amount will not be accepted at the risk of application abandonment.
- ❑ Applicant asserting small entity status. See 37 CFR 1.27 — NOTE: If the application was previously under micro entity status, checking this box will be taken to be a notification of loss of entitlement to micro entity status.
- ❑ Applicant changing to regular undiscounted fee status. — NOTE: Checking this box will be taken to be a notification of loss of entitlement to small or micro entity status, as applicable.

NOTE: This form must be signed in accordance with 37 CFR 1.31 and 1.33. See 37 CFR 1.4 for signature requirements and certifications.

Authorized Signature _____ Date _____

Typed or printed name _____ Registration No. _____

PTOL-85 Part B (10-13) Approved for use through 10/31/2013. OMB 0651-0033 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

31518 7590 09/11/2014

NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

EXAMINER	
OKEKE, IZUNNA	
ART UNIT	PAPER NUMBER
2432	

DATE MAILED: 09/11/2014

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(Applications filed on or after May 29, 2000)

The Office has discontinued providing a Patent Term Adjustment (PTA) calculation with the Notice of Allowance.

Section 1(h)(2) of the AIA Technical Corrections Act amended 35 U.S.C. 154(b)(3)(B)(i) to eliminate the requirement that the Office provide a patent term adjustment determination with the notice of allowance. See Revisions to Patent Term Adjustment, 78 Fed. Reg. 19416, 19417 (Apr. 1, 2013). Therefore, the Office is no longer providing an initial patent term adjustment determination with the notice of allowance. The Office will continue to provide a patent term adjustment determination with the Issue Notification Letter that is mailed to applicant approximately three weeks prior to the issue date of the patent, and will include the patent term adjustment on the patent. Any request for reconsideration of the patent term adjustment determination (or reinstatement of patent term adjustment) should follow the process outlined in 37 CFR 1.705.

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

OMB Clearance and PRA Burden Statement for PTOL-85 Part B

The Paperwork Reduction Act (PRA) of 1995 requires Federal agencies to obtain Office of Management and Budget approval before requesting most types of information from the public. When OMB approves an agency request to collect information from the public, OMB (i) provides a valid OMB Control Number and expiration date for the agency to display on the instrument that will be used to collect the information and (ii) requires the agency to inform the public about the OMB Control Number's legal significance in accordance with 5 CFR 1320.5(b).

The information collected by PTOL-85 Part B is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Notice of Allowability	Application No. 13/556,420	Applicant(s) MOSKOWITZ, SCOTT A.	
	Examiner IZUNNA OKEKE	Art Unit 2432	AIA (First Inventor to File) Status No

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *07/14/2014*.
   ☐ A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on_____.
2. ☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.
3. ☒ The allowed claim(s) is/are *58-107*. As a result of the allowed claim(s), you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to PPHfeedback@uspto.gov .
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

   Certified copies:

   a) ☐ All b) ☐ Some *c) ☐ None of the:
   1. ☐ Certified copies of the priority documents have been received.
   2. ☐ Certified copies of the priority documents have been received in Application No. _____ .
   3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

   * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
   ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

   Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☒ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 05/08/2014
3. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
4. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .
5. ☒ Examiner's Amendment/Comment
6. ☒ Examiner's Statement of Reasons for Allowance
7. ☐ Other _____.

/IZUNNA OKEKE/
Primary Examiner, Art Unit 2432

DETAILED ACTION

1. The present application is being examined under the pre-AIA first to invent provisions.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Richard Neifeld on 09/02/2014.

The application has been amended as follows: Claims 58, 62, 67, 75, 78-80, 86, 93 and 104.

58. (Currently Amended) A computing device for running application software, comprising:

memory designed to store data in non transitory form;

an operating system;

wherein said memory stores an application software;

wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;

wherein said application software is designed to call said memory scheduler code resource; ~~and~~

wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory; and

wherein said memory scheduler code resource is designed to modify a stack frame in said memory.

62. (Currently Amended) The device of claim 58 wherein said memory scheduler code resource is designed ~~to modify a stack frame,~~ to modify a calling address, and to copy itself to a memory location associated with said calling address.

67. (Currently Amended) A system for executing application software code, comprising:

a memory designed to store data in non transitory form, and storing executable code resources;

wherein said executable code resources comprise a memory scheduler and other executable code resources; ~~and~~

wherein said memory scheduler is designed to shuffle said other executable code resources in memory; and

wherein said memory scheduler is designed to modify a stack frame in said memory.

75. (Currently Amended) A system for executing an application on a computer system, comprising:

a computer system comprising a processor for processing instructions and memory designed to store data in non transitory form;

a software application comprising a plurality of executable code resources loaded in said memory;

said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources; ~~and~~

wherein a first one of said plurality of executable code resources is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor; and

wherein said first one of said plurality of executable code resources is designed to modify a stack frame in said memory.

78. (Currently Amended) A system comprising:

a processor;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory; ~~and~~

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least one code resource into a different location in said memory after said program has been loaded into memory; and

wherein one of said code resources is designed to modify a stack frame in said memory.

79. (Currently Amended) A system comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory; ~~and~~

wherein said system is configured to load an executable program comprising at least two code resources into memory and to randomize the location of at least one of the at least two code resources in the memory, using said processor; and

wherein one of the at least two code resources is designed to modify a stack frame in said memory.

80. (Currently Amended) A system for executing a program comprising code, comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory; ~~and~~

a memory scheduler configured to randomize location of code defining a code resource, in memory, using said processor; and

wherein said memory scheduler is configured to modify a stack frame in said memory.

86. (Currently Amended) A system comprising:

a processor;

a memory coupled to said processor;

wherein said memory is designed to store data in non transitory form; ~~and~~

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least a subset of said code resources into a different location in said memory after said program has been loaded into memory; and

wherein one of said code resources is configured to modify a stack frame in said memory.

93. (Currently Amended) A system for protecting a software application, said software application comprising a plurality of executable code resources, said system comprising:

a processor;

memory for storing data in non transitory form;

wherein said processor is coupled to said memory for transmitting data there between;

wherein, when executing said software application, said system is configured to store in said memory an original plurality of code resource addresses;

wherein, when executing said software application, said system is configured to store a plurality of executable code resources for executing said software application;

wherein each one of said original plurality of code resource addresses specifies an address in said memory of one of said plurality of executable code resources; ~~and~~

wherein, when executing said software application, said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses; and

wherein one of said plurality of executable code resources is configured to modify a stack frame in said memory.

104. (Currently Amended) A computer program product storing in non-transitory form instructions defining a software program, which instructions, when loaded into a computer system instruct the computer system to perform the following steps:

load said program into memory of said computer system;

wherein said program, as loaded into memory, comprises a plurality of code resources; ~~and~~

shuffle locations in memory of at least a subset of said plurality of code resources; and

wherein said shuffle comprises one of said plurality of code resources modifying a stack frame in said memory.

Allowable Subject Matter

3. Claims 58-107 are allowed.

4. The following is an examiner's statement of reasons for allowance:

The primary reason for allowance of the claims is the limitation of the scheduler modifying the stack frame. After shuffling the code resources randomly in memory, the scheduler randomly relocates itself by modifying the stack frame so that it could jump into the

new copy of the scheduler but return to the correct calling frame. This adds to the complexity of attempting to analyze snapshots of memory.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to IZUNNA OKEKE whose telephone number is (571)270-3854. The examiner can normally be reached on Monday - Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on (571) 272-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/IZUNNA OKEKE/
Primary Examiner, Art Unit 2432

Search Notes	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	Art Unit
	IZUNNA OKEKE	2432

CPC- SEARCHED		
Symbol	Date	Examiner
G06F21/10	9/5/2014	IO
H04L9/3247	9/5/2014	IO
H04L9/3236	9/5/2014	IO
H04L9/065	9/5/2014	IO
G06F21/335	9/5/2014	IO
G06F21/125	9/5/2014	IO
G06F21/16	9/5/2014	IO
G06F2221/2107	9/5/2014	IO
G06F2211/007	9/5/2014	IO
G06F2221/0737	9/5/2014	IO

CPC COMBINATION SETS - SEARCHED		
Symbol	Date	Examiner

US CLASSIFICATION SEARCHED			
Class	Subclass	Date	Examiner

SEARCH NOTES		
Search Notes	Date	Examiner
UpdatedText Search (See Search History)	9/5/2014	IO
Updated Keyword + Classification Search (See Search History)	9/5/2014	IO
Updated Search (713/1, 713/151, 713/165, 713/166, 713/167, 713/193, 713/194, 713/152, 713/187, 713/189, 713/188, 713/190, 726/26, 380/201, 711/1, 711/100) (See Search History)	9/5/2014	IO
Updated NPL Database Search (Google Scolar database search : Search terms : "rearranging code in runtime memory during execution to prevent memory analysis")	9/3/2014	IO
Review of parent case 08,587,943 and related case 11,895,388	9/3/2014	IO
Inventor Name Search for Double Patenting Issues	9/5/2014	IO

INTERFERENCE SEARCH			
US Class/ CPC Symbol	**US Subclass / CPC Group**	**Date**	**Examiner**
	PG-PUB Text Search (See Interference Search History)	9/12/2014	IO
	UPAD Text Search (See Interference Search History)	9/5/2014	IO

Index of Claims	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	Art Unit
	IZUNNA OKEKE	2432

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

☐ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

CLAIM		DATE								
Final	Original	03/21/2013	03/13/2014	09/06/2014						
1	58	✓	✓	=						
2	59	✓	✓	=						
3	60	✓	✓	=						
4	61	✓	✓	=						
5	62	✓	✓	=						
6	63	✓	✓	=						
7	64	✓	✓	=						
8	65	✓	✓	=						
9	66	✓	✓	=						
10	67	✓	✓	=						
11	68	✓	✓	=						
12	69	✓	✓	=						
13	70	✓	✓	=						
14	71	✓	✓	=						
15	72	✓	✓	=						
16	73	✓	✓	=						
17	74	✓	✓	=						
18	75	✓	✓	=						
19	76	✓	✓	=						
20	77	✓	✓	=						
21	78	✓	✓	=						
22	79	✓	✓	=						
23	80	✓	✓	=						
24	81	✓	✓	=						
25	82		✓	=						
26	83		✓	=						
27	84		✓	=						
28	85		✓	=						
29	86		✓	=						
30	87		✓	=						
31	88		✓	=						
32	89		✓	=						
33	90		✓	=						
34	91		✓	=						
35	92		✓	=						
36	93		✓	=						

Index of Claims	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	Art Unit
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☐ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

CLAIM		DATE								
Final	Original	03/21/2013	03/13/2014	09/06/2014						
37	94		✓	=						
38	95		✓	=						
39	96		✓	=						
40	97		✓	=						
41	98		✓	=						
42	99		✓	=						
43	100		✓	=						
44	101		✓	=						
45	102		✓	=						
46	103		✓	=						
47	104		✓	=						
48	105		✓	=						
49	106		✓	=						
50	107		✓	=						

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13/556,420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: MOSKOWITZ, Scott
Title: DATA PROTECTION METHOD AND DEVICE
Examiner/ArtUnit: OKEKE, Izunna/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data:
This application is a Continuation of 11/895,388 filed 08-24-2007
11/895,388 is a Division of 10/602,777 filed 06-25-2003 Patented 7,664,263
10/602,777 is a continuation of 09/046,627 filed 03-24-1998 Patented 6,598,162

37 CFR 1.97 INFORMATION DISCLOSURE STATEMENT

This application is:
____ within 3 months of the US or 371 national stage filing date;
____ before first action on the merits (no fee required);
XXX after first action on the merits and before final action (1.17(P) fee required);
____ after final action;
____ after notice of allowance and before payment of the issue fee; or
____ after payment of the issue fee.

XXX The applicant is paying herewith the fee for obtaining consideration of an IDS filed after a first action on the merits.

IDENTIFICATION OF REFERENCES CITED IN APPLICATIONS TO WHICH 13/556,420 CLAIMS CONTINUING STATUS

REGARDING CITED REFERENCES

This IDS is an attempt to compile all references previously cited in Scott Moskowitz's cases. Upon compilation, some of the reference citations were vague, and some were to filed patent applications instead of published documents. This IDS attempts to account for each item to provide all citations to the examiner.

References previously cited and considered by the examiner in application13/556,420 (SCOT0014-6) are identified by placement of an "X" in the far right column.

CITED US PATENTS AND US PATENT APPLICATION PUBLICATIONS

Most pending Scott Moskowitz cases claim 35 USC 120 priority to prior cases containing a large number of cited US patents and published US applications. The citations list herein should incorporate all of those documents and may incorporate any additional documents found in other patent applications in patent families not linked by 35 USC 120 to this application. Since no US patent or US published applications need to be filed in order for the examiner to

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

consider citations thereto; the applicant may attempt to correlate the US patents and publications cited herein to those already of record due to citations in applications to which this application claims priority, if the examiner so requests.

FOREIGN PATENT REFERENCES

The IDS cites foreign patent references identified herewith as F001- F029 .

The table below identifies F references cited in this application or an application to which this application claims 35 USC 120 priority.

DOCKET NO	APPLICATION NUMBER	CITED F REFERENCES
SCOT0014-6	13/556,420	F01-F029

Accordingly, the following F references are not yet of record and are submitted herewith: NONE

NON PATENT LITERATURE REFERENCES

The IDS cites foreign patent references identified herewith as L001- L231 .

The table below identifies L references cited in this application or in an application to which this application claims 35 USC 120 priority.

DOCKET NO	APPLICATION NUMBER	CITED L REFERENCES
SCOT0014-6	13/556,420	L1-L212 & L218
	L reference citations of patent applications as filed for which a subsequent publication of the application is identified and cited herein.	L1-35, L101; L104-L105; L185-L202
	L reference citation numbers that have no associated citation; original citation was a duplicate of some other citation.	L98, L100, L102-103, L106-L114, L116-L154

References previously cited, applications for which a subsequent publication is cited, and reference numbers having no associated reference: L01-L212 & L218

Accordingly, the following L references are not yet of record and are submitted herewith: L213-L231

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

MASTER LIST OF RELATED CASES IN WHICH THE SAME INFORMATION MAY BE CITED

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0010-4	11/599,838	11/15/2006	10/15/2010 JRE
SCOT0010-5	11/899,662	9/7/2007	10/15/2010 JRE
SCOT0010-6	10/369,344	2/18/2003	08/1/2011 JRE
SCOT0010-7	11/482,654	7/7/2006	08/1/2011 JRE
SCOT0010-8	12/215,812	6/30/2008	10/15/2010 JRE
SCOT0010-10	12/901,568	10/10/2010	11/4/2010 JRE
SCOT0010-11	11/497,822	8/2/2006	08/1/2011 JRE
SCOT0010-12	12/217,834	7/9/2008	11/8/2010 JRE
SCOT0010-13	11/897,790	8/31/2007	08/1/2011 JRE
SCOT0010-14	12/462,799	8/10/2009	12/15/2010 JRE
SCOT0010-16	11/899,661	9/7/2007	08/1/2011 JRE
SCOT0010-17	12/590,681	11/19/2009	12/15/2010 JRE
SCOT0010-18	11/897,791	8/31/2007	08/1/2011 JRE
SCOT0010-19	12/590,553	11/10/2009	08/1/2011 JRE
SCOT0010-20	12/592,331	11/23/2009	08/1/2011 JRE
SCOT0010-21	11/599,964	11/15/2006	08/1/2011 JRE
SCOT0010-22	13/212,264	8/18/2011	1/11/2012 JRE
SCOT0011-1	08/674,726	7/2/1996	08/1/2011 JRE
SCOT0011-2	09/545,589	4/7/2000	1/11/2012 JRE
SCOT0011-3	11/244,213	10/5/2005	1/11/2012 JRE
SCOT0011-4	12/009,914	1/23/2008	10/15/2010 JRE
SCOT0011-5	12/005,230	12/26/2007	10/15/2010 JRE
SCOT0011-6	12/803,168	6/21/2010	10/15/2010 JRE
SCOT0011-7	11/649,026	1/3/2007	08/1/2011 JRE

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0011-8	12/803,194	06/21/2010	10/15/2010 JRE
SCOT0011-9	12/892,900	9/28/2010	11/8/2010 JRE
SCOT0012-1	08/489,172	6/7/1995	08/1/2011 JRE
SCOT0012-2	08/775,216	12/31/1996	01/11/2011 JRE
SCOT0012-3	08/999,766	7/23/1997	10/15/2010 JRE
SCOT0012-4	11/894,476	8/21/2007	10/15/2010 JRE
SCOT0012-5	11/050,779	2/7/2005	10/15/2010 JRE
SCOT0012-6	12/802,519	6/8/2010	11/4/2010 JRE
SCOT0012-7	12/383,916	3/30/2009	10/15/2010 JRE
SCOT0012-8	11/894,443	8/21/2007	10/15/2010 JRE
SCOT0012-9	12/913,751	10/27/2010	11/8/2010 JRE
SCOT0012-10	13/803,889	3/14/2013	4/16/2013 JRE
SCOT0013-1	08/587,943	1/17/1996	1/11/2012 JRE
SCOT0014-1	09/046,627	3/24/1998	1/11/2012 JRE
SCOT0014-2	10/602,777	6/25/2003	08/1/2011 JRE
SCOT0014-3 redocketed as SCOT0020-2	11/512,701	8/29/2006	10/15/2010 JRE
SCOT0014-4	11/895,388	8/24/2007	10/15/2010 JRE
SCOT0014-5	12/655,002	12/22/2009	08/1/2011 JRE
SCOT0014-6	13/556,420	7/24/2012	9/17/2012 JRE
SCOT0014-7	13/794,584	3/12/2013	4/16/2013 JRE
SCOT0015-1	09/731,039	12/7/2000	1/11/2012 JRE
SCOT0015-2	11/647/861	12/29/2006	1/11/2012 JRE
SCOT0015-3	12/383,879	3/30/2009	10/15/2010 JRE
SCOT0015-4	12/886,732	9/21/2010	10/15/2010 JRE
SCOT0015-5	13/572,641	8/11/2012	10/11/2012 JRE
SCOT0015-6	13/794,742	3/12/2013	4/16/2013 JRE
SCOT0016-1	10/049,101	7/23/2002	1/11/2012 JRE
SCOT0016-2	12/287,443	10/9/2008	10/15/2010 JRE
SCOT0016-3	13/413,691	3/7/2012	8/30/2012 JRE

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0016-4	13/796,538	3/12/2013	4/16/2013 JRE
SCOT0017-1	09.657,181	9/7/2000	1/11/2012 JRE
SCOT0017-2	12/005,229	12/26/2007	1/11/2012 JRE
SCOT0017-3	12/655,357	12/22/2009	10/15/2010 JRE
SCOT0017-4	13/035,964	2/26/2011	08/1/2011 JRE
SCOT0017-5	13/487,119	6/1/2012	4/16/2013 JRE
SCOT0017-6	13/802,384	3/13/2013	4/16/2013 JRE
SCOT0018-1	10/417/231	4/17/2003	01/11/2011 JRE
SCOT0018-2	11/900,065	9/10/2007	10/15/2010 JRE
SCOT0018-3	11/900,066	9/10/2007	1/11/2012 JRE
SCOT0018-4	12/383,289	3/23/2009	08/1/2011 JRE
SCOT0018-5	13/273,930	10/14/2011	1/11/2012 JRE
SCOT0018-6	13/551,097	7/17/2012	4/16/2013 JRE
SCOT0018-7	13/488,357	6/4/2012	9/9/2012 JRE
SCOT0018-8	13/488,395	6/4/2012	9/9/2012 JRE
SCOT0019-1	09/053,628	4/2/1998	1/11/2012 JRE
SCOT0019-2	09/644,098	8/23/2000	1/11/2012 JRE
SCOT0019-3	11/358,874	2/21/2006	1/11/2012 JRE
SCOT0019-4	12/799,894	5/4/2010	12/13/2010 JRE
SCOT0020-1	09/731,040	12/7/2000	1/11/2012 JRE
SCOT0020-2	11/512,701	8/29/2006	08/1/2011 JRE
SCOT0020-3	13/826,858	3/14/2013	4/16/2013 JRE
SCOT0020-4	13/797,744	3/12/2013	4/16/2013 JRE
SCOT0022-1	09/594,719	6/16/2000	4/16/2013 JRE
SCOT0022-2	11/519,467	9/12/2006	4/16/2013 JRE
SCOT0022-3	12/655,036	12/22/2009	08/1/2011 JRE
SCOT0022-4	13/423,650	3/19/2012	7/26/2012 JRE
SCOT0022-5	13/802,471	3/13/2013	4/16/2013 JRE
SCOT0023-1	08/772,222	12/20/1996	4/16/2013 JRE
SCOT0023-2	09/456,319	12/8/1999	4/16/2013 JRE

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0023-3	11/826,234	12/30/2004	4/16/2013 JRE
SCOT0023-4	11/592,879	11/2/2006	4/16/2013 JRE
SCOT0023-5	12/798,959	4/14/2010	08/1/2011 JRE
SCOT0024-2	11/518,806	9/11/2006	08/1/2011 JRE
SCOT0024-3	13/429,396	3/25/2012	7/26/2012 JRE
SCOT0025-1	61/794,141	3/15/2013	4/16/2013JRE

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

AS OF 1/12/2012, THE FOLLOWING TABLE COLLATES ADDITIONAL REFERENCES CITED IN ANY SCOT (SCOTT MOSKOWITZ) CASE

Date of Document Citing Reference	Atty Ref	Application Number	ID of paper in which references were cited	References checked to see if they existed in the master IDS (initials of person checking)	Reference Identifiers of New references in document, now added to master IDS
Sept 14, 2010	SCOT0012-7	12/383,916	892	JRE	U#299
11/17/2010	ALL	N/A	Review of draft master IDS, correction to cite publications in lieu of filed applications, per RAN instructions.	JRE	P76-P82
12/9/2010	SCOT0018-2	11/900,065	892	JRE	U303 & P83
11/30/2010	SCOT0019-4	12/799,894	892	JRE	U304
11/21/2011	SCOT0016-2	12/287,443	892	JRE	U305, U306 & U307
1/12/2012	SCOT0011-8	12/803,194	892	JRE	U308
1/12/2012	SCOT0014-5	12/655,002	892	JRE	U309
1/12/2012	SCOT0017-4	13/035,964	892	JRE	U310-U316
1/12/2012	SCOT0018-2	11/900,065	892	JRE	P84-P85
3/7/2012	SCOT0018-2	11/900,065	892	JRE	P86 -P87 & U317
8/30/2012	SCOT0016-3	13/413,691	892	JRE	U318 & U319

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

9/17/2012	SCOT0014-6	13/556,420	Per RAN created CTS reminder	JRE	L212
11/26/2012	SCOT0017-4	13/035.964	Per RAN inst rec'vd 11/15/2012	JRE	U320 & L213-L217
4/16/2013	SCOT0017-4	13/035,964	Review of Patented case	JRE	U322-U326 & P88-P90
6/13/2013	SCOT0018-7	13/488,357	Per instructions received from RAN	JRE	U329-332 L218-L223
6/28/2013	SCOT0014-6	13/556,420	Per instructions received from RAN	JRE	U0333
1/21/2014			Per Instructions received from RAN on 1/7/2014	JRE	L229
2/6/2014	SCOT0017-6	13/802,384	Per instructions received from RAN on 1/30/2014	JRE	U335

NOTE: MPEP 609.02 Information Disclosure Statements in Continued Examinations or Continuing Applications [R-5] states in part that:

"2. Continuation Applications , Divisional Applications, or Continuation-In-Part Applications Filed Under 37 CFR 1.53(b)
The examiner will consider information which has been considered by the Office in a parent application when examining: (A) a continuation application filed under 37 CFR 1.53(b), (B) a divisional application filed under 37 CFR 1.53(b), or (C) a continuation-in-part application filed under 37 CFR 1.53(b). A listing of the information need not be resubmitted in the continuing application unless the applicant desires the information to be printed on the patent"

See http://mpep.uspto.gov/RDMS/detail/manual/MPEP/e8r9/d0e18.xml#/manual/MPEP/e8r9/d0e53250.xml (8/2012)

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

Accordingly, we are submitting only references not cited in the parent application.

Please consider the references cited herein.

Date signed:5/7/2014 Signature: /BruceMargulies/
Printed Name: Bruce T. Margulies, Reg No: 64175
Attorney of Record

JRE
Printed: May 8, 2014 (1:26pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\IDS_SCOT0014-6_5-5-2014.wpd

/Izunna Okeke/ 09/06/2014

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

Printed: May 8, 2014 (1:29pm)
Path: Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ReferenceCitationList_SCOT0014-6_05-01-2014.wpd
37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

LISTING OF UNITED STATES PATENTS - U series

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 01	3947825	March 1976	Cassada	X
	U 02	3984624	October 1976	Waggener	X
	U 03	3986624	October 1976	Cates, Jr. et al.	X
	U 04	4038596	July 1977	Lee	X
	U 05	4200770	April 1980	Hellman et al.	X
	U 06	4218582	August 1980	Hellman et al.	X
	U 07	4339134	July 1982	Macheel	X
	U 08	4390898	June 1983	Bond et al.	X
	U 09	4405829	September 1983	Rivest et al.	X
	U 010	4424414	January 1984	Hellman et al.	X
	U 011	4528588	July 1985	Lofberg	X
	U 012	4672605	June 1987	Hustig et al.	X
	U 013	4748668	May 1988	Shamir et al.	X
	U 014	4789928	December 1988	Fujisaki	X
	U 015	4827508	May 1989	Shear	X
	U 016	4876617	October 1989	Best et al.	X
	U 017	4896275	January 1990	Jackson	X
	U 018	4908873	March 1990	Philibert et al.	X
	U 019	4939515	July 1990	Adelson	X
	U 020	4969204	November 1990	Melnychuk et al.	X
	U 021	4972471	November 1990	Gross et al.	X
	U 022	4977594	December 1990	Shear	X

DATE: 09/06/2014	EXAMINER'S SIGNATURE: /Izunna Okeke/

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

Printed: May 8, 2014 (1:29pm)
Path: Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ReferenceCitationList_SCOT0014-6_05-01-2014.wpd
37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 023	4979210	December 1990	Nagata et al.	X
	U 024	4980782	December 1990	Ginkel	X
	U 025	5050213	September 1991	Shear	X
	U 026	5073925	December 1991	Nagata et al.	X
	U 027	5077665	December 1991	Silverman et al.	X
	U 028	5113437	May 1992	Best et al.	X
	U 029	5136581	August 1992	Muehrcke	X
	U 030	5136646	August 1992	Haber et al.	X
	U 031	5136647	August 1992	Haber et al.	X
	U 032	5142576	August 1992	Nadan	X
	U 033	5161210	November 1992	Druyvesteyn et al.	X
	U 034	5210820	May 1993	Kenyon	X
	U 035	5243423	September 1993	DeJean et al.	X
	U 036	5243515	September 1993	Lee	X
	U 037	5287407	February 1994	Holmes	X
	U 038	5319735	June 1994	Preuss et al.	X
	U 039	5341429	August 1994	Stringer et al.	X
	U 040	5341477	August 1994	Pitkin et al.	X
	U 041	5363448	November 1994	Koopman et al.	X
	U 042	5365586	November 1994	Indeck et al.	X
	U 043	5369707	November 1994	Follendore, III	X
	U 044	5379345	January 1995	Greenberg	X
	U 045	5394324	February 1995	Clearwater	X

DATE:	09/06/2014	EXAMINER'S SIGNATURE:	/Izunna Okeke/

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

Printed: May 8, 2014 (1:29pm)
Path: Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ReferenceCitationList_SCOT0014-6_05-01-2014.wpd
37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 046	5398285	March 1995	Borgelt et al.	X
	U 047	5406627	April 1995	Thompson et al.	X
	U 048	5408505	April 1995	Indeck et al.	X
	U 049	5410598	April 1995	Shear	X
	U 050	5412718	May 1995	Narasimhalv et al.	X
	U 051	5418713	May 1995	Allen	X
	U 052	5428606	June 1995	Moskowitz	X
	U 053	5450490	September 1995	Jensen et al.	X
	U 054	5469536	November 1995	Blank	X
	U 055	5471533	November 1995	Wang et al.	X
	U 056	5478990	December 1995	Montanari et al.	X
	U 057	5479210	December 1995	Cawley et al.	X
	U 058	5487168	January 1996	Geiner et al.	X
	U 059	5493677	February 1996	Balogh et al.	X
	U 060	5497419	March 1996	Hill	X
	U 061	5506795	April 1996	Yamakawa	X
	U 062	5513126	April 1996	Harkins et al.	X
	U 063	5513261	April 1996	Maher	X
	U 064	5530739	June 1996	Okada	X
	U 065	5530751	June 1996	Morris	X
	U 066	5530759	June 1996	Braudaway et al.	X
	U 067	5539735	July 1996	Moskowitz	X
	U 068	5548579	August 1996	Lebrun et al.	X

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	U 069	5568570	October 1996	Rabbani	X
	U 070	5579124	November 1996	Aijala et al.	X
	U 071	5581703	December 1996	Baugher et al.	X
	U 072	5583488	December 1996	Sala et al.	X
	U 073	5598470	January 1997	Cooper et al.	X
	U 074	5606609	February 1997	Houser et al.	X
	U 075	5613004	March 1997	Cooperman et al.	X
	U 076	5617119	April 1997	Briggs et al.	X
	U 077	5625690	April 1997	Michel et al.	X
	U 078	5629980	May 1997	Stefik et al.	X
	U 079	5633932	May 1997	Davis et al.	X
	U 080	5634040	May 1997	Her et al.	X
	U 081	5636276	June 1997	Brugger	X
	U 082	5636292	June 1997	Rhoads	X
	U 083	5640569	June 1997	Miller et al.	X
	U 084	5646997	July 1997	Barton	X
	U 085	5657461	August 1997	Harkins et al.	X
	U 086	5659726	August 1997	Sandford, II et al.	X
	U 087	5664018	September 1997	Leighton	X
	U 088	5673316	September 1997	Auerbach et al.	X
	U 089	5677952	October 1997	Blakely et al.	X
	U 090	5680462	October 1997	Miller et al.	X
	U 091	5687236	November 1997	Moskowitz et al.	X

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	U 092	5689587	November 1997	Bender et al.	X
	U 093	5696828	December 1997	Koopman, Jr.	X
	U 094	5719937	February 1998	Warren et al.	X
	U 095	5721788	February 1998	Powell et al.	X
	U 096	5734752	March 1998	Knox	X
	U 097	5737416	April 1998	Cooper et al.	X
	U 098	5737733	April 1998	Eller	X
	U 099	5740244	April 1998	Indeck et al.	X
	U 0100	5745569	April 1998	Moskowitz et al.	X
	U 0101	5748783	May 1998	Rhoads	X
	U 0102	5751811	May 1998	Magnotti et al.	X
	U 0103	5754697	May 1998	Fu et al.	X
	U 0104	5757923	May 1998	Koopman, Jr.	X
	U 0105	5765152	June 1998	Erickson	X
	U 0106	5768396	June 1998	Sone	X
	U 0107	5774452	June 1998	Wolosewicz	X
	U 0108	5790677	August 1998	Fox et al.	X
	U 0109	5799083	August 1998	Brothers et al.	X
	U 0110	5809139	September 1998	Grirod et al.	X
	U 0111	5809160	September 1998	Powell et al.	X
	U 0112	5822432	October 1998	Moskowitz et al.	X
	U 0113	5828325	October 1998	Wolosewicz et al.	X

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	U 0114	5832119	November 1998	Rhoads	X
	U 0115	5848155	December 1998	Cox	X
	U 0116	5850481	December 1998	Rhoads	X
	U 0117	5859920	January 1999	Daly et al.	X
	U 0118	5860099	January 1999	Milios et al.	X
	U 0119	5862260	January 1999	Rhoads	X
	U 0120	5870474	February 1999	Wasilewski et al.	X
	U 0121	5884033	March 1999	Duvall et al.	X
	U 0122	5889868	March 1999	Moskowitz et al.	X
	U 0123	5893067	April 1999	Bender et al.	X
	U 0124	5894521	April 1999	Conley	X
	U 0125	5903721	May 1999	Sixtus	X
	U 0126	5905800	May 1999	Moskowitz et al.	X
	U 0127	5905975	May 1999	Ausubel	X
	U 0128	5912972	June 1999	Barton	X
	U 0129	5915027	June 1999	Cox et al.	X
	U 0130	5917915	June 1999	Hirose	X
	U 0131	5918223	June 1999	Blum	X
	U 0132	5920900	July 1999	Poole et al.	X
	U 0133	5923763	July 1999	Walker et al.	X
	U 0134	5930369	July 1999	Cox et al.	X
	U 0135	5930377	July 1999	Powell et al	. X
	U 0136	5940134	August 1999	Wirtz	X

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	U 0137	5943422	August 1999	Van Wie et al.	X
	U 0138	5963909	October 1999	Warren et al.	X
	U 0139	5973731	October 1999	Schwab	X
	U 0140	5974141	October 1999	Saito	X
	U 0141	5991426	November 1999	Cox et al.	X
	U 0142	5999217	December 1999	Berners-Lee	X
	U 0143	6009176	December 1999	Gennaro et al.	X
	U 0144	6029126	February 2000	Malvar	X
	U 0145	6041316	March 2000	Allen	X
	U 0146	6044471	March 2000	Colvin	X
	U 0147	6049838	April 2000	Miller et al.	X
	U 0148	6051029	April 2000	Paterson et al.	X
	U 0149	6061793	May 2000	Tewfik et al.	X
	U 0150	6069914	May 2000	Cox	X
	U 0151	6078664	June 2000	Moskowitz et al.	X
	U 0152	6081251	June 2000	Sakai et al.	X
	U 0153	6081587	June 2000	Reyes et al.	X
	U 0154	6088455	July 2000	Logan et al.	X
	U 0155	6131162	October 2000	Yoshiura et al.	X
	U 0156	6141753	October 2000	Zhao et al.	X
	U 0157	6141754	October 2000	Choy	X
	U 0158	6154571	November 2000	Cox et al.	X
	U 0159	6192138	February 2001	Yamadaji	X

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	U 0160	6199058	March 2001	Wong et al.	X
	U 0161	6205249	March 2001	Moskowitz	X
	U 0162	6208745	March 2001	Florenio et al.	X
	U 0163	6230268	May 2001	Miwa et al.	X
	U 0164	6233347	May 2001	Chen et al.	X
	U 0165	6233684	May 2001	Stefik et al.	X
	U 0166	6240121	May 2001	Senoh	X
	U 0167	6263313	July 2001	Milstead et al.	X
	U 0168	6272634	August 2001	Tewfik et al.	X
	U 0169	6275988	August 2001	Nagashima et al.	X
	U 0170	6278780	August 2001	Shimada	X
	U 0171	6278791	August 2001	Honsinger et al.	X
	U 0172	6282300	August 2001	Bloom et al.	X
	U 0173	6282650	August 2001	Davis	X
	U 0174	6285775	September 2001	Wu et al.	X
	U 0175	6301663	October 2001	Kato et al.	X
	U 0176	6310962	October 2001	Chung et al.	X
	U 0177	6330335	December 2001	Rhoads	X
	U 0178	6330672	December 2001	Shur	X
	U 0179	6345100	February 2002	Levine	X
	U 0180	6351765	February 2002	Pietropaolo et al.	X
	U 0181	6363483	March 2002	Keshav	X
	U 0182	6373892	April 2002	Ichien et al.	X

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	U 0183	6373960	April 2002	Conover et al.	X
	U 0184	6374036	April 2002	Ryan et al.	X
	U 0185	6377625	April 2002	Kim	X
	U 0186	6381618	April 2002	Jones et al.	X
	U 0187	6381747	April 2002	Wonfor et al.	X
	U 0188	6385329	May 2002	Sharma et al.	X
	U 0189	6389538	May 2002	Gruse et al.	X
	U 0190	6405203	June 2002	Collart	X
	U 0191	6415041	July 2002	Oami et al.	X
	U 0192	6425081	July 2002	Iwamura	X
	U 0193	6430301	August 2002	Petrovic	X
	U 0194	6430302	August 2002	Rhoads	X
	U 0195	6442283	August 2002	Tewfik et al.	X
	U 0196	6446211	September 2002	Colvin	X
	U 0197	6453252	September 2002	Laroche	X
	U 0198	6457058	September 2002	Ullum et al.	X
	U 0199	6463468	October 2002	Buch et al.	X
	U 0200	6484264	November 2002	Colvin	X
	U 0201	6493457	December 2002	Quackenbush	X
	U 0202	6502195	December 2002	Colvin	X
	U 0203	6522767	February 2003	Moskowitz et al.	X
	U 0204	6522769	February 2003	Rhoads et al.	X
	U 0205	6523113	February 2003	Wehrenberg	X

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	U 0206	6530021	March 2003	Epstein et al.	X
	U 0207	6532284	March 2003	Walker et al.	X
	U 0208	6539475	March 2003	Cox et al.	X
	U 0209	6557103	April 2003	Boncelet, Jr. et al.	X
	U 0210	6584125	June 2003	Katto	X
	U 0211	6587837	July 2003	Spagna et al.	X
	U 0212	6598162	July 2003	Moskowitz	X
	U 0213	6606393	August 2003	Xie et al.	X
	U 0214	6647424	November 2003	Pearson et al.	X
	U 0215	6658010	December 2003	Enns et al.	X
	U 0216	6665489	December 2003	Collart	X
	U 0217	6668246	December 2003	Yeung et al.	X
	U 0218	6668325	December 2003	Collberg et al	. X
	U 0219	6687683	February 2004	Harada et al.	X
	U 0220	6725372	April 2004	Lewis et al	. X
	U 0221	6754822	June 2004	Zhao	X
	U 0222	6775772	August 2004	Binding et al.	X
	U 0223	6784354	August 2004	Lu et al.	X
	U 0224	6785815	August 2004	Serret-Avila et al.	X
	U 0225	6785825	August 2004	Colvin	X
	U 0226	6792548	September 2004	Colvin	X
	U 0227	6792549	September 2004	Colvin	X
	U 0228	6795925	September 2004	Colvin	X

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	U 0229	6799277	September 2004	Colvin	X
	U 0230	6813717	November 2004	Colvin	X
	U 0231	6813718	November 2004	Colvin	X
	U 0232	6823455	November 2004	Macy et al.	X
	U 0233	6834308	December 2004	Ikezoye et al.	X
	U 0234	6842862	January 2005	Chow et al.	X
	U 0235	6853726	February 2005	Moskowitz et al.	X
	U 0236	6857078	February 2005	Colvin	X
	U 0237	6931534	August 2005	Jandel et al.	X
	U 0238	6966002	November 2005	Torrubia-Saez	X
	U 0239	6983337	November 2005	Wold	X
	U 0240	6977894	December 2005	Achilles et al.	X
	U 0241	6978370	December 2005	Kocher	X
	U 0242	6986063	January 2006	Colvin	X
	U 0243	7007166	February 2006	Moskowitz et al.	X
	U 0244	7020285	March 2006	Kirovski et al.	X
	U 0245	7035409	April 2006	Moskowitz	X
	U 0246	7043050	May 2006	Yuval	X
	U 0247	7046808	May 2006	Metois et al.	X
	U 0248	7050396	May 2006	Cohen et al.	X
	U 0249	7051208	May 2006	Venkatesan et al.	X
	U 0250	7058570	June 2006	Yu et al.	X
	U 0251	7093295	August 2006	Saito	X

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	U 0252	7095874	August 2006	Moskowitz et al	X
	U 0253	7103184	September 2006	Jian	X
	U 0254	7107451	September 2006	Moskowitz	X
	U 0255	7123718	October 2006	Moskowitz et al.	X
	U 0256	7127615	October 2006	Moskowitz	X
	U 0257	7150003	December 2006	Naumovich et al.	X
	U 0258	7152162	December 2006	Moskowitz et al.	X
	U 0259	7159116	January 2007	Moskowitz	X
	U 0260	7162642	January 2007	Schumann et al.	X
	U 0261	7177429	February 2007	Moskowitz et al.	X
	U 0262	7177430	February 2007	Kim	X
	U 0263	7206649	April 2007	Kirovski et al.	X
	U 0264	7231524	June 2007	Bums	X
	U 0265	7233669.	June 2007	Candelore	X
	U 0266	7240210	July 2007	Michak et al.	X
	U 0267	7266697	September 2007	Kirovski et al	X
	U 0268	7287275	October 2007	Moskowitz	X
	U 0269	7289643	October 2007	Brunk et al.	X
	U 0270	7343492	March 2008	Moskowitz et al.	X
	U 0271	7346472	March 2008	Moskowitz et al.	X
	U 0272	7362775	April 2008	Moskowitz	X
	U 0273	7363278	April 2008	Schmelzer et al.	X
	U 0274	7409073	August 2008	Moskowitz et al.	X

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	U 0275	7457962	November 2008	Moskowitz	X
	U 0276	7460994	December 2008	Herre et al.	X
	U 0277	7475246	January 2009	Moskowitz	X
	U 0278	7530102	May 2009	Moskowitz	X
	U 0279	7532725	May 2009	Moskowitz et al.	X
	U 0280	7568100	July 2009	Moskowitz et al.	X
	U 0281	7647502	January 2010	Moskowitz	X
	U 0282	7647503	January 2010	Moskowitz	X
	U 0283	7779261	August 2010	Moskowitz	X
	U 0284	6990453	January 2006	Wang	X
	U 0285	6081597	June 2000	Hoffstein	X
	U 0286	7035049	Apr 2006	Yamamoto	X
	U 0287	7664263	Feb 2010	Moskowitz	X
	U 0288	7286451	Oct 2007	Wirtz	X
	U 0289	6385324	May 2002	Koppen	X
	U 0290	6674858	Jan 2004	Kimura	X
	U 0291	6148333	Nov 2000	Guedalia	X
	U 0292	6418421	Jun 2002	Hurtado	X
	U 0293	6385596	May 2002	Wiser	X
	U 0294	6226618	May 2001	Downs	X
	U 0295	6957330	Oct 2005	Hughes	X
	U 0296	5842213	Nov 1998	Odom	X
	U 0297	5818818	Oct 1998	Soumiya	X

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	U 0298	6590996	Jun 2003	Reed	X
	U 0299	5949055	Sept 1999	Fleet	X
	U 0300	6067622	May 2000	Moore	X
	U 0301	7761712	Jun 2010	Moskowitz	X
	U 0302	7743001	Jun 2010	Vermeulen	X
	U 0303	6865747	Mar 2005	Mercier	X
	U 0304	6611599	Aug 2003	Natarajan	X
	U 0305	6480937	Nov 2002	Vorbach	X
	U 0306	6398245	Jun 2002	Gruse	X
	U 0307	6950941	Sept 2005	Lee	X
	U 0308	6983058	Jan 2006	Fukuoka	X
	U 0309	5675653	Oct 1997	Nelson	X
	U 0310	6804453	Oct 2004	Sasamoto	X
	U 0311	6178405	Jan 2001	Ouyang	X
	U 0312	5839100	Nov 1998	Wegener	X
	U 0313	5781184	Jul 1998	Wasserman	X
	U 0314	5617506	Apr 1997	Burk	X
	U 0315	5327520	Jul 1994	Chen	X
	U 0316	5111530	May 1992	Kutaragi	X
	U 0317	7095715	Aug 2006	Buckman	X
	U 0318	6173322	Jan 2001	Hu	X
	U 0319	5754938	May 1998	Herz	X
	U 0320	6035398	Mar 2000	Bjorn	

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	U 0321	5901178	May 1999	Lee	
	U 0322	8214175	July 2012	Moskowitz	
	U 0323	8265278	Sept 2012	Moskowitz	
	U 0324	8161286	Nov 2010	Moskowitz	
	U 0325	8307213	Jan 2011	Moskowitz	
	U 0326	8121343	May 2012	Moskowitz	
	U 0327	5437050	Jul 1995	Lamb	
	U 0328	5123045	Jun 1992	Ostrovsky	X
	U 0329	7310815	Dec 2007	Yanovsky	
	U 0330	8179846	May 2012	Dolganow	
	U 0331	7719966	May 2010	Luft	
	U 0332	7630379	Dec 2009	Morishita	
	U 0333	5949973	Sept 1999	Yarom	
	U 0334	8400566	Mar. 2013	Terry	
	U 0335	5649284	July 1997	Yoshinobu	
	U 0336	744506	Oct 2008	Datta	
	U 0337	6480963	Oct 2002	Tachibana	
	U 0338	6510513	Jan 2003	Darrow	
	U 0339	5189411	Feb 1993	Collar	
	U 0340	5293633	Mar 1994	Robbins	
	U 0341	4633462	Dec 1986	Stifle	
	U 0342	5103461	Mar 1992	Cain	
	U 0343	6272535	Aug 2001	Iwamura	

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EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 0344	6029195	Feb 2000	Herz	
	U 0345	8095949	Jan 2012	Hendricks	
	U 0346	5297032	Mar 1994	Trojan	
	U 0347	5644727	Jul 1997	Atkins	
	U 0348	5721781	Feb 1998	Deo	
	U 0349	5822436	Oct 1998	Rhoads	
	U 0350	5845266	Dec 1998	Lupien	
	U 0351	5864827	Jan 1999	Wilson	
	U 0352	5875437	Feb 1999	Atkins	
	U 0353	5892900	Apr 1999	Ginter	
	U 0354	6108722	Aug 2000	Troeller	
	U 0355	6029146	Feb 2000	Hawkins	
	U 0356	6032957	Mar 2000	Kiyosaki	
	U 0357	6134535	Oct 2000	Belzberg	
	U 0358	6185683	Feb 2001	Ginter	
	U 0359	6233566	May 2001	Levine	
	U 0360	6253193	Jun 2001	Ginter	
	U 0361	6272474	Aug 2001	Garcia	
	U 0362	6317728	Nov 2001	Kane	
	U 0363	6363488	Mar 2002	Ginter	
	U 0364	6389402	May 2002	Ginter	
	U 0365	6427140	Jul 2002	Ginter	
	U 0366	6484153	Nov 2002	Walker	

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	U 0367	6556976	Aug 1987	Callen	
	U 0368	6574608	Jun 2003	Dahod	
	U 0369	6601044	Jul 2003	Wallman	
	U 0370	6594643	Jul 2003	Freeny	
	U 0371	6618188	Sep 2003	Haga	
	U 0372	6778968	Aug 2004	Gulati	
	U 0373	6839686	Jan 2005	Galant	
	U 0374	6856867	Feb 2005	Woolston	
	U 0375	6876982	Apr 2005	Lancaster	
	U 0376	7003480	Feb 2006	Fox	
	U 0377	5822436	Oct 1998	Rhoads	

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LISTING OF UNITED STATES PUBLISHED APPLICATIONS - P Series

EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	P 01	20010010078	July 2001	Moskowitz	X
	P 02	20010043594	November 2001	Ogawa et al.	X
	P 03	20020010684	January 2002	Moskowitz	X
	P 04	20020026343	February 2002	Duenke	X
	P 05	20020056041	May 2002	Moskowitz	X
	P 06	20020071556	June 2002	Moskowitz et al.	X
	P 07	20020073043	June 2002	Herman et al.	X
	P 08	20020097873	July 2002	Petrovic	X
	P 09	20020103883	August 2002	Haverstock et al.	X
	P 010	20020161741	October 2002	Wang et al.	X
	P 011	20030126445	July 2003	Wehrenberg	X
	P 012	20030133702	July 2003	Collart	X
	P 013	20030200439	October 2003	Moskowitz	X
	P 014	20030219143	November 2003	Moskowitz et al.	X
	P 015	20040028222	February 2004	Sewell et al.	X
	P 016	20040037449	February 2004	Davis et al.	X
	P 017	20040049695	March 2004	Choi et al.	X
	P 018	20040059918	March 2004	Xu	X
	P 019	20040083369	April 2004	Erlingsson et al.	X
	P 020	20040086119	May 2004	Moskowitz	X
	P 021	20040093521	May 2004	Hamadeh et al.	X
	P 022	20040117628	June 2004	Colvin	X

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	P 023	20040117664	June 2004	Colvin	X
	P 024	20040125983	July 2004	Reed et al.	X
	P 025	20040128514.	July 2004	Rhoads	X
	P 026	20040225894	November 2004	Colvin	X
	P 027	20040243540	December 2004	Moskowitz et al.	X
	P 028	20050135615	June 2005	Moskowitz et al.	X
	P 029	20050160271	July 2005	Brundage et al.	X
	P 030	20050177727	August 2005	Moskowitz et al.	X
	P 031	20050246554	November 2005	Batson	X
	P 032	20060005029	January 2006	Petrovic et al.	X
	P 033	20060013395	January 2006	Brundage et al.	X
	P 034	20060013451	January 2006	Haitsma	X
	P 035	20060041753	February 2006	Haitsma	X
	P 036	20060101269	May 2006	Moskowitz et al.	X
	P 037	20060140403	June 2006	Moskowitz	X
	P 038	20060285722	December 2006	Moskowitz et al.	X
	P 039	20070011458	January 2007	Moskowitz	X
	P 040	20070028113	February 2007	Moskowitz	X
	P 041	20070064940	March 2007	Moskowitz et al.	X
	P 042	20070079131.	April 2007	Moskowitz et al.	X
	P 043	20070083467	April 2007	Lindahl et al.	X
	P 044	20070110240	May 2007	Moskowitz et al.	X
	P 045	20070113094	May 2007	Moskowitz et al.	X

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	P 046	20070127717	June 2007	Herre et al.	X
	P 047	20070226506	September 2007	Moskowitz	X
	P 048	20070253594	November 2007	Lu et al.	X
	P 049	20070294536.	December 2007	Moskowitz et al.	X
	P 050	20070300072	December 2007	Moskowitz	X
	P 051	20070300073	December 2007	Moskowitz	X
	P 052	20080005571	January 2008	Moskowitz	X
	P 053	20080005572	January 2008	Moskowitz	X
	P 054	20080016365	January 2008	Moskowitz	X
	P 055	20080022113	January 2008	Moskowitz	X
	P 056	20080022114	January 2008	Moskowitz	X
	P 057	20080028222	January 2008	Moskowitz	X
	P 058	20080046742	February 2008	Moskowitz	X
	P 059	20080075277	March 2008	Moskowitz et al.	X
	P 060	20080109417	May 2008	Moskowitz	X
	P 061	20080133927	June 2008	Moskowitz et al.	X
	P 062	20080151934	June 2008	Moskowitz et al.	X
	P 063	20090037740	February 2009	Moskowitz	X
	P 064	20090089427	April 2009	Moskowitz et al.	X
	P 065	20090190754	July 2009	Moskowitz et al.	X
	P 066	20090210711	August 2009	Moskowitz	X
	P 067	20090220074	September 2009	Moskowitz et al.	X
	P 068	20100002904	January 2010	Moskowitz	X

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	P 069	20100005308	January 2010	Moskowitz	X
	P 070	20100098251	Apr 2010	Moskowitz	X
	P 071	20100220861	Sept 2010	Moskowitz	X
	P 072	20100202607	Aug 2010	Moskowitz	X
	P 073	20020047873	June 2002	Petrovic	X
	P 074	20020009208	Jan 2002	Alattar	X
	P 075	20010029580	October 2001	Moskowitz	X
	P 076	20100182570	July 2010	Chota	X
	P 077	20100077220	March 2010	Moskowitz	X
	P 078	20100077219	March 2010	Moskowitz	X
	P 079	20100064140	March 2010	Moskowitz	X
	P 080	20100153734	June 2010	Moskowitz	X
	P 081	20100106736	April 2010	Moskowitz	X
	P 082	20060251291	November 2006	Rhoads	X
	P 083	20030002862	January 2003	Rodriguez	X
	P 084	2003005780	May 2003	Hansen	X
	P 085	20020152179	Oct 2002	Racov	X
	P 086	20030027549	Feb 2003	Kiel	X
	P 087	20020057651	May 2002	Roberts	X
	P 088	20110069864	March 2011	Moskowitz	
	P 089	20100313033	Dec 2010	Moskowitz	
	P 090	20110019691	Jan 2011	Moskowitz	
	P 091	20030023852	Jan. 2003	Wold	

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	P 092	20030033321	Feb 2003	Schrempp	
	P 093	20130145058	June 2013	Shuholm	
	P 094	20120057012	Mar. 2012	Sitrick	
	P 095	20110128445	Jun 2011	Carrieres	
	P 096	20020188570	Dec 2002	Holliman	
	P 097	20020069174	Jun 2002	Fox	

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LISTING OF FOREIGN AND INTERNATIONAL PATENT DOCUMENTS - F Series

EXAMINER INITIALS	REFERENCE NUMBER (F SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	COUNTRY OR REGION	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	F 01-	EP0372601	Jun., 1990	EP		X
	F 02-	EP0565947	Oct., 1993	EP		X
	F 03-	EP0581317	Feb., 1994	EP		X
	F 04-	EP0649261	Apr., 1995	EP		X
	F 05-	EP0651554	May., 1995	EP		X
	F 06-	EP1354276	Dec., 2007	EP		X
	F 07-	NL 1005523	Sep., 1998	NL		X
	F 08-	WO 9514289	May., 1995	WO		X
	F 09-	WO 9629795	Sep., 1996	WO		X
	F 010-	WO 9724833	Jul., 1997	WO		X
	F 011-	WO 9744736	Nov., 1997	WO		X
	F 012-	WO9837513	Aug., 1998	WO		X
	F 013-	WO 9952271	Oct., 1999	WO		X
	F 014-	WO 9962044	Dec., 1999	WO		X
	F 015-	WO 9963443	Dec., 1999	WO		X
	F 016-	WO9726733	Jan. 1997	WO		X
	F 017-	WO98002864	Jul. 1997	WO		X
	F 018-	WO 0057643	Sept 2000	WO		X
	F 019-	WO 9642151	Dec 1996	WO		X
	F 020-	EP0872073	July 1996	EP		X
	F 021-	WO0118628	March 2001	WO		X
	F 022-	WO0143026	June 2001	WO		X

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EXAMINER INITIALS	REFERENCE NUMBER (F SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	COUNTRY OR REGION	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	F 023-	WO0203385	Jan 2002	WO		X
	F 024-	WO9701892	June 1995	WO		X
	F 025-	WO9726732	July 1997	WO		X
	F 026-	WO9802864	Jan 1998	WO		X
	F 027-	EP1547337	Mar 2006	EP		X
	F 028-	EP0581317A2	Feb 1994	EP		X
	F 029-	WO023385A1	Oct 2002	WO		X

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LISTING OF NON PATENT LITERATURE - L Series

EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	1	L- 01	N/A	US. Appl. No. 08/999,766, filed Jul. 23, 1997, entitled "Steganographic Method and Device", published as 7568100 07-28-2009, cited as U280.	X
	2	L- 02	N/A	EPO Application No. 96919405.9, entitled "Steganographic Method and Device"; published as EP0872073 (A2), 10-21-1998, cited herein as F20.	X
	3	L- 03	N/A	U.S. Appl. No. 11/050,779, filed Feb. 7, 2005, entitled "Steganographic Method and Device", published as 20050177727 A1 08-11-2005, cited herein as P30.	X
	4	L- 04	N/A	U.S. Appl. No. 08/674,726, filed Jul. 2, 1996, entitled "Exchange Mechanisms for Digital Information Packages with Bandwidth Securitization, Multichannel Digital Watermarks, and Key Management", published as 7362775 04-22-2008, cited herein as U272 .	X
	5	L- 05	N/A	U.S. Appl. No. 09/545,589, filed Apr. 7, 2000, entitled "Method and System for Digital Watermarking", published as 7007166 02-28-2006, cited herein as U243	X
	6	L- 06	N/A	U.S. Appl. No. 11/244,213, filed Oct. 5, 2005, entitled "Method and System for Digital Watermarking", published as 2006-0101269 A1 05-11-2006, cited herein as P36	X
	7	L- 07	N/A	U.S. Appl. No. 11/649,026, filed Jan. 3, 2007, entitled "Method and System for Digital Watermarking", published as 2007-0113094 A1 05-17-2007, cited herein as P45.	X
	8	L- 08	N/A	U.S. Appl. No. 09/046,627, filed Mar. 24, 1998, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 6,598,162 07-22-2003, cited herein as U212.	X
	9	L- 09	N/A	U.S. Appl. No. 10/602,777, filed Jun. 25, 2003, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 2004-0086119 A1 05-06-2004, cited herein P20.	X

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EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	10	L- 010	N/A	U.S. Appl. No. 09/053,628, filed Apr. 2, 1998, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", 6,205,249 03-20-2001, cited herein as U161.	X
	11	L- 011	N/A	U.S. Appl. No. 09/644,098, filed Aug. 23, 2000, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 7,035,409 04-25-2006, cited herein as U245.	X
	12	L- 012	N/A	Jap. App. No. 2000-542907, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking"; which is a JP national stage of PCT/US1999/007262, published as WO/1999/052271, 10/14/1999, F13 here in above..	X
	13	L- 013	N/A	U.S. Appl. No. 09/767,733, filed Jan. 24, 2001 entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 2001-0010078 A1 07-26-2001, cited herein as P1.	X
	14	L- 014	N/A	U.S. Appl. No. 11/358,874, filed Feb. 21, 2006, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 2006-0140403 A1 06-29-2006, cited herein as P37.	X
	15	L- 015	N/A	U.S. Appl. No. 10/417,231, filed Apr. 17, 2003, entitled "Methods, Systems And Devices For Packet Watermarking And Efficient Provisioning Of Bandwidth", published as 2003-0200439 A1 10-23-2003, cited herein as P13,	X
	16	L- 016	N/A	U.S. Appl. No. 09/789,711, filed Feb. 22, 2001, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2001-0029580 A1 10-11-2001, cited herein as P75.	X
	17	L- 017	N/A	U.S. Appl. No. 11/497,822, filed Aug. 2, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2007-0011458 A1 01-11-2007, cited herein as P39.	X

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	18	L- 018	N/A	U.S. Appl. No. 11/599,964, filed Nov. 15, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2008-0046742 A1 02-21-2008, cited herein as P58.	X
	19	L- 019	N/A	U.S. Appl. No. 11/599,838, filed Nov. 15, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2007-0226506 A1 09-27-2007, cited herein as P47.	X
	20	L- 020	N/A	U.S. Appl. No. 10/369,344, filed Feb. 18, 2003, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digitized Data", published as 2003-0219143 A1 11-27-2003, cited herein as P14.	X
	21	L- 021	N/A	U.S. Appl. No. 11/482,654, filed Jul. 7, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digitized Data", published as 2006-0285722 A1 12-21-2006, cited herein as P38.	X
	22	L- 022	N/A	U.S. Appl. No. 09/594,719, filed Jun. 16, 2000, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 7,123,718 10-17-2006, cited herein as U255.	X
	23	L- 023	N/A	U.S. Appl. No. 11/519,467, filed Sep. 12, 2006, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 2007-0064940 A1 03-22-2007, cited herein as P41.	X
	24	L- 024	N/A	U.S. Appl. No. 09/731,040, filed Dec. 7, 2000, entitled "Systems, Methods And Devices For Trusted Transactions", 2002-0010684 A1 01-24-2002, cited herein as P3.	X
	25	L- 025	N/A	U.S. Appl. No. 11/512,701, filed Aug. 29, 2006, entitled "Systems, Methods And Devices For Trusted Transactions", published as 2007-0028113 A1 02-01-2007, cited herein as P40.	X
	26	L- 026	N/A	U.S. Appl. No. 10/049,101, filed Feb. 8, 2002, entitled "A Secure Personal Content Server", published as 7,475,246 01-06-2009, cited herein as U277.	X

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	27	L- 027	N/A	PCT Application No. PCT/US00/21189, filed Aug. 4, 2000, entitled, "A Secure Personal Content Server", Pub. No.: WO/2001/018628 ; Publication Date: 15.03.2001, cited herein as F21.	X
	28	L- 028	N/A	U.S. Appl. No. 09/657,181, filed Sep. 7, 2000, entitled "Method and Device For Monitoring And Analyzing Signals", published as 7,346,472 03-18-2008, cited herein as U271.	X
	29	L- 029	N/A	U.S. Appl. No. 10/805,484, filed Mar. 22, 2004, entitled "Method And Device For Monitoring And Analyzing Signals", published as 2004-0243540 A1 12-02-2004, cited herein as P27.	X
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	31	L- 031	N/A	U.S. Appl. No. 11/518,806, filed Sep. 11, 2006, entitled "Improved Security Based on Subliminal and Supraliminal Channels For Data Objects", 2008-0028222 A1 01-31-2008, cited herein as P57.	X
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	33	L- 033	N/A	U.S. Appl. No. 11/592,079, filed Nov. 2, 2006, entitled "Linear Predictive Coding Implementation of Digital Watermarks", published as 2007-0079131 A1 04-05-2007, cited herein as P42.	X
	34	L- 034	N/A	U.S. Appl. No. 09/731,039, filed Dec. 7, 2000, entitled "System and Methods for Permitting Open Access to Data Objects and for Securing Data within the Data Objects", published as 2002-0071556 A1 06-13-2002, cited herein as P06.	X

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	46	L- 046	1996	Gruhl, Daniel et al., Echo Hiding. In Proceeding of the Workshop on Information Hiding. No. 1174 in Lecture Notes in Computer Science, Cambridge, England (May/Jun. 1996).	X
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	52	L- 052	1995	Bender, Walter R. et al., Techniques for Data Hiding, SPIE Int. Soc. Opt. Eng., vol. 2420, pp. 164-173, 1995.	X
	53	L- 053	1995	Zhao, Jian et al., Embedding Robust Labels into Images for Copyright Protection, (xp 000571976), pp. 242-251, 1995.	X
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	55	L- 055	1994	Schneier, Bruce, Applied Cryptography, 1st Ed., pp. 67-68, 1994.	X
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	77	L- 077	1991	Pohlmann, Ken C., "Principles of Digital Audio", 2nd Ed., 1991, pp. 1-9, 19-25, 30-33, 41-48, 54-57, 86-107, 375-387.	X
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	104	L- 0104	N/A	U.S. Appl. No. 60/234,199, filed Sep. 20, 2000, "Improved Security Based on Subliminal and Supraliminal Channels For Data Objects".	X
	105	L- 0105	N/A	U.S. Appl. No. 09/671,739, filed Sep. 29, 2000, entitled "Method And Device For Monitoring And Analyzing Signals".	X
	106	L- 0106		DUPLICATE OF L-34, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
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	109	L- 0109		DUPLICATE OF L-58, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	110	L- 0110		DUPLICATE OF L-59, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	111	L- 0111		DUPLICATE OF L-61, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	112	L- 0112		DUPLICATE OF L-62, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X

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	113	L- 0113		DUPLICATE OF L-63, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	114	L- 0114		DUPLICATE OF L-65, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	115	L- 0115	Unknown.	Tirkel, A.Z., "A Two-Dimensional Digital Watermark", Scientific Technology, 686, 14, date unknown. (citation revised upon review on 11/16/10 by RAN.)	X
	116	L- 0116		DUPLICATE OF L-65, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	117	L- 0117		DUPLICATE OF L-68, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	118	L- 0118		DUPLICATE OF L-69, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	119	L- 0119		DUPLICATE OF L-70, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	120	L- 0120		DUPLICATE OF L-71, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	121	L- 0121		DUPLICATE OF L-72, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	122	L- 0122		DUPLICATE OF L-73, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	123	L- 0123		DUPLICATE OF L-74, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	124	L- 0124		DUPLICATE OF L-75, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	125	L- 0125		DUPLICATE OF L-076, REMOVED. RN. 11/16/2010	X

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	126	L- 0126		DUPLICATE OF L-77, REMOVED. RN. 11/16/2010	X
	127	L- 0127		DUPLICATE OF L-78, REMOVED. RN. 11/16/2010	X
	128	L- 0128		DUPLICATE OF L-79, REMOVED. RN. 11/16/2010	X
	129	L- 0129		EP0581317A2, MOVED TO FOREIGN PATENT PUBS as F-028	X
	130	L- 0130		DUPLICATE OF L-52, REMOVED. RN. 11/16/2010	X
	131	L- 0131		DUPLICATE OF L-36, REMOVED. RN. 11/16/2010	X
	132	L- 0132		DUPLICATE OF L-38, REMOVED. RN. 11/16/2010.	X
	133	L- 0133		DUPLICATE OF L-37, REMOVED. RN. 11/16/2010	X
	134	L- 0134		DUPLICATE OF L-36, REMOVED. RN. 11/16/2010	X
	135	L- 0135		DUPLICATE OF L-37, REMOVED. RN. 11/16/2010	X
	136	L- 0136		DUPLICATE OF L-38, REMOVED. RN. 11/16/2010	X
	137	L- 0137		DUPLICATE OF L-39, REMOVED. RN. 11/16/2010	X
	138	L- 0138		DUPLICATE OF L-40, REMOVED. RN. 11/16/2010	X
	139	L- 0139		DUPLICATE OF L-41, REMOVED. RN. 11/16/2010	X

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	140	L- 0140		DUPLICATE OF L-42, REMOVED. RN. 11/16/2010	X
	141	L- 0141		DUPLICATE OF L-43, REMOVED. RN. 11/16/2010	X
	142	L- 0142		DUPLICATE OF L-44, REMOVED. RN. 11/16/2010	X
	143	L- 0143		DUPLICATE OF L-45, REMOVED. RN. 11/16/2010.	X
	144	L- 0144		DUPLICATE OF L-46, REMOVED. RN. 11/16/2010.	X
	145	L- 0145		DUPLICATE OF L-47, REMOVED. RN. 11/16/2010	X
	146	L- 0146		DUPLICATE OF L-48, REMOVED. RN. 11/16/2010	X
	147	L- 0147		DUPLICATE OF L-49, REMOVED. RN. 11/16/2010	X
	148	L- 0148		DUPLICATE OF L-50, REMOVED. RN. 11/16/2010	X
	149	L- 0149		DUPLICATE OF L-51, REMOVED. RN. 11/16/2010	X
	150	L- 0150		DUPLICATE OF L-52, REMOVED. RN. 11/16/2010	X
	151	L- 0151		DUPLICATE OF L-63, REMOVED. RN. 11/16/2010	X
	152	L- 0152		DUPLICATE OF L-54, REMOVED. RN. 11/16/2010	X
	153	L- 0153		DUPLICATE OF L-55, REMOVED. RN. 11/16/2010.	X

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	154	L- 0154		DUPLICATE OF L-80, REMOVED. RN. 11/16/2010.	X
	155	L- 0155	N/A	PCT International Search Report in PCT/US95/08159.	X
	156	L- 0156	N/A	PCT International Search Report in PCT/US96/10257.	X
	157	L- 0157	N/A	Supplementary European Search Report in EP 96919405.	X
	158	L- 0158	N/A	PCT International Search Report in PCT/US97/00651.	X
	159	L- 0159	N/A	PCT International Search Report in PCT/US97/00652	X
	160	L- 0160	N/A	PCT International Search Report in PCT/US97/11455.	X
	161	L- 0161		PCT International Search Report in PCT/US99/07262.	X
	162	L- 0162		PCT International Search Report in PCT/US00/06522	Xf
	163	L- 0163		Supplementary European Search Report in EP00919398	X
	164	L- 0164		PCT International Search Report in PCT/US00/18411.	X
	165	L- 0165		PCT International Search Report in PCT/US00/18411.	X
	166	L- 0166		PCT International Search Report in PCT/US00/33126	X
	167	L- 0167		PCT International Search Report in PCT/US00/21189	X
	168	L- 0168		Delaigle, J.-F., et al. "Digital Watermarking," Proceedings of the SPIE, vol. 2659, Feb 1, 1996, pp. 99-110.	X

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	171	L- 0171	1998	Wong, Ping Wah. "A Public Key Watermark for Image Verification and Authentication," IEEE International Conference on Image Processing, vol. 1 Oct. 4-7, 1998, pp. 455-459.	X
	172	L- 0172	1998	Fabien A.P. Petitcolas, Ross J. Anderson and Markkus G. Kuhn, "Attacks on Copyright Marking Systems," LNCS, vol. 1525, Apr. 14-17, 1998, pp. 218-238 ISBN: 3-540-65386-4.	X
	173	L- 0173	1996	Ross Anderson, "Stretching the Limits of Steganography," LNCS, vol. 1174, May/Jun. 1996, 10 pages, ISBN: 3-540-61996-8.	X
	174	L- 0174	1997	Joseph J.K. O'Ruanaidh and Thierry Pun, "Rotation, Scale and Translation Invariant Digital Image Watermarking", pre-publication, Summer 1997 4 pages.	X
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	177	L- 0177	1998	Rivest, R. "Chaffing and Winnowing: Confidentiality without Encryption", MIT Lab for Computer Science, http://people.csail.mit.edu/rivest/Chaffing.txt Apr. 24, 1998, 9 pp.	X
	178	L- 0178	2003	PortalPlayer, PP5002 digital media management system-on-chip, May 1, 2003, 4 pp.	X

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	180	L- 0180	2008	Cayre, et al., "Kerckhoff's-Based Embedding Security Classes for WOA Data Hiding", IEEE Transactions on Information Forensics and Security, vol. 3 No. 1, Mar. 2008, 15 pp.	X
	181	L- 0181	1999	Wayback Machine, dated Jan. 17, 1999, http://web.archive.org/web/19990117020420/http://www.netzero.com/, accessed on Feb. 19, 2008.	X
	182	L- 0182	1997	Namgoong, H., "An Integrated Approach to Legacy Data for Multimedia Applications", Proceedings of the 23rd EUROMICRO Conference, vol., Issue 1-4, Sep. 1997, pp. 387-391.	X
	183	L- 0183	2007	Wayback Machine, dated Aug. 26, 2007, http://web.archive,org/web/20070826151732/http://www.screenplaysmag.com/t-abid/96/articleType/ArticleView/articleId/495/Default.aspx/.	X
	184	L- 0184	2009	"YouTube Copyright Policy: Video Identification tool--YouTube Help", accessed Jun. 4, 2009, http://www.google.com/support/youtube/bin/answer.py?h1=en&answer=83766, 3 pp.	X
	185	L- 0185	N/A	U.S. Appl. No. 12/665,002, filed Dec. 22, 2009, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 20100182570 A1 07-22-2010, P76.	X
	186	L- 0186	N/A	U.S. Appl. No. 12/592,331, filed Nov. 23, 2009, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 20100077220 A1 03-25-2010, P77.	X
	187	L- 0187	N/A	U.S. Appl. No. 12/590,553, filed Nov. 10, 2009, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 20100077219 A1 03-25-2010, P78.	X

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	188	L- 0188	N/A	U.S. Appl. No. 12/590,681, filed Nov. 12, 2009, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 20100064140 A1 03-11-2010, P79.	X
	189	L- 0189	N/A	U.S. Appl. No. 12/655,036, filed Dec. 22, 2009, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 20100153734 A1 06-17-2010, P80 .	X
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	191	L- 0191	N/A	PCT Application No. PCT/US95/08159, filed Jun. 26, 1995, entitled, "Digital Information Commodities Exchange with Virtual Menuing", published as WO/1997/001892; Publication Date: 16.01.1997, F24.	X
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	193	L- 0193	N/A	PCT Application No. PCT/US97/00651, filed Jan. 16, 1997, entitled, "Method for Stega-Cipher Protection of Computer Code", published as WO/1997/026732; Publication Date: 24.07.1997.	X
	194	L- 0194	N/A	PCT Application No. PCT/US97/00652, filed Jan. 17, 1997, entitled, "Method for an Encrypted Digital Watermark", published as WO/1997/026733; Publication Date: 24.07.1997	X
	195	L- 0195	N/A	PCT Application No. PCT/US97/11455, filed Jul. 2, 1997, entitled, "Optimization Methods for the Insertion, Protection and Detection of Digital Watermarks in Digitized Data", published as WO/1998/002864; Publication Date: 22.01.1998	X
	196	L- 0196	N/A	PCT Application No. PCT/US99/07262, filed Apr. 2, 1999, entitled, "Multiple Transform Utilization and Applications for Secure Digital Watermarking", published as WO/1999/052271; Publication Date: 14.10.1999.	X

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	198	L- 0198	N/A	PCT Application No. PCT/US00/18411, filed Jul. 5, 2000, entitled, "Copy Protection of Digital Data Combining Steganographic and Cryptographic Techniques"	X
	199	L- 0199	N/A	PCT Application No. PCT/US00/33126, filed Dec. 7, 2000, entitled "Systems, Methods and Devices for Trusted Transactions", published as WO/2001/043026; Publication Date: 14.06.2001.	X
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	201	L- 0201	N/A	US Provisional Application 60/222,023 filed July 31, 2007 entitled "Method and apparatus for recognizing sound and signals in high noise and distortion"	X
	202	L- 0202	N/A	US Application 11/458,639 filed July 19, 2006 entitled "Methods and Systems for Inserting Watermarks in Digital Signals", published as 20060251291 A1 11-09-2006, P82.	X
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	228	L- 0228	2013	Blue Spike, LLC. v. Texas Instruments, Inc et. al, (No: 6:12-CV-499-MHS), Audible Magic Corporations's amended Answer (E.D. TX filed 7/15/2013) (Document 885 page ID 9581), (PACER)	
	229	L- 0229	2006	Moskowitz, "Introduction-Digital Rights Management," Multimedia Security Technologies for Digital Rights Management (2006), Elsevier	
	230	L- 0230	1999	George, Mercy; Chouinard, Jean-Yves; Georgana, Nicolas. Digital Watermarking of Images and video using Direct Sequence Spread Spectrum Techniques. 1999 IEEE Canadian Conference on Electrical and Computer Engineering Vol. 1. Pub. Date: 1999 Relevant pages 116-121. http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=arnumber=807181	

DATE:	09/06/2014	EXAMINER'S SIGNATURE:	/Izunna Okeke/

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

Printed: May 8, 2014 (1:29pm)
Path: Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ReferenceCitationList_SCOT0014-6_05-01-2014.wpd
37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	231	L- 0231	4/4/2014	Shazam Entertainment Limited's Amended Answer to Blue Spike, LLC's complaint and counterclaims against Blue Spike LLC, Blue Spike, Inc and Scott A. Moskowitz , Shazam Entertainment Ltd v. Blue Spike, LLC, Blue Spike, Inc, and Scott Moskowitz (E.D.T.X Dist Ct.) Case No. 6:12-CV-00499-MHS	
	232	L- 0232	4/4/2014	Audible Magic Corporation's Second Amended Answer to Blue Spike LLC's Original Complaint for patent infringement and counterclaims against Blue Spike LLC, Blue Spike, Inc and Scott Moskowitz. Blue Spike LLC v. Texas Instruments, Audible Magic Corporation (E.D.T.X Dist Ct.) Case No. 6:12-CV-499-MHS	

DATE: 09/06/2014	EXAMINER'S SIGNATURE: /Izunna Okeke/

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

S129	47	((shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) and ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:55
S130	2	S88 and S129	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:55
S131	117	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or $3arrang$3) same (code or object) same (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:57
S132	2	S88 and S131	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:57
S135	57978	G06F21/10;H04L9/3247;H04L9/3236;H04L9/065;G06F21/335;G06F21/125;G06f21/16;G06F2221/2107;G06F2211/007;G06F2221/0737.cpc.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:55
S136	65	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory or register)) and ((protect$3 or stop$4 or inhibit$3) with (memory or register) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S138	27	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory or register)) and ((protect$3 or stop$4 or inhibit$3) with (memory or register) with analysis) and ((run or execution) adj time) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S140	41	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or arrang$3) same (code or object) same (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S142	52	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S146	2	((shuffl$3 or arrang$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT;	AND	ON	2014/09/04 20:57

			USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			
S154	1383	((shuffl$3 or arrang$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S155	85	S135 and S154	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S156	4	((shuffl$3 or arrang$3) with (code) with (memory)) and ((modif$5 or alter$5 or chang$5) with (program adj counter)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S158	142	((shuffl$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S159	83	S135 and S158	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:57
S160	96	((shuffl$3) near5 (code) near5 (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:58
S161	83	S135 and S160	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:58
S162	59	((shuffl$3 or arrang$3) same (code) same (memory)) and ((modif$5 or alter$5 or chang$5) same (program adj counter)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:58
S168	37	((shuffl$3 or (arrang$3 or order) or transpos$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS;	AND	ON	2014/09/04 20:58

			EPO; JPO; DERWENT; IBM_TDB			
S170	47	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) and ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:58
S172	6	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) same ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/04 20:58
S174	57978	G06F21/10;H04L9/3247;H04L9/3236;H04L9/065;G06F21/335;G06F21/125;G06f21/16;G06F2221/2107;G06F2211/007;G06F2221/0737.cpc.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S175	57978	S174	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S176	5824	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) with ((code or software) near5 (memory or register or buffer)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S177	114	S174 and S176	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S178	3797	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) with ((code or software) near3 (memory or register or buffer)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S179	95	S174 and S178	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S180	3474	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) with ((code or software) near3 (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT;	AND	ON	2014/09/05 08:33

			IBM_TDB			
S181	95	S174 and S180	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S182	832	(shuffl$3 or transpos$5 or $3arrang$3 or permut$5) with ((code or software) near3 (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S183	86	S174 and S182	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S184	745	(shuffl$3 or transpos$5 or $3arrang$3 or permut$5) near5 ((code or software) near5 (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S185	90	S174 and S184	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S186	349	(shuffl$3 or transpos$5 or $3arrang$3 or permut$5) near3 ((code or software) near3 (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S187	86	S174 and S186	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S188	119	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or $3arrang$3) same (code or object) same (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S189	0	S174 and S188	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:33
S190	1383	((shuffl$3 or arrang$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB;	AND	ON	2014/09/05

			USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			08:34
S191	85	S174 and S190	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:34
S192	96	((shuffl$3) near5 (code) near5 (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:34
S193	83	S174 and S192	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/09/05 08:34

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S194	0	(memory adj scheduler adj code adj resource).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2014/09/05 08:38
S195	0	(memory adj scheduler adj code adj resource) and (shuffle with (code adj resource)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2014/09/05 08:38
S196	0	(memory adj scheduler adj code adj resource) and (shuffle with (code adj resource) and (program adj counter)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2014/09/05 08:39
S197	0	(stack adj frame) and (memory adj scheduler adj code adj resource) and (shuffle with (code adj resource) and (program adj counter)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2014/09/05 08:39

9/6/2014 11:32:48 PM
C:\Users\iokeke\Documents\EAST\Workspaces\13556420.wsp

Issue Classification	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	**Art Unit**
	IZUNNA OKEKE	2432

CPC							
Symbol						**Type**	**Version**
G06F		21	/	10		F	2013-01-01
G06F		21	/	125		I	2013-01-01
G06F		21	/	16		I	2013-01-01
G06F		21	/	335		I	2013-01-01
G06T		1	/	0021		I	2013-01-01
H04L		9	/	065		I	2013-01-01
H04L		9	/	3236		I	2013-01-01
H04L		9	/	3247		I	2013-01-01
G06F		2211	/	007		A	2013-01-01
G06F		2221	/	0737		A	2013-01-01
G06F		2221	/	2107		A	2013-01-01
G06T		2201	/	0064		A	2013-01-01
G06T		2201	/	0083		A	2013-01-01
H04L		2209	/	605		A	2013-01-01
H04L		2209	/	608		A	2013-01-01

CPC Combination Sets								
Symbol					**Type**	**Set**	**Ranking**	**Version**
			/					
			/					

NONE		**Total Claims Allowed:**	
(Assistant Examiner)	(Date)	50	
/IZUNNA OKEKE/ Primary Examiner.Art Unit 2432	09/06/2014	O.G. Print Claim(s)	O.G. Print Figure
(Primary Examiner)	(Date)	93	1

U.S. Patent and Trademark Office — Part of Paper No. 20140905

Issue Classification	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	**Art Unit**
	IZUNNA OKEKE	2432

US ORIGINAL CLASSIFICATION		INTERNATIONAL CLASSIFICATION										
CLASS	SUBCLASS	CLAIMED					NON-CLAIMED					
713	193	G	0	6	F	21 / 00 (2013.01.01)						
CROSS REFERENCE(S)												
CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)											

NONE (Assistant Examiner)	(Date)	**Total Claims Allowed:** 50	
/IZUNNA OKEKE/ Primary Examiner.Art Unit 2432 (Primary Examiner)	09/06/2014 (Date)	O.G. Print Claim(s) 93	O.G. Print Figure 1

U.S. Patent and Trademark Office

Part of Paper No. 20140905

Issue Classification	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	**Art Unit**
	IZUNNA OKEKE	2432

☐ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original
1	58	17	74	33	90	49	106								
2	59	18	75	34	91	50	107								
3	60	19	76	35	92										
4	61	20	77	36	93										
5	62	21	78	37	94										
6	63	22	79	38	95										
7	64	23	80	39	96										
8	65	24	81	40	97										
9	66	25	82	41	98										
10	67	26	83	42	99										
11	68	27	84	43	100										
12	69	28	85	44	101										
13	70	29	86	45	102										
14	71	30	87	46	103										
15	72	31	88	47	104										
16	73	32	89	48	105										

NONE (Assistant Examiner)	(Date)	**Total Claims Allowed:** 50	
/IZUNNA OKEKE/ Primary Examiner.Art Unit 2432 (Primary Examiner)	09/06/2014 (Date)	O.G. Print Claim(s) 93	O.G. Print Figure 1

U.S. Patent and Trademark Office Part of Paper No. 20140905

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**0
Number of independent claims previously paid for: 8
Number of independent claims in excess of 8: 0.
Number of total claims previously paid for: 26
Number of total claims in excess of 26: 0
3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**

SUMMARY OF EXAMINER INITIATED INTERVIEW

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: PClaw BankAcct, G/L: **6, 5010**
PCLAW BILLING REFERENCE: [SCOT0001]
Check#, Entry date, Amount: [1916, 9/19/13, 4320.00]
Service Fees: Amount/CreditAtty/Entry date/Services: []

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: ran
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 9-4-2014 **SIGNATURE**: /RichardNeifeld/
Printed: September 4, 2014 (5:17pm) RICHARD NEIFELD, REG. NO. 35,299
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\SummaryOfExaminerInitiatedInterview_SCOT0014-6_9-4-2014.wpd

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE

SUMMARY OF EXAMINER INITIATED INTERVIEW

On 9/2/2014, Examiner Okeke called the undersigned. Examiner Okeke indicated that he would allow the application, if the limitations of claim 62 were incorporated into independent claim 58, and similar limitations were added in to each of the other independent claims.

The undersigned ask Examiner Okeke whether he would allow the application if only the portion of claim 62 reading "wherein said memory scheduler code resource is designed to modify a stack frame" were incorporated into claim 58, and similar limitations incorporated into the other independent claims. A telecommunication problem terminated the telephone conference before Examiner Okeke could answer.

Several minutes later, the telephone conference continued. Examiner Okeke indicated that he would allow claim 58, if the limitation "wherein said memory scheduler code resource is designed to modify a stack frame" followed by the recitation "in said memory" were added to claim 58, and similar limitations were added in to each of the other independent claims. Examiner Okeke indicated that if the applicant agreed, the undersigned should email Examiner Okeke an amended set of claims in text format to facilitate the examiner entering the claims by an examiner amendment.

On 9/3/2014, the undersigned spoke with Examiner Okeke noting that the applicant had agreed to the proposed amendment and that the undersigned would email amended claims to the examiner.

On 9/4/2014, the undersigned emailed amended claims to the examiner.

/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

Date/Time: September 4, 2014 (5:17pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\SummaryOfExaminerInitiatedInterview_SCOT0014-6_9-4-2014.wpd

Electronic Acknowledgement Receipt

EFS ID:	20050821
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Richard A. Neifeld
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	04-SEP-2014
Filing Date:	24-JUL-2012
Time Stamp:	17:35:20
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Applicant summary of interview with examiner	SummaryOfExaminerInitiatedInterview_SCOT0014-6_9-4-2014.pdf	173164 1d670d74534a3c9c5c1d6c2b6c75c973637f88cd	no	2

Warnings:

Information:

Total Files Size (in bytes):	173164

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
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CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE

37 CFR 1.111 AMENDMENT REMARKS

This is a response to the Non Final Office Action (NF)A) dated 3/21/2014.

The Office Action summary page of the Non Final Office Action (NFOA) indicates that all claims, claims 58-107, are examined, that the action is NON final, and that all claims are rejected.

Items 1-2 indicate the restriction has been withdrawn. In response, the applicant appreciates that withdrawal.

Items 3-10 reject claims 82-107 for lack of a written description in the specification (112(a); item 10 rejects claims 82-107 as indefinite (112(b)); and items 11-12 reject claims 58-81 as anticipated by Ostrovsky. For the reasons explained below, the applicant traverses generally all of the rejections, with some minor clarifying amendments in view of the examiner's claim-specific comments.

Each of the examiner's comments is addressed below, in the order they appear in the office action. Citations are the paragraph of the specification of this application as published in the US patent application publication: 20130014271

TRAVERSE OF 112(a) REJECTIONS

Item 5 in the NFOA states that:

> 5. Claim 86 and 104 recites "to shuffle at least a subset of said code resources". There is no support or description in the specification for the claim language "shuffle at lease a subset". Claims 105-107 are rejected with the same rationale for their dependence on claim 104.

Claim 86 recites:

> 86. (Previously Presented) A system comprising:
> a processor;
> a memory coupled to said processor;
> wherein said memory is designed to store data in non transitory form;
> wherein said system is configured to load a program comprising code resources into said memory and *to shuffle at least a subset of said code resources* into a different location in said memory after said program has been loaded into memory.

In response, please note that paragraph [0054] explicitly mentions the concept of protecting a "sub-set" of code resources of a computer program, stating:

> [0054] The first method of the present invention described involves hiding necessary "parts" or code "resources" in digitized sample resources using a "digital watermarking" process, such as that described in the "Steganographic Method and Device" patent application. The basic premise for this scheme is that ***there are a certain sub-set of executable code resources, that comprise an application and that are "essential" to the proper function of the application***. ...

That concept of a sub-set of code resources is a generic disclosure applicable to the memory shuffling concept. It provides express support for claiming a "subset". In further response, please note paragraph [0066] which reads in relevant part, emphasis supplied:

[0066] Under the present invention, the application contains a special code resource which knows about all the other code resources in memory. During execution time, this ***special code resource, called a "memory scheduler***," can be called periodically, or at random or pseudo random intervals, at which time it intentionally shuffles ***the other code*** resources randomly in memory, so that someone trying to analyze snapshots of memory at various intervals cannot be sure if they are looking at the same code or organization from one "break" to the next. This adds significant complexity to their job. The scheduler also randomly relocates itself when it is finished. In order to do this, the scheduler would have to first copy itself to a new location, and then specifically modify the program counter and stack frame, so that it could then jump into the new copy of the scheduler, but return to the correct calling frame. Finally, the scheduler would need to maintain a list of all memory addresses which contain the address of the scheduler, and change them to reflect its new location.

The disclosure in paragraph [0054] that the memory schedule shuffles only "***the other code*** resources " provides support for recitation "to shuffle at least a subset of said code resources". This is because the memory schedule is also a code resource, and the emphasized disclosure does not indicate the memory shuffler code resource is shuffled a the specified "time". The specification does disclose that the "The scheduler also randomly relocates itself when it is finished." but that is in addition to the disclosure that "it intentionally shuffles ***the other code*** resources randomly in memory." Consequently, the specification does disclose that the shuffling of "at least a subset of said code resources." Anyone skilled in the art would understand this disclosure to mean what is claimed.

Item 6 in the NFOA states that:

6. Claims 82 and 87 recites "sequence of instructions that have sequential addresses". There is no support or description in the specification for the claim language "sequential addresses". Claim 83 is rejected with the same rationale for its dependence on claim 82. Claims 88 and 89 are rejected with the same rationale for their dependence on claim 87.

In response, please note paragraph [0052] which reads in relevant part, emphasis supplied:

[0052] The ***memory address*** of the first instruction in one of these ***sub-objects is called the "entry point"*** of the function or procedure. The rest of the instructions comprising that sub-object ***immediately follow*** from the entry point. Some systems may prefix information to the entry point which describes calling and return conventions for the code which follows, an example is the Apple Macintosh Operating System (MacOS). These sub-objects can be packaged into what are referred to in certain systems as "code resources," which may be stored separately from the application, or shared with other applications, although not necessarily. Within an application there are also data objects, which consist of some data to be operated on by the executable code. These data objects are not executable. That is, they do not consist of executable instructions. The data objects can be referred to in certain systems as "resources."

Claim 82 recites:

82. (Previously Presented) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that have sequential addresses in said memory.

The description that the rest of a code resource's instructions "***immediately follow*** from the" - ***memory address*** of the first instruction" clearly discloses that the "sequence of instructions that have sequential addresses." Anyone skilled in the art would understand this disclosure to mean what is claimed.

Item 7 in the NFOA states that:

7. Claims 84 and 90 recites "plurality of executable code resources loaded in said memory have memory addresses that immediately follow one another". There is no support or description in the specification for the claim language "memory addresses that immediately follow one another". Claim 85 is rejected with the same rationale for its dependence on claim 84. Claims 91 and 92 are rejected with the same rationale for their dependence on claim 90.

Claim 84 recites:

84. (Previously Presented) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that immediately follow one another in said memory.

In response, again please note paragraph [0052] which reads in relevant part, emphasis supplied:

[0052] The ***memory address*** of the first instruction in one of these ***sub-objects is called the "entry point"*** of the function or procedure. The rest of the instructions comprising that sub-object ***immediately follow*** from the entry point. Some systems may prefix information to the entry point which describes calling and return conventions for the code which follows, an example is the Apple Macintosh Operating System (MacOS). These sub-objects can be packaged into what are referred to in certain systems as "code resources," which may be stored separately from the application, or shared with other applications, although not necessarily. Within an application there are also data objects, which consist of some data to be operated on by the executable code. These data objects are not executable. That is, they do not consist of executable instructions. The data objects can be referred to in certain systems as "resources."

The claimed recitation "memory addresses that immediately follow one another" is almost *ipsus verbis* states in the specification. The specification recites:" ***memory address*** ...of the ... sub-object ***immediately follow*** from the entry point." Anyone skilled in the art would understand this disclosure to mean what is claimed.

Item 8 in the NFOA states that:

8. Claim 93 recites "configured to store in memory an original/new plurality of code resource addresses". There is no support or description in the specification for the claim language "store in memory an original/new plurality of

code resource addresses". Claims 94-103 are rejected with the same rationale for their dependence on claim 93.

Claim 93 reads:

> 93. (Previously Presented) A system for protecting a software application, said software application comprising a plurality of executable code resources, said system comprising:
>
> a processor;
>
> memory for storing data in non transitory form;
>
> wherein said processor is coupled to said memory for transmitting data there between;
>
> wherein, when executing said software application, said system is configured to store in said memory an original plurality of code resource addresses;
>
> wherein, when executing said software application, said system is configured to store a plurality of executable code resources for executing said software application;
>
> wherein each one of said original plurality of code resource addresses specifies an address in said memory of one of said plurality of executable code resources;
>
> wherein, when executing said software application, said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

In response, please first note paragraph [0066] in the specification, which reads, with emphasis supplied:

> [0066] Under the present invention, the application contains a special code resource which knows about all the other code resources in memory. During execution time, this special code resource, called a "memory scheduler," can be called periodically, or at random or pseudo random intervals, at which time it intentionally ***shuffles the other code resources randomly in memory***, so that someone trying to analyze snapshots of memory at various intervals cannot be sure if they are looking at the same code or organization from one "break" to the next. This adds significant complexity to their job. The scheduler also ***randomly relocates itself*** when it is finished. In order to do this, the scheduler would have to first copy itself to a new location, and then specifically modify the program counter and stack frame, so that it could then jump into the new copy of the scheduler, but return to the correct calling frame. Finally, the scheduler would need to maintain a list of all memory addresses which contain the address of the scheduler, and change them to reflect its new location.

In response, please note that claim 93 does not recite "configured to store in memory an original/new plurality of code resource addresses". Accordingly, the applicant (and the undersigned) does not know why these claims stand rejected for lack of written description.

In any case, the examiner should note that disclosure corresponding to the "shuffles" and

"relocate" claim recitation appears in paragraph [0066] emphasized above.

TRAVERSE OF 112(b) REJECTIONS

Item 10 in the NFOA states that:

> 10. Claims 82-107 are rejected under 35 U.S.C. 112(b) or 35 U.S.C. 112 (pre-AlA), second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which the inventor or a joint inventor, or for pre-AlA the applicant regards as the invention. As outlined above, the identified claim language/limitations are not recited or described in the disclosure. Therefore, the interpretation of the claims is indefinite and unclear. For instance, claim 86 recites "shuffle at least a subset of said code resources". In the absence of any disclosure describing what a "subset of a code resource" is, it is unclear and indefinite what the claimed subject matter is. The same rational (not supported by the specification) applies to the rest of the claims identified above.

In response, basis of 112(b) being a lack of 112(a) disclosure, please disclosure noted herein above in response to the 112(a) rejections.

In response to the specific reasoning directed to claim 86, first note that claim 86 reads as follows.

> 86. (Previously Presented) A system comprising:
> a processor;
> a memory coupled to said processor;
> wherein said memory is designed to store data in non transitory form;
> wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least ***a subset of said code resources*** into a different location in said memory after said program has been loaded into memory.

In response to the rejection of claim 86 for indefiniteness of a recitation "subset of a code resource", please note that claim 86 does not contain that recitation. Claim 86 recites "resources" in the plural, not the singular. Accordingly, there is no basis for this rejection insofar as it is based upon an incorrect reading of claim 86's recitation "resources".

In further response, it appears that the examiner has concludes that the word "subset" is unclear. However, "subset" has an exact definition. See for example the published definition of our National Institute of Science and Technology (NIST) at URL "http://xlinux.nist.gov/dads//HTML/subset.html". This definition reads:

> Definition: A set S1 is a subset of another set S2 if every element in S1 is in S2. S2 may have exactly the same elements as S1.

Hence, claim 86's "***subset of said code resources***" defines a set consisting of less than all, or all, of the code resources. There is no ambiguity in what constitutes a subset of code resources based upon the meaning of the word "subset".

In yet further response, it may be that the examiner considers "code resources" to be unclear. If so, the examiner's attention is directed to the background information in paragraphs [0013] ("using the watermarks encoded with such keys to hide an essential subset of the

application code resources.); [0033] ("Attempts to tamper or "patch" substitute code resources can be made highly difficult by randomizing the location of said resources in memory on an intermittent basis to resist most attacks at disabling the system."); and then to paragraph [0052] which defines "code resources":

> [0052] The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. The rest of the instructions comprising that sub-object immediately follow from the entry point. Some systems may prefix information to the entry point which describes calling and return conventions for the code which follows, an example is the Apple Macintosh Operating System (MacOS). ***These sub-objects can be packaged into what are referred to in certain systems as "code resources***," which may be stored separately from the application, or shared with other applications, although not necessarily. Within an application there are also data objects, which consist of some data to be operated on by the executable code. These data objects are not executable. That is, they do not consist of executable instructions. The data objects can be referred to in certain systems as "resources."

Hence, the specification makes the concept of code resources not indefinite. In summary, one skilled in the art would have no problem understanding the meaning and scope of claim 86.

PRIOR ART ANTICIPATION REJECTIONS BASED UPON OSTROVSKY (5123045)

The applicant appreciates that claims 82-107 are not rejected based upon prior art.

The NFOA dated 3/21/2014 repeats verbatim the 102 anticipation rejections of claims 58-81 appearing in the NFOA dated 3/28/2013. Accordingly, the applicant copies herein below the remarks responding to that rejection filed 9/23/2103. However, for clarity, the applicant first summarizes why each independent claim is not anticipated by Ostrovsky.

Regarding independent claim 58, our specification defines a “code resource” as having an entry point and comprises more than one instruction that immediately follows from that entry point. Ostrovsky does not disclose making a random order or arrangement of sets of instructions each set of which immediately follow from its entry point. Nothing in Ostrovsky suggests that function.

Similarly, claim 67 recites "wherein said memory scheduler is designed to shuffle said other executable code resources in memory."

Similarly, claim 75 recites "designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application."

Similarly, claim 78 recites "shuffle at least one code resource into a different location in said memory after it has been loaded into memory."

Similarly, claim 79 recites " to randomize the location of at least one of the at least two code resources in the memory."

Similarly, claim 80 recites "configured to randomize the location of at least a portion of the code defining a code resource."

Similarly, claim 93 recites “said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.”

Similarly, claim 104 recites "shuffle locations in memory of at least a subset of said

plurality of code resources." Ostrovsky does not disclose shuffling code resources in memory, does not disclose a memory scheduler stored in memory to perform that function and does not disclose the limitations recited above.

OSTROVSKY DOES NOT DISCLOSE WHAT IS CLAIMED

Ostrovsky's goal is to "provide[] protection of the pattern of access to memory during execution of a program." Abstract. Ostrovsky accomplishes that goal by providing a system in which "values stored in the general purpose memory are hidden ... [and] the sequence in which memory location are accessed during execution is hidden." Abstract.

Ostrovsky discloses a data processing system including protected CPU and protected memory (Fig. 1, elements 10, 12; col. 1 lines 39-47) which communicates over unprotected data bus 20 with unprotected random access memory (RAM) (fig. 1, element 14; col. 4 lines 53-57). Ostrovsky discloses that "The data processing system further includes a plurality of 'buffer' data structures for storing encrypted software and data in an unprotected memory." Col. 1 lines 44-47. This refers to data structures in the unprotected RAM, element 14. Col. 4 lines 60-61.

Ostrovsky discloses storing values defining a program in a random order (as generated by a pseudo random number generator) in unprotected RAM. Ostrovsky states "The method of protection comprises initially permuting the order in which values are stored in the unprotected addressable locations prior to the beginning of execution of the program." Col. 2 lines 21-24. These values include the data and instructions of the software program. Ostrovsky states "The types of protection embodied with the preferred embodiment include protection of the access pattern to memory by the program and protection of *values held in memory such as data and instructions*." Col. 4 lines 15-18.

Ostrovsky defines a virtual address as follows: "A virtual address is the address referenced by the program and is distinct from a physical address which identifies the physical location where the virtual memory location resides." Col. 6 lines 23-27. Ostrovsky states "The virtual memory locations that make up the program and the data are stored in physical addresses specified by a pseudo-random function of the virtual addresses." Col. 3 lines 3-6. In the Detailed Description, Ostrovsky clarifies that "The code and data constitute a plurality of virtual memory locations. They are not stored in the order in which their corresponding virtual addresses are sequenced; rather, they are stored in a pseudo-random sequence." Col. 6 lines 33-36.

STORING OF *PROGRAM VALUES* IN "A PSEUDO-RANDOM SEQUENCE"

Ostrovsky's storing of *program values* in "a pseudo-random sequence" in the unprotected RAM does not disclose our claimed shuffling of *code resources* in memory. This is because our application discloses that a "code resource" has an entry point and comprises more than one instruction that immediately follows from that entry point. Our specification states that "[0052] The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. **The rest of the instructions comprising that sub-object *immediately follow from the entry point*.** ... These sub-objects can be packaged into what are referred to in certain systems as **'code resources'**,...".

Ostrovsky discloses compiling and executing the program. Ostrovsky states that "[t]he present invention specifies a cryptographic compiler which transforms source-code programs into equivalent compiled programs which run on a general-purpose computer using the protected chip." Col. 1 lines 58-62. And that it is this cryptographically compiled program that resides in the unprotected RAM. "It should be stressed that ... the compiled programs reside in the unprotected, general-purpose random access memory." Col. 2 lines 5-8. Regarding executing

the program, Ostrovsky discloses that "the values at the unprotected adjustable locations are accessed in light of the order imposed by the permuting step and by the partially permuting step." Col. 2 lines 30-33.

Ostrovsky then discloses that, because of the permutation, "[s]pecific accesses are achieved within an access pattern which is independent of the original access pattern." Col. 2 lines 33-35. However, Ostrovsky discloses that those accesses of unprotected RAM for executing the software program are interspersed with accesses of unprotected RAM to dummy addresses that do not contain a program instruction or data, in order to fool an adversary. Ostrovsky states "Next, execution of the program is begun. Each time a memory access is desired, the CPU 10 examines each of the buffers for the memory location that is sought. *Only one of the buffers will have the memory location truly sought*. The other memory accesses are dummy accesses designed to fool adversaries." Col. 5 lines 38-43.

MOVING A SINGLE VALUE OF THE SOFTWARE PROGRAM TO A DIFFERENT MEMORY LOCATION

Ostrovsky discloses moving the "desired memory location" that is "truly sought" for "execution of the program," once it is identified. Ostrovsky states "Next, execution of the program is begun. Each time a memory access is desired, the CPU 10 examines each of the buffers for the memory location that is sought. Only one of the buffers will have the memory location truly sought. The other memory accesses are dummy accesses designed to fool adversaries. *When a desired memory location is found, it is moved up to the level 1 buffer*." Col. 5 lines 38-44. This passage in Ostrovsky clarifies that individual memory locations, which correspond to an individual machine instructions of data associated with the software program, are moved, *one at a time*, independent of movement of any other instruction or data associated with the memory. Furthermore, Ostrovsky discloses that sequential values of the program found in the foregoing process and stored in buffer 1 are not stored in sequential locations in buffer 1. Instead, Ostrovsky discloses that each found value of the program is stored in the next sequential empty bucket in buffer 1, and that buckets are not a single memory location but a set or sequential locations. "Store value v of the virtual memory location L from B into first empty bucket of level 1 buffer" and that "each bucket constitutes a set of memory locations". Col. 9 lines 37-38. Thus, Ostrovsky discloses that sequentially accessed instructions in the compiled software program are not stored in the unprotected RAM in sequential memory addresses.

Moreover, Ostrovsky discloses that when a buffer becomes full, the contents of that buffer are transferred to another buffer and then "obliviously rehashed" which means pseudo randomly reordering the contents of the memory locations containing each value of the program. Col. 9 lines 1-11.

Ostrovsky's moving of single found value of the software program, independent of other values of the software program, in the unprotected RAM, does not disclose our claimed shuffling of code resources in memory. This is because our application discloses that a "code resource" has an entry point and comprises more than one instruction that immediately follows from that entry point. Our specification states that "[0052] The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. **The rest of the instructions comprising that sub-object immediately follow from the entry point**. ... These sub-objects can be packaged into what are referred to in certain systems as '**code resources**',...". Ostrovsky's moving values one at a time to non sequential locations in memory is not our claimed shuffling of a code resource because it does not maintain a sequence of instructions that **immediately follow from the entry point**.

PARTIALLY PERMUTING THE ORDER OF SOFTWARE PROGRAM VALUES

Ostrovsky discloses "the order in which values are stored in subsets of the unprotected adjustable locations is partially permuted at various times during the execution of the program." Col. 2 lines 27-30. Regarding the partial permutation, Ostrovsky states "The partial permuting step includes the step of transferring values from one subset of the unprotected adjustable locations to another subset of the unprotected adjustable locations," (col. 2 lines 42-44) and "Given that each subset may be of a different size, the frequency with which the various subsets are permuted differs" (col. 2 lines 48-49) and "Each time the accessible memory is shuffled (permuted), a counter is incremented" (col. 3 lines 48-49) and "All the non-empty buckets are moved together by obliviously repermuting B at the bucket level" (col. lines 53-54) and "As fixed intervals (i.e. when a buffer may be potentially full) the buffer's contents are moved to a next higher priority buffer and are pseudo-randomly rearranged so that the order in which the virtual memory locations are held in the next higher priority buffer is shuffled. The movement from the lower order buffer to a higher order buffer is achieved preferably using a pseudo-random function" (col. 3 line 16-24) and finally "Thus, if the second level buffer 17 gets full, it is passed on to the third level buffer 19, and the third level buffer 19 is shuffled" (col. 9 lines 8-10). In the foregoing, Ostrovsky discloses partially permuting meaning transferring of values from one buffer (or bucket, which is a subset of a buffer) to another buffer (or bucket, which is a subset of a buffer). However, Ostrovsky discloses that each buffer or bucket subset of a buffer contains a disordered subset of the values (code and instruction) defining the software program. Further, Ostrovsky fails to disclose any relation between buckets and what we define as code resources, and Ostrovsky fails to disclose any relation between buffers and what we define as code resources. Consequently, Ostrovsky's moving contents of one buffer or bucket to another, moves a disordered set of the software program's instructions from one location to another. There is no entry point immediately followed by the other values required to execute a code resource. Even of a bucket or buffer happened to have all instructions for some code resources, the ordered movement of those bucket or buffer values to another bucket or buffer would not result in a shuffle of the order of the corresponding code resources. **Therefore, Ostrovsky's moving the contents of one buffer to another, or of one bucket to another, does not correspond to our claimed shuffling of code resources.**

Our application discloses that a "code resource" has an entry point and comprises more than one instruction that immediately follows from that entry point. Our specification states that "[0052] The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. **The rest of the instructions comprising that sub-object immediately follow from the entry point**. ... These sub-objects can be packaged into what are referred to in certain systems as '**code resources**',...". **Ostrovsky's shuffling of buffers or buckets does not relocate in memory, preserving the sequence in memory, of an entry point of a function or procedure and instructions comprising that sub-object immediately following the entry point**.

OSTROVSKY'S PERMUTING, PARTIALLY PERMUTING, AND MOVING OF VALUES OF A SOFTWARE PROGRAM DOES NOT DISCLOSE WHAT WE CLAIM

In summary, Ostrovsky's initial permuting is limited to permuting of values, "permuting the order in which values are stored". Ostrovsky's relocating of an instruction to buffer 1 does not reorder instructions in sequence because they are move to sequential buckets, not sequential memory locations. Ostrovsky's partially permuting merely shifts values of one buffer to another, en masse, and that moving is not shuffling of a code resource as defined in our specification because the buffers do not include an entry point followed immediately by the instructions

defining a subroutine or procedure.

Ostrovsky discloses more details, but these details are not relevant. For example, Ostrovsky discloses encryption by the CPU, association by the CPU of a virtual address with each value of the program/data, and storing that virtual address in association with the corresponding value in unprotected RAM. Ostrovsky discloses plural buffers and buckets intended to confuse an adversary by making calls to RAM unrelated to execution of the program, and shuffling the buffers and buckets to confuse an adversary.

"CODE RESOURCES" DISCLOSURE

Paragraphs [0051] and [0052] from US patent publication 20130014271 of application 13556420, the subject application, are copied below, with more relevant passages emphasized, for the examiner's convenience.

> [0051] An executable computer program is variously referred to as an application, from the point of view of a user, or executable object code from the point of view of the engineer. A collection of smaller, atomic (or indivisible) chunks of object code typically comprise the complete executable object code or application which may also require the presence of certain data resources. These indivisible portions of object code correspond with the programmers' function or procedure implementations in higher level languages, such as C or Pascal. In creating an application, a programmer writes "code" in a higher level language, which is then compiled down into "machine language," or, the executable object code, which can actually be run by a computer, general purpose or otherwise. Each function, or procedure, written in the programming language, represents a self-contained portion of the larger program, and implements, typically, a very small piece of its functionality. The order in which the programmer types the code for the various functions or procedures, and the distribution of and arrangement of these implementations in various files which hold them is unimportant. Within a function or procedure, however, the order of individual language constructs, which correspond to particular machine instructions is important, and so functions or procedures are considered indivisible for purposes of this discussion. **That is, once a function or procedure is compiled, the order of the machine instructions which comprise the executable object code of the function is important and their order in the computer memory is of vital importance**. Note that many "compilers" perform "optimizations" within functions or procedures, which determine, on a limited scale, if there is a better arrangement for executable instructions which is more efficient than that constructed by the programmer, but does not change the result of the function or procedure**. Once these optimizations are performed, however, making random changes to the order of instructions is very likely to "break" the function. When a program is compiled, then, it consists of a collection of these sub-objects,** whose exact order or arrangement in memory is not important, so long as any sub-object which uses another sub-object knows where in memory it can be found.
>
> [0052] ***The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. The rest of the instructions comprising that sub-object immediately follow from the entry point***. Some systems may prefix information to the entry point which describes calling

and return conventions for the code which follows, an example is the Apple Macintosh Operating System (MacOS). ***These sub-objects can be packaged into what are referred to in certain systems as "code resources,"*** which may be stored separately from the application, or shared with other applications, although not necessarily. Within an application there are also data objects, which consist of some data to be operated on by the executable code. These data objects are not executable. That is, they do not consist of executable instructions. The data objects can be referred to in certain systems as "resources."

INDEPENDENT CLAIM 58

58. (New) A computing device for running application software, comprising:
memory designed to store data in non transitory form;
an operating system;
wherein said memory stores an application software;
wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;
wherein said application software is designed to call said memory scheduler code resource;
wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory.

Shuffle means to "make a random order of arrangement." or "A rearrangement of elements, where none are lost, added, or changed" See for example: http://education.yahoo.com/reference/dictionary/entry/shuffle attachment 1 [submitted with prior response], verb definition 5.
Ostrovsky does not disclose shuffling code resources in memory; and http://xlinux.nist.gov/dads//HTML/permutation.html attachment 2 [submitted with prior response].

As noted above, our specification defines a "code resource" as having an entry point and comprises more than one instruction that immediately follows from that entry point. Ostrovsky does not disclose making a random order or arrangement of sets of instructions each set of which immediately follow from its entry point. Nothing in Ostrovsky suggests that function. Accordingly, Ostrovsky neither discloses nor suggests claim 58.

INDEPENDENT CLAIMS: 67; 75; 78; 79; 80 93; and 104

Regarding the other independent claims: claim 75 recites "designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application"; claim 78 recites " shuffle at least one code resource into a different location in said memory after it has been loaded into memory"; claim 79 recites " to randomize the location of at least one of the at least two code resources in the memory "; claim 80 recites "configured to randomize the location of at least a portion of the code defining a code resource"; and claim 93 recites "said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses" and claim 104 recites "shuffle locations in memory of at least a subset of said plurality of code resources". These claims are not anticipated and are not obvious based upon Ostrovsky

for at least the foregoing reasons.

DEPENDENT CLAIMS

Dependent claims such as 87 and 88 clarify the code resources comprise a sequence of instructions that have sequential addresses and define functions or procedures, support for which was discussed above.

Allowance of this application, as amended, is requested.

/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

Date/Time: April 23, 2014 (11:53am)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\Amendment_SCOT0014-6_4-22-2014.wpd

IN THE CLAIMS

1-57. (Canceled)

58. (Currently Amended) A computing device for running application software, comprising:

memory designed to store data in non transitory form;

an operating system;

wherein said memory stores an application software;

wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;

wherein said application software is designed to call said memory scheduler code resource; and

wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory.

59. (Previously Presented) The system of claim 75 wherein said first one of said plurality of executable code resources is a memory scheduler code resource, wherein, when said memory scheduler code resource is called, said memory scheduler code resource functions to shuffle said memory scheduler code resource randomly in memory.

60. (Previously Presented) The device of claim 58 further comprising a program counter designed to store values and wherein said system is designed to enable application software to modify a value stored by said program counter.

61. (Previously Presented) The device of claim 58 further comprising a program counter designed to store values and wherein said memory scheduler code resource is designed to modify a value stored by said program counter.

62. (Previously Presented) The device of claim 58 wherein said memory scheduler code resource is designed to modify a stack frame, to modify a calling address, and to copy itself to a memory location associated with said calling address.

63. (Previously Presented) The device of claim 58 wherein said system comprises an operating system, and wherein said operating system is designed to call said memory scheduler code resource.

64. (Previously Presented) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in memory, wherein each one of said addresses is associated with one of the code resources.

65. (Previously Presented) The device of claim 58 wherein said memory scheduler

code resource maintains a list of addresses in memory associated with said other code resources in memory.

66. (Previously Presented) The device of claim 58 further comprising a processor for processing instructions defined by said application software.

67. (Previously Presented) A system for executing application software code, comprising:

a memory designed to store data in non transitory form, and storing executable code resources;

wherein said executable code resources comprise a memory scheduler and other executable code resources; and

wherein said memory scheduler is designed to shuffle said other executable code resources in memory.

68. (Currently Amended) The system of claim 67, wherein said memory scheduler is designed to shuffle said other executable code resources randomly in said memory.

69. (Currently Amended) The system of claim 67 wherein said memory scheduler is designed to shuffle said other executable code resources in memory such that the shuffling results in a change in location in said memory of at least one of said other executable code resources.

70. (Previously Presented) The system of claim 67 further comprising a program counter designed to store values and wherein said memory scheduler is designed to modify values stored by said program counter.

71. (Previously Presented) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of at least one code resource.

72. (Previously Presented) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of said other executable code resources.

73. (Previously Presented) The system of claim 67 designed to call said memory scheduler.

74. (Previously Presented) The system of claim 67 wherein said application software code is designed to call said memory scheduler.

75. (Previously Presented) A system for executing an application on a computer system, comprising:

a computer system comprising a processor for processing instructions and memory designed to store data in non transitory form;

a software application comprising a plurality of executable code resources loaded in said

memory;

said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources; and

wherein a first one of said plurality of executable code resources is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor.

76. (Previously Presented) The system of claim 75 wherein said first one is also designed to relocate itself to a different address during execution of said software application.

77. (Previously Presented) The system of claim 75 wherein said system is designed to repeatedly relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application.

78. (Currently Amended) A system comprising:

a processor;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory; and

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least one code resource into a different location in said memory after said program has been loaded into memory.

79. (Currently Amended) A system comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory; and

wherein said system is configured to load an executable program comprising at least two code resources into memory and to randomize the location of at least one of the at least two code resources in the memory, using said processor

80. (Currently Amended) A system for executing a program comprising code, comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory; and

a memory scheduler configured to randomize location of code defining a code resource, in memory, using said processor.

81. (Previously Presented) The system of claim 80 wherein said memory scheduler is

configured to repeatedly randomize the location of at least a portion of the code in memory.

82. (Previously Presented) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that have sequential addresses in said memory.

83. (Previously Presented) The device of claim 82 wherein each one of said other code resources defines a function or procedure.

84. (Previously Presented) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that immediately follow one another in said memory.

85. (Previously Presented) The device of claim 84 wherein each one of said other code resources defines a function or procedure.

86. (Currently Amended) A system comprising:

a processor;

a memory coupled to said processor;

wherein said memory is designed to store data in non transitory form; and

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least a subset of said code resources into a different location in said memory after said program has been loaded into memory.

87. (Previously Presented) The system of claim 75 wherein each one of said plurality of executable code resources loaded in said memory comprises a sequence of instructions that have sequential addresses in said memory.

88. (Previously Presented) The system of claim 87 wherein each one of said plurality of executable code resources loaded in said memory defines a function or procedure.

89. (Previously Presented) The system of claim 88 wherein each one of said plurality of executable code resources loaded in said memory is an essential code resource.

90. (Previously Presented) The system of claim 75 wherein each one of plurality of executable code resources loaded in said memory comprises a sequence of instructions, and the sequence of instructions for one of said plurality of executable code resources loaded in said memory have memory addresses that immediately follow one another.

91. (Previously Presented) The system of claim 90 wherein each one of said plurality of executable code resources loaded in said memory defines a function or procedure.

92. (Previously Presented) The system of claim 90 wherein said system comprises one of a set-top box; a cellular phone; a smart television; and a personal digital assistant (PDA).

93. (Currently Amended) A system for protecting a software application, said software

application comprising a plurality of executable code resources, said system comprising:

a processor;

memory for storing data in non transitory form;

wherein said processor is coupled to said memory for transmitting data there between;

wherein, when executing said software application, said system is configured to store in said memory an original plurality of code resource addresses;

wherein, when executing said software application, said system is configured to store a plurality of executable code resources for executing said software application;

wherein each one of said original plurality of code resource addresses specifies an address in said memory of one of said plurality of executable code resources; and

wherein, when executing said software application, said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

94. (Previously Presented) The system of claim 93, wherein said system is configured to repeatedly (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

95. (Previously Presented) The system of claim 93, wherein each one of said plurality of executable code resources are relocated to random addresses.

96. (Previously Presented) The system of claim 93, wherein each one said original plurality of code resource addresses specifies an entry point of an original location in memory of each one of said plurality of executable code resources.

97. (Previously Presented) The system of claim 93, wherein each one said new plurality of code resource addresses specifies an entry point of a new location in memory of each one of said plurality of executable code resources.

98. (Previously Presented) The system of claim 93 wherein each one of said plurality of executable code resources is stored in sequential locations in said memory.

99. (Previously Presented) The system of claim 98 wherein each one of said plurality of executable code resources executes a function or procedure when called.

100. (Previously Presented) The system of claim 93 wherein each one of said

plurality of executable code resources executes a function or procedure

101. (Previously Presented) The system of claim 93 wherein said system is configured to relocate each one of said plurality of executable code resources in said memory in response to a call to an executable code resource of said plurality of executable code resources.

102. (Currently Amended) The system of claim 101 wherein said system is configured to update a reference to said executable code resource in said memory ~~of the computer system~~ to reflect a new address in said memory of said executable code resource resulting from relocating said executable code resource.

103. (Previously Presented) The system of claim 102 wherein said executable code resource is a memory scheduler which is called periodically, or at random or pseudo random intervals.

104. (Currently Amended) A computer program product storing in non-transitory form instructions defining a software program, which instructions, when loaded into a computer system instruct the computer system to perform the following steps:

load said program into memory of said computer system;

wherein said program, as loaded into memory, comprises a plurality of code resources; and

shuffle locations in memory of at least a subset of said plurality of code resources.

105. (Previously Presented) The product of claim 104 wherein each one of said subset of said code resources comprises a sequence of instructions that have sequential addresses in said memory.

106. (Previously Presented) The product of claim 104 wherein each one of said subset of said code resources defines a function or procedure.

107. (Previously Presented) The product of claim 104 wherein said subset comprises at least two code resources.

Date/Time: April 23, 2014 (11:53am)
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Electronic Patent Application Fee Transmittal

Application Number:	13556420
Filing Date:	24-Jul-2012
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Filer:	Robert Valerian Mihail
Attorney Docket Number:	SCOT0014-6

Filed as Large Entity

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 1 month with $0 paid	1251	1	200	200

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Miscellaneous:				
		Total in USD ($)		**200**

Electronic Acknowledgement Receipt	
EFS ID:	19572926
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Robert Valerian Mihail
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	14-JUL-2014
Filing Date:	24-JUL-2012
Time Stamp:	16:16:19
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	$200
RAM confirmation Number	3095
Deposit Account	502106
Authorized User	NEIFELD, RICHARD ALAN
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows: Charge any Additional Fees required under 37 C.F.R. Section 1.17 (Patent application and reexamination processing fees) Charge any Additional Fees required under 37 C.F.R. Section 1.21 (Miscellaneous fees and charges)	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Extension of Time	Petition_1_Month_EOT.pdf	187822 f6de3f102c656fdb92c67f5996d75f1ee71ccc30	no	2
Warnings:					
Information:					
2		Amendment_SCOT0014-6_4-22-2014.pdf	128297 f8371869ec7249f4d12d42815b609fd54d325c22	yes	20

Multipart Description/PDF files in .zip description

Document Description	Start	End
Transmittal Letter	1	1
Amendment/Req. Reconsideration-After Non-Final Reject	2	14
Claims	15	20

Warnings:					
Information:					
3	Fee Worksheet (SB06)	fee-info.pdf	30107 f3fdc5532a8ee26c14dcac19b69840a8d730cd72	no	2
Warnings:					
Information:					
		Total Files Size (in bytes):	346226		

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

PTO/SB/22 (03-13)
Approved for use through 3/31/2013. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

PETITION FOR EXTENSION OF TIME UNDER 37 CFR 1.136(a)	Docket Number (Optional) SCOT0014-6
Application Number 13/556,420	Filed 7/24/2012
For Data Protection Method and Device	
Art Unit 2432	Examiner OKEKE

This is a request under the provisions of 37 CFR 1.136(a) to extend the period for filing a reply in the above-identified application.

The requested extension and fee are as follows (check time period desired and enter the appropriate fee below):

		Fee	Small Entity Fee	Micro Entity Fee	
☑	One month (37 CFR 1.17(a)(1))	$200	$100	$50	$ 200
☐	Two months (37 CFR 1.17(a)(2))	$600	$300	$150	$
☐	Three months (37 CFR 1.17(a)(3))	$1,400	$700	$350	$
☐	Four months (37 CFR 1.17(a)(4))	$2,200	$1,100	$550	$
☐	Five months (37 CFR 1.17(a)(5))	$3,000	$1,500	$750	$

☐ Applicant asserts small entity status. See 37 CFR 1.27.

☐ Applicant certifies micro entity status. See 37 CFR 1.29.
Form PTO/SB/15A or B or equivalent must either be enclosed or have been submitted previously.

☐ A check in the amount of the fee is enclosed.

☐ Payment by credit card. Form PTO-2038 is attached.

☐ The Director has already been authorized to charge fees in this application to a Deposit Account.

☑ The Director is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 50-2106.

☑ Payment made via EFS-Web.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

I am the

☐ applicant/inventor.

☐ assignee of record of the entire interest. See 37 CFR 3.71. 37 CFR 3.73(b) statement is enclosed (Form PTO/SB/96).

☑ attorney or agent of record. Registration number 35,299.

☐ attorney or agent acting under 37 CFR 1.34. Registration number _____.

/RichardNeifeld/	7-14-2014
Signature	Date
Richard Neifeld	703-415-0012
Typed or printed name	Telephone Number

NOTE: This form must be signed in accordance with 37 CFR 1.33. See 37 CFR 1.4 for signature requirements and certifications. Submit multiple forms if more than one signature is required, see below*.

☑ * Total of 1 forms are submitted.

Privacy Act Statement

The **Privacy Act of 1974 (P.L. 93-579)** requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (*i.e.*, GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**0
Number of independent claims previously paid for: 8
Number of independent claims in excess of 8: 0.
Number of total claims previously paid for: 26
Number of total claims in excess of 26: 0
3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
37 CFR 1.111 AMENDMENT - REMARKS
37 CFR 1.111 AMENDMENT - CLAIMS

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: PClaw BankAcct, G/L: **6, 5010**
PCLAW BILLING REFERENCE: [SCOT0001]
Check#, Entry date, Amount: [1916, 9/19/13, 4320.00]
Service Fees: Amount/CreditAtty/Entry date/Services: []

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: ran
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 1-23-2014 **SIGNATURE**: /RichardNeifeld/
Printed: April 23, 2014 (11:53am) RICHARD NEIFELD, REG. NO. 35,299
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\Amendment_SCOT0014-6_4-22-2014.wpd

PTO/SB/06 (09-11)
Approved for use through 1/31/2014. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 13/556,420	Filing Date 07/24/2012	☐ To be Mailed

ENTITY: ☒ LARGE ☐ SMALL ☐ MICRO

APPLICATION AS FILED – PART I

FOR	(Column 1) NUMBER FILED	(Column 2) NUMBER EXTRA	RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).			
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))				
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL	

APPLICATION AS AMENDED – PART II

AMENDMENT 07/14/2014

	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
Total (37 CFR 1.16(i))	* 49	Minus	** 50	= 0	X $80 =	0
Independent (37 CFR 1.16(h))	* 7	Minus	***9	= 0	X $420 =	0
☐ Application Size Fee (37 CFR 1.16(s))						
☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
					TOTAL ADD'L FEE	0

AMENDMENT

	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =	
Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =	
☐ Application Size Fee (37 CFR 1.16(s))						
☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
					TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

LIE
/DIANE JOHNSON/

PTO/SB/06 (09-11)
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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 13/556,420	Filing Date 07/24/2012	☐ To be Mailed

ENTITY: ☒ LARGE ☐ SMALL ☐ MICRO

APPLICATION AS FILED – PART I

	(Column 1)	(Column 2)		
FOR	NUMBER FILED	NUMBER EXTRA	RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).			
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))				
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL	

APPLICATION AS AMENDED – PART II

AMENDMENT	07/14/2014	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	* 50	Minus	** 50	= 0	X $80 =	0
	Independent (37 CFR 1.16(h))	* 9	Minus	***9	= 0	X $420 =	0
	☐ Application Size Fee (37 CFR 1.16(s))						
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
						TOTAL ADD'L FEE	0

AMENDMENT		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =	
	☐ Application Size Fee (37 CFR 1.16(s))						
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
						TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

LIE
/EVELYN NIMMONS/

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13/556,420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: MOSKOWITZ, Scott
Title: DATA PROTECTION METHOD AND DEVICE
Examiner/ArtUnit: OKEKE, Izunna/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data:
This application is a Continuation of 11/895,388 filed 08-24-2007
11/895,388 is a Division of 10/602,777 filed 06-25-2003 Patented 7,664,263
10/602,777 is a continuation of 09/046,627 filed 03-24-1998 Patented 6,598,162

37 CFR 1.97 INFORMATION DISCLOSURE STATEMENT

This application is:
____ within 3 months of the US or 371 national stage filing date;
____ before first action on the merits (no fee required);
XXX after first action on the merits and before final action (1.17(P) fee required);
____ after final action;
____ after notice of allowance and before payment of the issue fee; or
____ after payment of the issue fee.

XXX The applicant is paying herewith the fee for obtaining consideration of an IDS filed after a first action on the merits.

IDENTIFICATION OF REFERENCES CITED IN APPLICATIONS TO WHICH 13/556,420 CLAIMS CONTINUING STATUS

REGARDING CITED REFERENCES

This IDS is an attempt to compile all references previously cited in Scott Moskowitz's cases. Upon compilation, some of the reference citations were vague, and some were to filed patent applications instead of published documents. This IDS attempts to account for each item to provide all citations to the examiner.

References previously cited and considered by the examiner in application13/556,420 (SCOT0014-6) are identified by placement of an "X" in the far right column.

CITED US PATENTS AND US PATENT APPLICATION PUBLICATIONS

Most pending Scott Moskowitz cases claim 35 USC 120 priority to prior cases containing a large number of cited US patents and published US applications. The citations list herein should incorporate all of those documents and may incorporate any additional documents found in other patent applications in patent families not linked by 35 USC 120 to this application. Since no US patent or US published applications need to be filed in order for the examiner to

consider citations thereto; the applicant may attempt to correlate the US patents and publications cited herein to those already of record due to citations in applications to which this application claims priority, if the examiner so requests.

FOREIGN PATENT REFERENCES

The IDS cites foreign patent references identified herewith as F001- F029 .

The table below identifies F references cited in this application or an application to which this application claims 35 USC 120 priority.

DOCKET NO	APPLICATION NUMBER	CITED F REFERENCES
SCOT0014-6	13/556,420	F01-F029

Accordingly, the following F references are not yet of record and are submitted herewith: NONE

NON PATENT LITERATURE REFERENCES

The IDS cites foreign patent references identified herewith as L001- L231 .

The table below identifies L references cited in this application or in an application to which this application claims 35 USC 120 priority.

DOCKET NO	APPLICATION NUMBER	CITED L REFERENCES
SCOT0014-6	13/556,420	L1-L212 & L218
	L reference citations of patent applications as filed for which a subsequent publication of the application is identified and cited herein.	L1-35, L101; L104-L105; L185-L202
	L reference citation numbers that have no associated citation; original citation was a duplicate of some other citation.	L98, L100, L102-103, L106-L114, L116-L154

References previously cited, applications for which a subsequent publication is cited, and reference numbers having no associated reference: L01-L212 & L218

Accordingly, the following L references are not yet of record and are submitted herewith: L213-L231

MASTER LIST OF RELATED CASES IN WHICH THE SAME INFORMATION MAY BE CITED

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0010-4	11/599,838	11/15/2006	10/15/2010 JRE
SCOT0010-5	11/899,662	9/7/2007	10/15/2010 JRE
SCOT0010-6	10/369,344	2/18/2003	08/1/2011 JRE
SCOT0010-7	11/482,654	7/7/2006	08/1/2011 JRE
SCOT0010-8	12/215,812	6/30/2008	10/15/2010 JRE
SCOT0010-10	12/901,568	10/10/2010	11/4/2010 JRE
SCOT0010-11	11/497,822	8/2/2006	08/1/2011 JRE
SCOT0010-12	12/217,834	7/9/2008	11/8/2010 JRE
SCOT0010-13	11/897,790	8/31/2007	08/1/2011 JRE
SCOT0010-14	12/462,799	8/10/2009	12/15/2010 JRE
SCOT0010-16	11/899,661	9/7/2007	08/1/2011 JRE
SCOT0010-17	12/590,681	11/19/2009	12/15/2010 JRE
SCOT0010-18	11/897,791	8/31/2007	08/1/2011 JRE
SCOT0010-19	12/590,553	11/10/2009	08/1/2011 JRE
SCOT0010-20	12/592,331	11/23/2009	08/1/2011 JRE
SCOT0010-21	11/599,964	11/15/2006	08/1/2011 JRE
SCOT0010-22	13/212,264	8/18/2011	1/11/2012 JRE
SCOT0011-1	08/674,726	7/2/1996	08/1/2011 JRE
SCOT0011-2	09/545,589	4/7/2000	1/11/2012 JRE
SCOT0011-3	11/244,213	10/5/2005	1/11/2012 JRE
SCOT0011-4	12/009,914	1/23/2008	10/15/2010 JRE
SCOT0011-5	12/005,230	12/26/2007	10/15/2010 JRE
SCOT0011-6	12/803,168	6/21/2010	10/15/2010 JRE
SCOT0011-7	11/649,026	1/3/2007	08/1/2011 JRE

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0011-8	12/803,194	06/21/2010	10/15/2010 JRE
SCOT0011-9	12/892,900	9/28/2010	11/8/2010 JRE
SCOT0012-1	08/489,172	6/7/1995	08/1/2011 JRE
SCOT0012-2	08/775,216	12/31/1996	01/11/2011 JRE
SCOT0012-3	08/999,766	7/23/1997	10/15/2010 JRE
SCOT0012-4	11/894,476	8/21/2007	10/15/2010 JRE
SCOT0012-5	11/050,779	2/7/2005	10/15/2010 JRE
SCOT0012-6	12/802,519	6/8/2010	11/4/2010 JRE
SCOT0012-7	12/383,916	3/30/2009	10/15/2010 JRE
SCOT0012-8	11/894,443	8/21/2007	10/15/2010 JRE
SCOT0012-9	12/913,751	10/27/2010	11/8/2010 JRE
SCOT0012-10	13/803,889	3/14/2013	4/16/2013 JRE
SCOT0013-1	08/587,943	1/17/1996	1/11/2012 JRE
SCOT0014-1	09/046,627	3/24/1998	1/11/2012 JRE
SCOT0014-2	10/602,777	6/25/2003	08/1/2011 JRE
SCOT0014-3 redocketed as SCOT0020-2	11/512,701	8/29/2006	10/15/2010 JRE
SCOT0014-4	11/895,388	8/24/2007	10/15/2010 JRE
SCOT0014-5	12/655,002	12/22/2009	08/1/2011 JRE
SCOT0014-6	13/556,420	7/24/2012	9/17/2012 JRE
SCOT0014-7	13/794,584	3/12/2013	4/16/2013 JRE
SCOT0015-1	09/731,039	12/7/2000	1/11/2012 JRE
SCOT0015-2	11/647/861	12/29/2006	1/11/2012 JRE
SCOT0015-3	12/383,879	3/30/2009	10/15/2010 JRE
SCOT0015-4	12/886,732	9/21/2010	10/15/2010 JRE
SCOT0015-5	13/572,641	8/11/2012	10/11/2012 JRE
SCOT0015-6	13/794,742	3/12/2013	4/16/2013 JRE
SCOT0016-1	10/049,101	7/23/2002	1/11/2012 JRE
SCOT0016-2	12/287,443	10/9/2008	10/15/2010 JRE
SCOT0016-3	13/413,691	3/7/2012	8/30/2012 JRE

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0016-4	13/796,538	3/12/2013	4/16/2013 JRE
SCOT0017-1	09.657,181	9/7/2000	1/11/2012 JRE
SCOT0017-2	12/005,229	12/26/2007	1/11/2012 JRE
SCOT0017-3	12/655,357	12/22/2009	10/15/2010 JRE
SCOT0017-4	13/035,964	2/26/2011	08/1/2011 JRE
SCOT0017-5	13/487,119	6/1/2012	4/16/2013 JRE
SCOT0017-6	13/802,384	3/13/2013	4/16/2013 JRE
SCOT0018-1	10/417/231	4/17/2003	01/11/2011 JRE
SCOT0018-2	11/900,065	9/10/2007	10/15/2010 JRE
SCOT0018-3	11/900,066	9/10/2007	1/11/2012 JRE
SCOT0018-4	12/383,289	3/23/2009	08/1/2011 JRE
SCOT0018-5	13/273,930	10/14/2011	1/11/2012 JRE
SCOT0018-6	13/551,097	7/17/2012	4/16/2013 JRE
SCOT0018-7	13/488,357	6/4/2012	9/9/2012 JRE
SCOT0018-8	13/488,395	6/4/2012	9/9/2012 JRE
SCOT0019-1	09/053,628	4/2/1998	1/11/2012 JRE
SCOT0019-2	09/644,098	8/23/2000	1/11/2012 JRE
SCOT0019-3	11/358,874	2/21/2006	1/11/2012 JRE
SCOT0019-4	12/799,894	5/4/2010	12/13/2010 JRE
SCOT0020-1	09/731,040	12/7/2000	1/11/2012 JRE
SCOT0020-2	11/512,701	8/29/2006	08/1/2011 JRE
SCOT0020-3	13/826,858	3/14/2013	4/16/2013 JRE
SCOT0020-4	13/797,744	3/12/2013	4/16/2013 JRE
SCOT0022-1	09/594,719	6/16/2000	4/16/2013 JRE
SCOT0022-2	11/519,467	9/12/2006	4/16/2013 JRE
SCOT0022-3	12/655,036	12/22/2009	08/1/2011 JRE
SCOT0022-4	13/423,650	3/19/2012	7/26/2012 JRE
SCOT0022-5	13/802,471	3/13/2013	4/16/2013 JRE
SCOT0023-1	08/772,222	12/20/1996	4/16/2013 JRE
SCOT0023-2	09/456,319	12/8/1999	4/16/2013 JRE

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0023-3	11/826,234	12/30/2004	4/16/2013 JRE
SCOT0023-4	11/592,879	11/2/2006	4/16/2013 JRE
SCOT0023-5	12/798,959	4/14/2010	08/1/2011 JRE
SCOT0024-2	11/518,806	9/11/2006	08/1/2011 JRE
SCOT0024-3	13/429,396	3/25/2012	7/26/2012 JRE
SCOT0025-1	61/794,141	3/15/2013	4/16/2013JRE

AS OF 1/12/2012, THE FOLLOWING TABLE COLLATES ADDITIONAL REFERENCES CITED IN ANY SCOT (SCOTT MOSKOWITZ) CASE

Date of Document Citing Reference	Atty Ref	Application Number	ID of paper in which references were cited	References checked to see if they existed in the master IDS (initials of person checking)	Reference Identifiers of New references in document, now added to master IDS
Sept 14, 2010	SCOT0012-7	12/383,916	892	JRE	U#299
11/17/2010	ALL	N/A	Review of draft master IDS, correction to cite publications in lieu of filed applications, per RAN instructions.	JRE	P76-P82
12/9/2010	SCOT0018-2	11/900,065	892	JRE	U303 & P83
11/30/2010	SCOT0019-4	12/799,894	892	JRE	U304
11/21/2011	SCOT0016-2	12/287,443	892	JRE	U305, U306 & U307
1/12/2012	SCOT0011-8	12/803,194	892	JRE	U308
1/12/2012	SCOT0014-5	12/655,002	892	JRE	U309
1/12/2012	SCOT0017-4	13/035,964	892	JRE	U310-U316
1/12/2012	SCOT0018-2	11/900,065	892	JRE	P84-P85
3/7/2012	SCOT0018-2	11/900,065	892	JRE	P86 -P87 & U317
8/30/2012	SCOT0016-3	13/413,691	892	JRE	U318 & U319

9/17/2012	SCOT0014-6	13/556,420	Per RAN created CTS reminder	JRE	L212
11/26/2012	SCOT0017-4	13/035.964	Per RAN inst rec'vd 11/15/2012	JRE	U320 & L213-L217
4/16/2013	SCOT0017-4	13/035,964	Review of Patented case	JRE	U322-U326 & P88-P90
6/13/2013	SCOT0018-7	13/488,357	Per instructions received from RAN	JRE	U329-332 L218-L223
6/28/2013	SCOT0014-6	13/556,420	Per instructions received from RAN	JRE	U0333
1/21/2014			Per Instructions received from RAN on 1/7/2014	JRE	L229
2/6/2014	SCOT0017-6	13/802,384	Per instructions received from RAN on 1/30/2014	JRE	U335

NOTE: MPEP 609.02 Information Disclosure Statements in Continued Examinations or Continuing Applications [R-5] states in part that:

"2. Continuation Applications , Divisional Applications, or Continuation-In-Part Applications Filed Under 37 CFR 1.53(b)
The examiner will consider information which has been considered by the Office in a parent application when examining: (A) a continuation application filed under 37 CFR 1.53(b), (B) a divisional application filed under 37 CFR 1.53(b), or (C) a continuation-in-part application filed under 37 CFR 1.53(b). A listing of the information need not be resubmitted in the continuing application unless the applicant desires the information to be printed on the patent"

See http://mpep.uspto.gov/RDMS/detail/manual/MPEP/e8r9/d0e18.xml#/manual/MPEP/e8r9/d0e53250.xml (8/2012)

Accordingly, we are submitting only references not cited in the parent application.

Please consider the references cited herein.

Date signed:5/7/2014 Signature: /BruceMargulies/
Printed Name: Bruce T. Margulies, Reg No: 64175
Attorney of Record

JRE
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Printed: May 8, 2014 (1:29pm)
Path: Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ReferenceCitationList_SCOT0014-6_05-01-2014.wpd
37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

LISTING OF UNITED STATES PATENTS - U series

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 01	3947825	March 1976	Cassada	X
	U 02	3984624	October 1976	Waggener	X
	U 03	3986624	October 1976	Cates, Jr. et al.	X
	U 04	4038596	July 1977	Lee	X
	U 05	4200770	April 1980	Hellman et al.	X
	U 06	4218582	August 1980	Hellman et al.	X
	U 07	4339134	July 1982	Macheel	X
	U 08	4390898	June 1983	Bond et al.	X
	U 09	4405829	September 1983	Rivest et al.	X
	U 010	4424414	January 1984	Hellman et al.	X
	U 011	4528588	July 1985	Lofberg	X
	U 012	4672605	June 1987	Hustig et al.	X
	U 013	4748668	May 1988	Shamir et al.	X
	U 014	4789928	December 1988	Fujisaki	X
	U 015	4827508	May 1989	Shear	X
	U 016	4876617	October 1989	Best et al.	X
	U 017	4896275	January 1990	Jackson	X
	U 018	4908873	March 1990	Philibert et al.	X
	U 019	4939515	July 1990	Adelson	X
	U 020	4969204	November 1990	Melnychuk et al.	X
	U 021	4972471	November 1990	Gross et al.	X
	U 022	4977594	December 1990	Shear	X

DATE:	EXAMINER'S SIGNATURE:

Printed: May 8, 2014 (1:29pm)
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37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 023	4979210	December 1990	Nagata et al.	X
	U 024	4980782	December 1990	Ginkel	X
	U 025	5050213	September 1991	Shear	X
	U 026	5073925	December 1991	Nagata et al.	X
	U 027	5077665	December 1991	Silverman et al.	X
	U 028	5113437	May 1992	Best et al.	X
	U 029	5136581	August 1992	Muehrcke	X
	U 030	5136646	August 1992	Haber et al.	X
	U 031	5136647	August 1992	Haber et al.	X
	U 032	5142576	August 1992	Nadan	X
	U 033	5161210	November 1992	Druyvesteyn et al.	X
	U 034	5210820	May 1993	Kenyon	X
	U 035	5243423	September 1993	DeJean et al.	X
	U 036	5243515	September 1993	Lee	X
	U 037	5287407	February 1994	Holmes	X
	U 038	5319735	June 1994	Preuss et al.	X
	U 039	5341429	August 1994	Stringer et al.	X
	U 040	5341477	August 1994	Pitkin et al.	X
	U 041	5363448	November 1994	Koopman et al.	X
	U 042	5365586	November 1994	Indeck et al.	X
	U 043	5369707	November 1994	Follendore, III	X
	U 044	5379345	January 1995	Greenberg	X
	U 045	5394324	February 1995	Clearwater	X

DATE:	EXAMINER'S SIGNATURE:

Printed: May 8, 2014 (1:29pm)
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37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 046	5398285	March 1995	Borgelt et al.	X
	U 047	5406627	April 1995	Thompson et al.	X
	U 048	5408505	April 1995	Indeck et al.	X
	U 049	5410598	April 1995	Shear	X
	U 050	5412718	May 1995	Narasimhalv et al.	X
	U 051	5418713	May 1995	Allen	X
	U 052	5428606	June 1995	Moskowitz	X
	U 053	5450490	September 1995	Jensen et al.	X
	U 054	5469536	November 1995	Blank	X
	U 055	5471533	November 1995	Wang et al.	X
	U 056	5478990	December 1995	Montanari et al.	X
	U 057	5479210	December 1995	Cawley et al.	X
	U 058	5487168	January 1996	Geiner et al.	X
	U 059	5493677	February 1996	Balogh et al.	X
	U 060	5497419	March 1996	Hill	X
	U 061	5506795	April 1996	Yamakawa	X
	U 062	5513126	April 1996	Harkins et al.	X
	U 063	5513261	April 1996	Maher	X
	U 064	5530739	June 1996	Okada	X
	U 065	5530751	June 1996	Morris	X
	U 066	5530759	June 1996	Braudaway et al.	X
	U 067	5539735	July 1996	Moskowitz	X
	U 068	5548579	August 1996	Lebrun et al.	X

DATE:	EXAMINER'S SIGNATURE:

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Path: Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ReferenceCitationList_SCOT0014-6_05-01-2014.wpd
37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 069	5568570	October 1996	Rabbani	X
	U 070	5579124	November 1996	Aijala et al.	X
	U 071	5581703	December 1996	Baugher et al.	X
	U 072	5583488	December 1996	Sala et al.	X
	U 073	5598470	January 1997	Cooper et al.	X
	U 074	5606609	February 1997	Houser et al.	X
	U 075	5613004	March 1997	Cooperman et al.	X
	U 076	5617119	April 1997	Briggs et al.	X
	U 077	5625690	April 1997	Michel et al.	X
	U 078	5629980	May 1997	Stefik et al.	X
	U 079	5633932	May 1997	Davis et al.	X
	U 080	5634040	May 1997	Her et al.	X
	U 081	5636276	June 1997	Brugger	X
	U 082	5636292	June 1997	Rhoads	X
	U 083	5640569	June 1997	Miller et al.	X
	U 084	5646997	July 1997	Barton	X
	U 085	5657461	August 1997	Harkins et al.	X
	U 086	5659726	August 1997	Sandford, II et al.	X
	U 087	5664018	September 1997	Leighton	X
	U 088	5673316	September 1997	Auerbach et al.	X
	U 089	5677952	October 1997	Blakely et al.	X
	U 090	5680462	October 1997	Miller et al.	X
	U 091	5687236	November 1997	Moskowitz et al.	X

DATE:	EXAMINER'S SIGNATURE:

Printed: May 8, 2014 (1:29pm)
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EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	U 092	5689587	November 1997	Bender et al.	X
	U 093	5696828	December 1997	Koopman, Jr.	X
	U 094	5719937	February 1998	Warren et al.	X
	U 095	5721788	February 1998	Powell et al.	X
	U 096	5734752	March 1998	Knox	X
	U 097	5737416	April 1998	Cooper et al.	X
	U 098	5737733	April 1998	Eller	X
	U 099	5740244	April 1998	Indeck et al.	X
	U 0100	5745569	April 1998	Moskowitz et al.	X
	U 0101	5748783	May 1998	Rhoads	X
	U 0102	5751811	May 1998	Magnotti et al.	X
	U 0103	5754697	May 1998	Fu et al.	X
	U 0104	5757923	May 1998	Koopman, Jr.	X
	U 0105	5765152	June 1998	Erickson	X
	U 0106	5768396	June 1998	Sone	X
	U 0107	5774452	June 1998	Wolosewicz	X
	U 0108	5790677	August 1998	Fox et al.	X
	U 0109	5799083	August 1998	Brothers et al.	X
	U 0110	5809139	September 1998	Grirod et al.	X
	U 0111	5809160	September 1998	Powell et al.	X
	U 0112	5822432	October 1998	Moskowitz et al.	X
	U 0113	5828325	October 1998	Wolosewicz et al.	X

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	U 0114	5832119	November 1998	Rhoads	X
	U 0115	5848155	December 1998	Cox	X
	U 0116	5850481	December 1998	Rhoads	X
	U 0117	5859920	January 1999	Daly et al.	X
	U 0118	5860099	January 1999	Milios et al.	X
	U 0119	5862260	January 1999	Rhoads	X
	U 0120	5870474	February 1999	Wasilewski et al.	X
	U 0121	5884033	March 1999	Duvall et al.	X
	U 0122	5889868	March 1999	Moskowitz et al.	X
	U 0123	5893067	April 1999	Bender et al.	X
	U 0124	5894521	April 1999	Conley	X
	U 0125	5903721	May 1999	Sixtus	X
	U 0126	5905800	May 1999	Moskowitz et al.	X
	U 0127	5905975	May 1999	Ausubel	X
	U 0128	5912972	June 1999	Barton	X
	U 0129	5915027	June 1999	Cox et al.	X
	U 0130	5917915	June 1999	Hirose	X
	U 0131	5918223	June 1999	Blum	X
	U 0132	5920900	July 1999	Poole et al.	X
	U 0133	5923763	July 1999	Walker et al.	X
	U 0134	5930369	July 1999	Cox et al.	X
	U 0135	5930377	July 1999	Powell et al	. X
	U 0136	5940134	August 1999	Wirtz	X

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	U 0137	5943422	August 1999	Van Wie et al.	X
	U 0138	5963909	October 1999	Warren et al.	X
	U 0139	5973731	October 1999	Schwab	X
	U 0140	5974141	October 1999	Saito	X
	U 0141	5991426	November 1999	Cox et al.	X
	U 0142	5999217	December 1999	Berners-Lee	X
	U 0143	6009176	December 1999	Gennaro et al.	X
	U 0144	6029126	February 2000	Malvar	X
	U 0145	6041316	March 2000	Allen	X
	U 0146	6044471	March 2000	Colvin	X
	U 0147	6049838	April 2000	Miller et al.	X
	U 0148	6051029	April 2000	Paterson et al.	X
	U 0149	6061793	May 2000	Tewfik et al.	X
	U 0150	6069914	May 2000	Cox	X
	U 0151	6078664	June 2000	Moskowitz et al.	X
	U 0152	6081251	June 2000	Sakai et al.	X
	U 0153	6081587	June 2000	Reyes et al.	X
	U 0154	6088455	July 2000	Logan et al.	X
	U 0155	6131162	October 2000	Yoshiura et al.	X
	U 0156	6141753	October 2000	Zhao et al.	X
	U 0157	6141754	October 2000	Choy	X
	U 0158	6154571	November 2000	Cox et al.	X
	U 0159	6192138	February 2001	Yamadaji	X

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	U 0160	6199058	March 2001	Wong et al.	X
	U 0161	6205249	March 2001	Moskowitz	X
	U 0162	6208745	March 2001	Florenio et al.	X
	U 0163	6230268	May 2001	Miwa et al.	X
	U 0164	6233347	May 2001	Chen et al.	X
	U 0165	6233684	May 2001	Stefik et al.	X
	U 0166	6240121	May 2001	Senoh	X
	U 0167	6263313	July 2001	Milstead et al.	X
	U 0168	6272634	August 2001	Tewfik et al.	X
	U 0169	6275988	August 2001	Nagashima et al.	X
	U 0170	6278780	August 2001	Shimada	X
	U 0171	6278791	August 2001	Honsinger et al.	X
	U 0172	6282300	August 2001	Bloom et al.	X
	U 0173	6282650	August 2001	Davis	X
	U 0174	6285775	September 2001	Wu et al.	X
	U 0175	6301663	October 2001	Kato et al.	X
	U 0176	6310962	October 2001	Chung et al.	X
	U 0177	6330335	December 2001	Rhoads	X
	U 0178	6330672	December 2001	Shur	X
	U 0179	6345100	February 2002	Levine	X
	U 0180	6351765	February 2002	Pietropaolo et al.	X
	U 0181	6363483	March 2002	Keshav	X
	U 0182	6373892	April 2002	Ichien et al.	X

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	U 0183	6373960	April 2002	Conover et al.	X
	U 0184	6374036	April 2002	Ryan et al.	X
	U 0185	6377625	April 2002	Kim	X
	U 0186	6381618	April 2002	Jones et al.	X
	U 0187	6381747	April 2002	Wonfor et al.	X
	U 0188	6385329	May 2002	Sharma et al.	X
	U 0189	6389538	May 2002	Gruse et al.	X
	U 0190	6405203	June 2002	Collart	X
	U 0191	6415041	July 2002	Oami et al.	X
	U 0192	6425081	July 2002	Iwamura	X
	U 0193	6430301	August 2002	Petrovic	X
	U 0194	6430302	August 2002	Rhoads	X
	U 0195	6442283	August 2002	Tewfik et al.	X
	U 0196	6446211	September 2002	Colvin	X
	U 0197	6453252	September 2002	Laroche	X
	U 0198	6457058	September 2002	Ullum et al.	X
	U 0199	6463468	October 2002	Buch et al.	X
	U 0200	6484264	November 2002	Colvin	X
	U 0201	6493457	December 2002	Quackenbush	X
	U 0202	6502195	December 2002	Colvin	X
	U 0203	6522767	February 2003	Moskowitz et al.	X
	U 0204	6522769	February 2003	Rhoads et al.	X
	U 0205	6523113	February 2003	Wehrenberg	X

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	U 0206	6530021	March 2003	Epstein et al.	X
	U 0207	6532284	March 2003	Walker et al.	X
	U 0208	6539475	March 2003	Cox et al.	X
	U 0209	6557103	April 2003	Boncelet, Jr. et al.	X
	U 0210	6584125	June 2003	Katto	X
	U 0211	6587837	July 2003	Spagna et al.	X
	U 0212	6598162	July 2003	Moskowitz	X
	U 0213	6606393	August 2003	Xie et al.	X
	U 0214	6647424	November 2003	Pearson et al.	X
	U 0215	6658010	December 2003	Enns et al.	X
	U 0216	6665489	December 2003	Collart	X
	U 0217	6668246	December 2003	Yeung et al.	X
	U 0218	6668325	December 2003	Collberg et al	. X
	U 0219	6687683	February 2004	Harada et al.	X
	U 0220	6725372	April 2004	Lewis et al	. X
	U 0221	6754822	June 2004	Zhao	X
	U 0222	6775772	August 2004	Binding et al.	X
	U 0223	6784354	August 2004	Lu et al.	X
	U 0224	6785815	August 2004	Serret-Avila et al.	X
	U 0225	6785825	August 2004	Colvin	X
	U 0226	6792548	September 2004	Colvin	X
	U 0227	6792549	September 2004	Colvin	X
	U 0228	6795925	September 2004	Colvin	X

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	U 0229	6799277	September 2004	Colvin	X
	U 0230	6813717	November 2004	Colvin	X
	U 0231	6813718	November 2004	Colvin	X
	U 0232	6823455	November 2004	Macy et al.	X
	U 0233	6834308	December 2004	Ikezoye et al.	X
	U 0234	6842862	January 2005	Chow et al.	X
	U 0235	6853726	February 2005	Moskowitz et al.	X
	U 0236	6857078	February 2005	Colvin	X
	U 0237	6931534	August 2005	Jandel et al.	X
	U 0238	6966002	November 2005	Torrubia-Saez	X
	U 0239	6983337	November 2005	Wold	X
	U 0240	6977894	December 2005	Achilles et al.	X
	U 0241	6978370	December 2005	Kocher	X
	U 0242	6986063	January 2006	Colvin	X
	U 0243	7007166	February 2006	Moskowitz et al.	X
	U 0244	7020285	March 2006	Kirovski et al.	X
	U 0245	7035409	April 2006	Moskowitz	X
	U 0246	7043050	May 2006	Yuval	X
	U 0247	7046808	May 2006	Metois et al.	X
	U 0248	7050396	May 2006	Cohen et al.	X
	U 0249	7051208	May 2006	Venkatesan et al.	X
	U 0250	7058570	June 2006	Yu et al.	X
	U 0251	7093295	August 2006	Saito	X

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	U 0252	7095874	August 2006	Moskowitz et al	X
	U 0253	7103184	September 2006	Jian	X
	U 0254	7107451	September 2006	Moskowitz	X
	U 0255	7123718	October 2006	Moskowitz et al.	X
	U 0256	7127615	October 2006	Moskowitz	X
	U 0257	7150003	December 2006	Naumovich et al.	X
	U 0258	7152162	December 2006	Moskowitz et al.	X
	U 0259	7159116	January 2007	Moskowitz	X
	U 0260	7162642	January 2007	Schumann et al.	X
	U 0261	7177429	February 2007	Moskowitz et al.	X
	U 0262	7177430	February 2007	Kim	X
	U 0263	7206649	April 2007	Kirovski et al.	X
	U 0264	7231524	June 2007	Bums	X
	U 0265	7233669.	June 2007	Candelore	X
	U 0266	7240210	July 2007	Michak et al.	X
	U 0267	7266697	September 2007	Kirovski et al	X
	U 0268	7287275	October 2007	Moskowitz	X
	U 0269	7289643	October 2007	Brunk et al.	X
	U 0270	7343492	March 2008	Moskowitz et al.	X
	U 0271	7346472	March 2008	Moskowitz et al.	X
	U 0272	7362775	April 2008	Moskowitz	X
	U 0273	7363278	April 2008	Schmelzer et al.	X
	U 0274	7409073	August 2008	Moskowitz et al.	X

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	U 0275	7457962	November 2008	Moskowitz	X
	U 0276	7460994	December 2008	Herre et al.	X
	U 0277	7475246	January 2009	Moskowitz	X
	U 0278	7530102	May 2009	Moskowitz	X
	U 0279	7532725	May 2009	Moskowitz et al.	X
	U 0280	7568100	July 2009	Moskowitz et al.	X
	U 0281	7647502	January 2010	Moskowitz	X
	U 0282	7647503	January 2010	Moskowitz	X
	U 0283	7779261	August 2010	Moskowitz	X
	U 0284	6990453	January 2006	Wang	X
	U 0285	6081597	June 2000	Hoffstein	X
	U 0286	7035049	Apr 2006	Yamamoto	X
	U 0287	7664263	Feb 2010	Moskowitz	X
	U 0288	7286451	Oct 2007	Wirtz	X
	U 0289	6385324	May 2002	Koppen	X
	U 0290	6674858	Jan 2004	Kimura	X
	U 0291	6148333	Nov 2000	Guedalia	X
	U 0292	6418421	Jun 2002	Hurtado	X
	U 0293	6385596	May 2002	Wiser	X
	U 0294	6226618	May 2001	Downs	X
	U 0295	6957330	Oct 2005	Hughes	X
	U 0296	5842213	Nov 1998	Odom	X
	U 0297	5818818	Oct 1998	Soumiya	X

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	U 0298	6590996	Jun 2003	Reed	X
	U 0299	5949055	Sept 1999	Fleet	X
	U 0300	6067622	May 2000	Moore	X
	U 0301	7761712	Jun 2010	Moskowitz	X
	U 0302	7743001	Jun 2010	Vermeulen	X
	U 0303	6865747	Mar 2005	Mercier	X
	U 0304	6611599	Aug 2003	Natarajan	X
	U 0305	6480937	Nov 2002	Vorbach	X
	U 0306	6398245	Jun 2002	Gruse	X
	U 0307	6950941	Sept 2005	Lee	X
	U 0308	6983058	Jan 2006	Fukuoka	X
	U 0309	5675653	Oct 1997	Nelson	X
	U 0310	6804453	Oct 2004	Sasamoto	X
	U 0311	6178405	Jan 2001	Ouyang	X
	U 0312	5839100	Nov 1998	Wegener	X
	U 0313	5781184	Jul 1998	Wasserman	X
	U 0314	5617506	Apr 1997	Burk	X
	U 0315	5327520	Jul 1994	Chen	X
	U 0316	5111530	May 1992	Kutaragi	X
	U 0317	7095715	Aug 2006	Buckman	X
	U 0318	6173322	Jan 2001	Hu	X
	U 0319	5754938	May 1998	Herz	X
	U 0320	6035398	Mar 2000	Bjorn	

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	U 0321	5901178	May 1999	Lee	
	U 0322	8214175	July 2012	Moskowitz	
	U 0323	8265278	Sept 2012	Moskowitz	
	U 0324	8161286	Nov 2010	Moskowitz	
	U 0325	8307213	Jan 2011	Moskowitz	
	U 0326	8121343	May 2012	Moskowitz	
	U 0327	5437050	Jul 1995	Lamb	
	U 0328	5123045	Jun 1992	Ostrovsky	X
	U 0329	7310815	Dec 2007	Yanovsky	
	U 0330	8179846	May 2012	Dolganow	
	U 0331	7719966	May 2010	Luft	
	U 0332	7630379	Dec 2009	Morishita	
	U 0333	5949973	Sept 1999	Yarom	
	U 0334	8400566	Mar. 2013	Terry	
	U 0335	5649284	July 1997	Yoshinobu	
	U 0336	744506	Oct 2008	Datta	
	U 0337	6480963	Oct 2002	Tachibana	
	U 0338	6510513	Jan 2003	Darrow	
	U 0339	5189411	Feb 1993	Collar	
	U 0340	5293633	Mar 1994	Robbins	
	U 0341	4633462	Dec 1986	Stifle	
	U 0342	5103461	Mar 1992	Cain	
	U 0343	6272535	Aug 2001	Iwamura	

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	U 0344	6029195	Feb 2000	Herz	
	U 0345	8095949	Jan 2012	Hendricks	
	U 0346	5297032	Mar 1994	Trojan	
	U 0347	5644727	Jul 1997	Atkins	
	U 0348	5721781	Feb 1998	Deo	
	U 0349	5822436	Oct 1998	Rhoads	
	U 0350	5845266	Dec 1998	Lupien	
	U 0351	5864827	Jan 1999	Wilson	
	U 0352	5875437	Feb 1999	Atkins	
	U 0353	5892900	Apr 1999	Ginter	
	U 0354	6108722	Aug 2000	Troeller	
	U 0355	6029146	Feb 2000	Hawkins	
	U 0356	6032957	Mar 2000	Kiyosaki	
	U 0357	6134535	Oct 2000	Belzberg	
	U 0358	6185683	Feb 2001	Ginter	
	U 0359	6233566	May 2001	Levine	
	U 0360	6253193	Jun 2001	Ginter	
	U 0361	6272474	Aug 2001	Garcia	
	U 0362	6317728	Nov 2001	Kane	
	U 0363	6363488	Mar 2002	Ginter	
	U 0364	6389402	May 2002	Ginter	
	U 0365	6427140	Jul 2002	Ginter	
	U 0366	6484153	Nov 2002	Walker	

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	U 0367	6556976	Aug 1987	Callen	
	U 0368	6574608	Jun 2003	Dahod	
	U 0369	6601044	Jul 2003	Wallman	
	U 0370	6594643	Jul 2003	Freeny	
	U 0371	6618188	Sep 2003	Haga	
	U 0372	6778968	Aug 2004	Gulati	
	U 0373	6839686	Jan 2005	Galant	
	U 0374	6856867	Feb 2005	Woolston	
	U 0375	6876982	Apr 2005	Lancaster	
	U 0376	7003480	Feb 2006	Fox	
	U 0377	5822436	Oct 1998	Rhoads	

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LISTING OF UNITED STATES PUBLISHED APPLICATIONS - P Series

EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	P 01	20010010078	July 2001	Moskowitz	X
	P 02	20010043594	November 2001	Ogawa et al.	X
	P 03	20020010684	January 2002	Moskowitz	X
	P 04	20020026343	February 2002	Duenke	X
	P 05	20020056041	May 2002	Moskowitz	X
	P 06	20020071556	June 2002	Moskowitz et al.	X
	P 07	20020073043	June 2002	Herman et al.	X
	P 08	20020097873	July 2002	Petrovic	X
	P 09	20020103883	August 2002	Haverstock et al.	X
	P 010	20020161741	October 2002	Wang et al.	X
	P 011	20030126445	July 2003	Wehrenberg	X
	P 012	20030133702	July 2003	Collart	X
	P 013	20030200439	October 2003	Moskowitz	X
	P 014	20030219143	November 2003	Moskowitz et al.	X
	P 015	20040028222	February 2004	Sewell et al.	X
	P 016	20040037449	February 2004	Davis et al.	X
	P 017	20040049695	March 2004	Choi et al.	X
	P 018	20040059918	March 2004	Xu	X
	P 019	20040083369	April 2004	Erlingsson et al.	X
	P 020	20040086119	May 2004	Moskowitz	X
	P 021	20040093521	May 2004	Hamadeh et al.	X
	P 022	20040117628	June 2004	Colvin	X

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EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	P 023	20040117664	June 2004	Colvin	X
	P 024	20040125983	July 2004	Reed et al.	X
	P 025	20040128514.	July 2004	Rhoads	X
	P 026	20040225894	November 2004	Colvin	X
	P 027	20040243540	December 2004	Moskowitz et al.	X
	P 028	20050135615	June 2005	Moskowitz et al.	X
	P 029	20050160271	July 2005	Brundage et al.	X
	P 030	20050177727	August 2005	Moskowitz et al.	X
	P 031	20050246554	November 2005	Batson	X
	P 032	20060005029	January 2006	Petrovic et al.	X
	P 033	20060013395	January 2006	Brundage et al.	X
	P 034	20060013451	January 2006	Haitsma	X
	P 035	20060041753	February 2006	Haitsma	X
	P 036	20060101269	May 2006	Moskowitz et al.	X
	P 037	20060140403	June 2006	Moskowitz	X
	P 038	20060285722	December 2006	Moskowitz et al.	X
	P 039	20070011458	January 2007	Moskowitz	X
	P 040	20070028113	February 2007	Moskowitz	X
	P 041	20070064940	March 2007	Moskowitz et al.	X
	P 042	20070079131.	April 2007	Moskowitz et al.	X
	P 043	20070083467	April 2007	Lindahl et al.	X
	P 044	20070110240	May 2007	Moskowitz et al.	X
	P 045	20070113094	May 2007	Moskowitz et al.	X

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	P 046	20070127717	June 2007	Herre et al.	X
	P 047	20070226506	September 2007	Moskowitz	X
	P 048	20070253594	November 2007	Lu et al.	X
	P 049	20070294536.	December 2007	Moskowitz et al.	X
	P 050	20070300072	December 2007	Moskowitz	X
	P 051	20070300073	December 2007	Moskowitz	X
	P 052	20080005571	January 2008	Moskowitz	X
	P 053	20080005572	January 2008	Moskowitz	X
	P 054	20080016365	January 2008	Moskowitz	X
	P 055	20080022113	January 2008	Moskowitz	X
	P 056	20080022114	January 2008	Moskowitz	X
	P 057	20080028222	January 2008	Moskowitz	X
	P 058	20080046742	February 2008	Moskowitz	X
	P 059	20080075277	March 2008	Moskowitz et al.	X
	P 060	20080109417	May 2008	Moskowitz	X
	P 061	20080133927	June 2008	Moskowitz et al.	X
	P 062	20080151934	June 2008	Moskowitz et al.	X
	P 063	20090037740	February 2009	Moskowitz	X
	P 064	20090089427	April 2009	Moskowitz et al.	X
	P 065	20090190754	July 2009	Moskowitz et al.	X
	P 066	20090210711	August 2009	Moskowitz	X
	P 067	20090220074	September 2009	Moskowitz et al.	X
	P 068	20100002904	January 2010	Moskowitz	X

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EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	P 069	20100005308	January 2010	Moskowitz	X
	P 070	20100098251	Apr 2010	Moskowitz	X
	P 071	20100220861	Sept 2010	Moskowitz	X
	P 072	20100202607	Aug 2010	Moskowitz	X
	P 073	20020047873	June 2002	Petrovic	X
	P 074	20020009208	Jan 2002	Alattar	X
	P 075	20010029580	October 2001	Moskowitz	X
	P 076	20100182570	July 2010	Chota	X
	P 077	20100077220	March 2010	Moskowitz	X
	P 078	20100077219	March 2010	Moskowitz	X
	P 079	20100064140	March 2010	Moskowitz	X
	P 080	20100153734	June 2010	Moskowitz	X
	P 081	20100106736	April 2010	Moskowitz	X
	P 082	20060251291	November 2006	Rhoads	X
	P 083	20030002862	January 2003	Rodriguez	X
	P 084	2003005780	May 2003	Hansen	X
	P 085	20020152179	Oct 2002	Racov	X
	P 086	20030027549	Feb 2003	Kiel	X
	P 087	20020057651	May 2002	Roberts	X
	P 088	20110069864	March 2011	Moskowitz	
	P 089	20100313033	Dec 2010	Moskowitz	
	P 090	20110019691	Jan 2011	Moskowitz	
	P 091	20030023852	Jan. 2003	Wold	

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	P 092	20030033321	Feb 2003	Schrempp	
	P 093	20130145058	June 2013	Shuholm	
	P 094	20120057012	Mar. 2012	Sitrick	
	P 095	20110128445	Jun 2011	Carrieres	
	P 096	20020188570	Dec 2002	Holliman	
	P 097	20020069174	Jun 2002	Fox	

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LISTING OF FOREIGN AND INTERNATIONAL PATENT DOCUMENTS - F Series

EXAMINER INITIALS	REFERENCE NUMBER (F SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	COUNTRY OR REGION	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	F 01-	EP0372601	Jun., 1990	EP		X
	F 02-	EP0565947	Oct., 1993	EP		X
	F 03-	EP0581317	Feb., 1994	EP		X
	F 04-	EP0649261	Apr., 1995	EP		X
	F 05-	EP0651554	May., 1995	EP		X
	F 06-	EP1354276	Dec., 2007	EP		X
	F 07-	NL 1005523	Sep., 1998	NL		X
	F 08-	WO 9514289	May., 1995	WO		X
	F 09-	WO 9629795	Sep., 1996	WO		X
	F 010-	WO 9724833	Jul., 1997	WO		X
	F 011-	WO 9744736	Nov., 1997	WO		X
	F 012-	WO9837513	Aug., 1998	WO		X
	F 013-	WO 9952271	Oct., 1999	WO		X
	F 014-	WO 9962044	Dec., 1999	WO		X
	F 015-	WO 9963443	Dec., 1999	WO		X
	F 016-	WO9726733	Jan. 1997	WO		X
	F 017-	WO98002864	Jul. 1997	WO		X
	F 018-	WO 0057643	Sept 2000	WO		X
	F 019-	WO 9642151	Dec 1996	WO		X
	F 020-	EP0872073	July 1996	EP		X
	F 021-	WO0118628	March 2001	WO		X
	F 022-	WO0143026	June 2001	WO		X

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EXAMINER INITIALS	REFERENCE NUMBER (F SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	COUNTRY OR REGION	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	F 023-	WO0203385	Jan 2002	WO		X
	F 024-	WO9701892	June 1995	WO		X
	F 025-	WO9726732	July 1997	WO		X
	F 026-	WO9802864	Jan 1998	WO		X
	F 027-	EP1547337	Mar 2006	EP		X
	F 028-	EP0581317A2	Feb 1994	EP		X
	F 029-	WO023385A1	Oct 2002	WO		X

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LISTING OF NON PATENT LITERATURE - L Series

EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
	1	L- 01	N/A	US. Appl. No. 08/999,766, filed Jul. 23, 1997, entitled "Steganographic Method and Device", published as 7568100 07-28-2009, cited as U280.	X
	2	L- 02	N/A	EPO Application No. 96919405.9, entitled "Steganographic Method and Device"; published as EP0872073 (A2), 10-21-1998, cited herein as F20.	X
	3	L- 03	N/A	U.S. Appl. No. 11/050,779, filed Feb. 7, 2005, entitled "Steganographic Method and Device", published as 20050177727 A1 08-11-2005, cited herein as P30.	X
	4	L- 04	N/A	U.S. Appl. No. 08/674,726, filed Jul. 2, 1996, entitled "Exchange Mechanisms for Digital Information Packages with Bandwidth Securitization, Multichannel Digital Watermarks, and Key Management", published as 7362775 04-22-2008, cited herein as U272 .	X
	5	L- 05	N/A	U.S. Appl. No. 09/545,589, filed Apr. 7, 2000, entitled "Method and System for Digital Watermarking", published as 7007166 02-28-2006, cited herein as U243	X
	6	L- 06	N/A	U.S. Appl. No. 11/244,213, filed Oct. 5, 2005, entitled "Method and System for Digital Watermarking", published as 2006-0101269 A1 05-11-2006, cited herein as P36	X
	7	L- 07	N/A	U.S. Appl. No. 11/649,026, filed Jan. 3, 2007, entitled "Method and System for Digital Watermarking", published as 2007-0113094 A1 05-17-2007, cited herein as P45.	X
	8	L- 08	N/A	U.S. Appl. No. 09/046,627, filed Mar. 24, 1998, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 6,598,162 07-22-2003, cited herein as U212.	X
	9	L- 09	N/A	U.S. Appl. No. 10/602,777, filed Jun. 25, 2003, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 2004-0086119 A1 05-06-2004, cited herein P20.	X

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	10	L- 010	N/A	U.S. Appl. No. 09/053,628, filed Apr. 2, 1998, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", 6,205,249 03-20-2001, cited herein as U161.	X
	11	L- 011	N/A	U.S. Appl. No. 09/644,098, filed Aug. 23, 2000, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 7,035,409 04-25-2006, cited herein as U245.	X
	12	L- 012	N/A	Jap. App. No. 2000-542907, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking"; which is a JP national stage of PCT/US1999/007262, published as WO/1999/052271, 10/14/1999, F13 here in above..	X
	13	L- 013	N/A	U.S. Appl. No. 09/767,733, filed Jan. 24, 2001 entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 2001-0010078 A1 07-26-2001, cited herein as P1.	X
	14	L- 014	N/A	U.S. Appl. No. 11/358,874, filed Feb. 21, 2006, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 2006-0140403 A1 06-29-2006, cited herein as P37.	X
	15	L- 015	N/A	U.S. Appl. No. 10/417,231, filed Apr. 17, 2003, entitled "Methods, Systems And Devices For Packet Watermarking And Efficient Provisioning Of Bandwidth", published as 2003-0200439 A1 10-23-2003, cited herein as P13,	X
	16	L- 016	N/A	U.S. Appl. No. 09/789,711, filed Feb. 22, 2001, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2001-0029580 A1 10-11-2001, cited herein as P75.	X
	17	L- 017	N/A	U.S. Appl. No. 11/497,822, filed Aug. 2, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2007-0011458 A1 01-11-2007, cited herein as P39.	X

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	18	L- 018	N/A	U.S. Appl. No. 11/599,964, filed Nov. 15, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2008-0046742 A1 02-21-2008, cited herein as P58.	X
	19	L- 019	N/A	U.S. Appl. No. 11/599,838, filed Nov. 15, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2007-0226506 A1 09-27-2007, cited herein as P47.	X
	20	L- 020	N/A	U.S. Appl. No. 10/369,344, filed Feb. 18, 2003, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digitized Data", published as 2003-0219143 A1 11-27-2003, cited herein as P14.	X
	21	L- 021	N/A	U.S. Appl. No. 11/482,654, filed Jul. 7, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digitized Data", published as 2006-0285722 A1 12-21-2006, cited herein as P38.	X
	22	L- 022	N/A	U.S. Appl. No. 09/594,719, filed Jun. 16, 2000, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 7,123,718 10-17-2006, cited herein as U255.	X
	23	L- 023	N/A	U.S. Appl. No. 11/519,467, filed Sep. 12, 2006, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 2007-0064940 A1 03-22-2007, cited herein as P41.	X
	24	L- 024	N/A	U.S. Appl. No. 09/731,040, filed Dec. 7, 2000, entitled "Systems, Methods And Devices For Trusted Transactions", 2002-0010684 A1 01-24-2002, cited herein as P3.	X
	25	L- 025	N/A	U.S. Appl. No. 11/512,701, filed Aug. 29, 2006, entitled "Systems, Methods And Devices For Trusted Transactions", published as 2007-0028113 A1 02-01-2007, cited herein as P40.	X
	26	L- 026	N/A	U.S. Appl. No. 10/049,101, filed Feb. 8, 2002, entitled "A Secure Personal Content Server", published as 7,475,246 01-06-2009, cited herein as U277.	X

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	27	L- 027	N/A	PCT Application No. PCT/US00/21189, filed Aug. 4, 2000, entitled, "A Secure Personal Content Server", Pub. No.: WO/2001/018628 ; Publication Date: 15.03.2001, cited herein as F21.	X
	28	L- 028	N/A	U.S. Appl. No. 09/657,181, filed Sep. 7, 2000, entitled "Method and Device For Monitoring And Analyzing Signals", published as 7,346,472 03-18-2008, cited herein as U271.	X
	29	L- 029	N/A	U.S. Appl. No. 10/805,484, filed Mar. 22, 2004, entitled "Method And Device For Monitoring And Analyzing Signals", published as 2004-0243540 A1 12-02-2004, cited herein as P27.	X
	30	L- 030	N/A	U.S. Appl. No. 09/956,262, filed Sep. 20, 2001, entitled "Improved Security Based on Subliminal and Supraliminal Channels For Data Objects", published as 2002-0056041 A1 05-09-2002, cited herein as P05	X
	31	L- 031	N/A	U.S. Appl. No. 11/518,806, filed Sep. 11, 2006, entitled "Improved Security Based on Subliminal and Supraliminal Channels For Data Objects", 2008-0028222 A1 01-31-2008, cited herein as P57.	X
	32	L- 032	N/A	U.S. Appl. No. 11/026,234, filed Dec. 30, 2004, entitled "Z-Transform Implementation of Digital Watermarks" , published as 2005-0135615 A1 06-23-2005, cited herein as P28.	X
	33	L- 033	N/A	U.S. Appl. No. 11/592,079, filed Nov. 2, 2006, entitled "Linear Predictive Coding Implementation of Digital Watermarks", published as 2007-0079131 A1 04-05-2007, cited herein as P42.	X
	34	L- 034	N/A	U.S. Appl. No. 09/731,039, filed Dec. 7, 2000, entitled "System and Methods for Permitting Open Access to Data Objects and for Securing Data within the Data Objects", published as 2002-0071556 A1 06-13-2002, cited herein as P06.	X

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	35	L- 035	N/A	U.S. Appl. No. 11/647,861, filed Dec. 29, 2006, entitled "System and Methods for Permitting Open Access to Data Objects and for Securing Data within the Data Objects", published as 2007-0110240 A1 05-17-2007, cited herein as P44.	X
	36	L- 036	1996	Schneier, Bruce, Applied Cryptography, 2nd Ed., John Wiley & Sons, pp. 9-10, 1996.	X
	37	L- 037	1997	Menezes, Alfred J., Handbook of Applied Cryptography, CRC Press, p. 46, 1997.	X
	38	L- 038	1997	Merriam-Webster's Collegiate Dictionary, 10th Ed., Merriam Webster, Inc., p. 207.	X
	39	L- 039	1984	Brealy, et al., Principles of Corporate Finance, "Appendix A--Using Option Valuation Models", 1984, pp. 448-449.	X
	40	L- 040	2001	Copeland, et al., Real Options: A Practitioner's Guide, 2001 pp. 106-107, 201-202, 204-208.	X
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	110	L- 0110		DUPLICATE OF L-59, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	111	L- 0111		DUPLICATE OF L-61, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	112	L- 0112		DUPLICATE OF L-62, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X

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	114	L- 0114		DUPLICATE OF L-65, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	115	L- 0115	Unknown.	Tirkel, A.Z., "A Two-Dimensional Digital Watermark", Scientific Technology, 686, 14, date unknown. (citation revised upon review on 11/16/10 by RAN.)	X
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	117	L- 0117		DUPLICATE OF L-68, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	118	L- 0118		DUPLICATE OF L-69, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	119	L- 0119		DUPLICATE OF L-70, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	120	L- 0120		DUPLICATE OF L-71, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	121	L- 0121		DUPLICATE OF L-72, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	122	L- 0122		DUPLICATE OF L-73, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	123	L- 0123		DUPLICATE OF L-74, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	124	L- 0124		DUPLICATE OF L-75, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	125	L- 0125		DUPLICATE OF L-076, REMOVED. RN. 11/16/2010	X

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	126	L- 0126		DUPLICATE OF L-77, REMOVED. RN. 11/16/2010	X
	127	L- 0127		DUPLICATE OF L-78, REMOVED. RN. 11/16/2010	X
	128	L- 0128		DUPLICATE OF L-79, REMOVED. RN. 11/16/2010	X
	129	L- 0129		EP0581317A2, MOVED TO FOREIGN PATENT PUBS as F-028	X
	130	L- 0130		DUPLICATE OF L-52, REMOVED. RN. 11/16/2010	X
	131	L- 0131		DUPLICATE OF L-36, REMOVED. RN. 11/16/2010	X
	132	L- 0132		DUPLICATE OF L-38, REMOVED. RN. 11/16/2010.	X
	133	L- 0133		DUPLICATE OF L-37, REMOVED. RN. 11/16/2010	X
	134	L- 0134		DUPLICATE OF L-36, REMOVED. RN. 11/16/2010	X
	135	L- 0135		DUPLICATE OF L-37, REMOVED. RN. 11/16/2010	X
	136	L- 0136		DUPLICATE OF L-38, REMOVED. RN. 11/16/2010	X
	137	L- 0137		DUPLICATE OF L-39, REMOVED. RN. 11/16/2010	X
	138	L- 0138		DUPLICATE OF L-40, REMOVED. RN. 11/16/2010	X
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	140	L- 0140		DUPLICATE OF L-42, REMOVED. RN. 11/16/2010	X
	141	L- 0141		DUPLICATE OF L-43, REMOVED. RN. 11/16/2010	X
	142	L- 0142		DUPLICATE OF L-44, REMOVED. RN. 11/16/2010	X
	143	L- 0143		DUPLICATE OF L-45, REMOVED. RN. 11/16/2010.	X
	144	L- 0144		DUPLICATE OF L-46, REMOVED. RN. 11/16/2010.	X
	145	L- 0145		DUPLICATE OF L-47, REMOVED. RN. 11/16/2010	X
	146	L- 0146		DUPLICATE OF L-48, REMOVED. RN. 11/16/2010	X
	147	L- 0147		DUPLICATE OF L-49, REMOVED. RN. 11/16/2010	X
	148	L- 0148		DUPLICATE OF L-50, REMOVED. RN. 11/16/2010	X
	149	L- 0149		DUPLICATE OF L-51, REMOVED. RN. 11/16/2010	X
	150	L- 0150		DUPLICATE OF L-52, REMOVED. RN. 11/16/2010	X
	151	L- 0151		DUPLICATE OF L-63, REMOVED. RN. 11/16/2010	X
	152	L- 0152		DUPLICATE OF L-54, REMOVED. RN. 11/16/2010	X
	153	L- 0153		DUPLICATE OF L-55, REMOVED. RN. 11/16/2010.	X

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	155	L- 0155	N/A	PCT International Search Report in PCT/US95/08159.	X
	156	L- 0156	N/A	PCT International Search Report in PCT/US96/10257.	X
	157	L- 0157	N/A	Supplementary European Search Report in EP 96919405.	X
	158	L- 0158	N/A	PCT International Search Report in PCT/US97/00651.	X
	159	L- 0159	N/A	PCT International Search Report in PCT/US97/00652	X
	160	L- 0160	N/A	PCT International Search Report in PCT/US97/11455.	X
	161	L- 0161		PCT International Search Report in PCT/US99/07262.	X
	162	L- 0162		PCT International Search Report in PCT/US00/06522	Xf
	163	L- 0163		Supplementary European Search Report in EP00919398	X
	164	L- 0164		PCT International Search Report in PCT/US00/18411.	X
	165	L- 0165		PCT International Search Report in PCT/US00/18411.	X
	166	L- 0166		PCT International Search Report in PCT/US00/33126	X
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37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET:
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a	REFERENCES CITED AND CONSIDERED BY EXAMINER IN PARENT CASE IDENTIFIED BY PLACEMENT OF "X"
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	193	L- 0193	N/A	PCT Application No. PCT/US97/00651, filed Jan. 16, 1997, entitled, "Method for Stega-Cipher Protection of Computer Code", published as WO/1997/026732; Publication Date: 24.07.1997.	X
	194	L- 0194	N/A	PCT Application No. PCT/US97/00652, filed Jan. 17, 1997, entitled, "Method for an Encrypted Digital Watermark", published as WO/1997/026733; Publication Date: 24.07.1997	X
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	232	L- 0232	4/4/2014	Audible Magic Corporation's Second Amended Answer to Blue Spike LLC's Original Complaint for patent infringement and counterclaims against Blue Spike LLC, Blue Spike, Inc and Scott Moskowitz. Blue Spike LLC v. Texas Instruments, Audible Magic Corporation (E.D.T.X Dist Ct.) Case No. 6:12-CV-499-MHS	

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Electronic Patent Application Fee Transmittal	
Application Number:	13556420
Filing Date:	24-Jul-2012
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Filer:	Bruce Talbot Margulies/Jamaal Evans
Attorney Docket Number:	SCOT0014-6
Filed as Large Entity	

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Miscellaneous:				
Submission- Information Disclosure Stmt	1806	1	180	180
		Total in USD ($)		**180**

Electronic Acknowledgement Receipt

EFS ID:	18977692
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Bruce Talbot Margulies/Jamaal Evans
Filer Authorized By:	Bruce Talbot Margulies
Attorney Docket Number:	SCOT0014-6
Receipt Date:	08-MAY-2014
Filing Date:	24-JUL-2012
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Application Type:	Utility under 35 USC 111(a)

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4	Non Patent Literature	L214_MarketingFlyer1_1998-1999.pdf	2636798 6da3c324298bb657c393568b6efde125c016377a	no	1
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5	Non Patent Literature	L215_UAreU_UserGuideVer1_1998.pdf	3790512 7b6e371f3d6f670e314f8829a36899bedda38300	no	4
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Information:					
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13	Non Patent Literature	L223_Wikipedia_DeepContentInspection.pdf	100220 d24184e7caf99bb60429a10aa053097857543a82	no	6
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21	Non Patent Literature	L231_1404AudibleMagicsrevisedcounterclaims2.pdf	277324 d5b71ae97b27b89c2f5c2153999514569b22a577	no	45
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National Stage of an International Application under 35 U.S.C. 371
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New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
US Application and filing date: 13/556,420 filed 7/24/2012
USPTO CONF. NO: 5811
Inventor: MOSKOWITZ, Scott
Title: DATA PROTECTION METHOD AND DEVICE
Examiner/ArtUnit: OKEKE, Izunna / 2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data:
This application is a Continuation of 11/895,388 filed 08-24-2007
11/895,388 is a Division of 10/602,777 filed 06-25-2003 Patented 7,664,263
10/602,777 is a continuation of 09/046,627 filed 03-24-1998 Patented 6,598,162

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

0. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

1. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:** 180.00

2. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
37 CFR 1.97 INFORMATION DISCLOSURE STATEMENT
37 CFR 1.98 REFERENCE CITATION LIST CITING REFERENCES CITING REFERENCES U01-U377, P01-P97, F01-F29, AND L01-L231
Copies of references L213-L231

3. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: PClaw BankAcct, G/L: **6, 5010**
PCLAW BILLING REFERENCE: SCOT0001
Check#, Entry date, Amount: 2081/5-7-2014/180.00
Service Fees: Amount/CreditAtty/Entry date/Services: 600.00/BTM/5-5-2014/IDS filing

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: JRE
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)

DATE: 5/7/2014 SIGNATURE: /BruceMargulies/
PRINTED NAME: Bruce T. Margulies, Reg No 64175
Printed: May 8, 2014 (1:26pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\IDS_SCOT0014-6_5-5-2014.wpd

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

31518 7590 03/21/2014

NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

EXAMINER	
OKEKE, IZUNNA	
ART UNIT	PAPER NUMBER
2432	

NOTIFICATION DATE	DELIVERY MODE
03/21/2014	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

general@neifeld.com
rneifeld@neifeld.com
rhahl@neifeld.com

Office Action Summary	Application No. 13/556,420	Applicant(s) MOSKOWITZ, SCOTT A.	
	Examiner IZUNNA OKEKE	Art Unit 2432	AIA (First Inventor to File) Status No

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTHS FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *01/23/2014*.

☐ A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on _____.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.

4)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims*

5)☒ Claim(s) *58-107* is/are pending in the application.

5a) Of the above claim(s) _____ is/are withdrawn from consideration.

6)☐ Claim(s) _____ is/are allowed.

7)☒ Claim(s) *58-107* is/are rejected.

8)☐ Claim(s) _____ is/are objected to.

9)☐ Claim(s) _____ are subject to restriction and/or election requirement.

* If any claims have been determined allowable, you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to PPHfeedback@uspto.gov.

Application Papers

10)☐ The specification is objected to by the Examiner.

11)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

Certified copies:

a)☐ All b)☐ Some** c)☐ None of the:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

** See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)

2) ☐ Information Disclosure Statement(s) (PTO/SB/08a and/or PTO/SB/08b) Paper No(s)/Mail Date _____.

3) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .

4) ☐ Other: _____.

DETAILED ACTION

1. The present application is being examined under the pre-AIA first to invent provisions.

Response to Arguments

2. Applicant's arguments, filed 01/23/2014, with respect to the Restriction/Election Requirement have been fully considered and are persuasive. The Restriction/Election Requirement of 01/23/2014 has been withdrawn.

Claim Rejections - 35 USC § 112

3. The following is a quotation of the first paragraph of 35 U.S.C. 112(a):

> (a) IN GENERAL.—The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same, and shall set forth the best mode contemplated by the inventor or joint inventor of carrying out the invention.

The following is a quotation of the first paragraph of pre-AIA 35 U.S.C. 112:

> The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same, and shall set forth the best mode contemplated by the inventor of carrying out his invention.

4. Claims 82-107 are rejected under 35 U.S.C. 112(a) or 35 U.S.C. 112 (pre-AIA), first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor or a joint inventor, or for pre-AIA the inventor(s), at the time the application was filed, had possession of the claimed invention.

5. Claim 86 and 104 recites "to *shuffle at least a subset* of said code resources". There is no support or description in the specification for the claim language "shuffle at lease a subset". Claims 105-107 are rejected with the same rationale for their dependence on claim 104

6. Claims 82 and 87 recites "sequence of instructions that have *sequential addresses*". There is no support or description in the specification for the claim language "sequential addresses". Claim 83 is rejected with the same rationale for its dependence on claim 82. Claims 88 and 89 are rejected with the same rationale for their dependence on claim 87.

7. Claims 84 and 90 recites "plurality of executable code resources loaded in said memory have *memory addresses that immediately follow one another*". There is no support or description in the specification for the claim language "memory addresses that immediately follow one another". Claim 85 is rejected with the same rationale for its dependence on claim 84. Claims 91 and 92 are rejected with the same rationale for their dependence on claim 90.

8. Claim 93 recites "configured to *store in memory an original/new plurality of code resource addresses*". There is no support or description in the specification for the claim language "store in memory an original/new plurality of code resource addresses". Claims 94-103 are rejected with the same rationale for their dependence on claim 93.

9. The following is a quotation of 35 U.S.C. 112(b):

> (b) CONCLUSION.—The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the inventor or a joint inventor regards as the invention.

The following is a quotation of 35 U.S.C. 112 (pre-AIA), second paragraph:

> The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

10. Claims 82-107 are rejected under 35 U.S.C. 112(b) or 35 U.S.C. 112 (pre-AIA), second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which the inventor or a joint inventor, or for pre-AIA the applicant regards as the invention. As outlined above, the identified claim language/limitations are not recited or described in the disclosure. Therefore, the interpretation of the claims is indefinite and unclear.

For instance, claim 86 recites "shuffle at least a subset of said code resources". In the absence of any disclosure describing what a "subset of a code resource" is, it is unclear and indefinite what the claimed subject matter is. The same rational (not supported by the specification) applies to the rest of the claims identified above.

Claim Rejections - 35 USC § 102

11. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

12. Claims 58-81 are rejected under 35 U.S.C. 102(b) as being anticipated by Ostrovsky et al. (US-5123045).

a. *Referring to claims 58, 63, 67, 73 and 74:*

Regarding claims 58, 63, 67, 73 and 74, Ostrovsky teaches a computing device for running application software, comprising: memory designed to store data in non transitory form; an operating system; wherein said memory stores an application software (Col 4, Line 35-57….. data processing system comprising a memory to store an application software and an operating system as known in the art); wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources (Col 4, Line 10-30 and Col 5, Line 5-31…. Software protection mechanism and other code resources of the software); wherein said application software is designed to call said memory scheduler code resource; wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory (Col 5, Line 32-59…. periodically calling the protection mechanism to shuffle other code resources in memory to prevent memory analysis).

a. *Referring to claims 59, 68 and 69:*

Regarding claims 59, 68 and 69, Ostrovsky teaches the device of claim 58 wherein said memory scheduler code resource, when called, functions to shuffle said other code resources randomly in memory (Col 5, Line 32-59…. periodically calling the protection mechanism to shuffle other code resources randomly in memory to prevent memory analysis).

a. *Referring to claims 60, 61 and 70:*

Regarding claims 60, 61 and 70, Ostrovsky teaches the device of claim 58 further comprising a program counter designed to store values and wherein said computing device is designed to enable application software to modify a value stored by said program counter (Col 3, Line 43-51…. modifying a counter value to correspond with the shuffling).

a. *Referring to claim 62:*

Regarding claim 62, Ostrovsky teaches the device of claim 58 wherein said memory scheduler is designed to modify a stack frame, to modify a calling address, and to copy itself to a memory location associated with said calling address (Col 32-46…. during execution, the protection mechanism in memory is periodically called to shuffle the code contents of memory).

a. *Referring to claims 64, 65, 71 and 72:*

Regarding claims 64, 65, 71 and 72, Ostrovsky teaches the device of claim 58 wherein said memory scheduler maintains a list of addresses in memory, said addresses associated with at least one of the code resources (Col 6, Line 15-55…. maintaining addresses where the code resources are stored).

a. *Referring to claim 66:*

Regarding claim 66, Ostrovsky teaches the device of claim 58 further comprising a processor for processing instructions defined by said application software (Col 4, Line 39-41…. CPU).

a. *Referring to claims 75, 76, 78-80:*

Regarding claims 75, 76, 78-80, Ostrovsky teaches a system for executing an application on a computer system, comprising: a computer system comprising a processor for processing instructions and memory designed to store data in non transitory form (See the rejection in claim 58); a software application comprising a plurality of executable code resources loaded in said memory (See the rejection in claim 58); said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources (See the rejection in claim 58); and wherein a first one of said plurality of executable code resource is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor (Col 5, Line 32-59…. shuffling the code resources from one location (address) in memory to another location (address) in memory) during execution of the program).

a. *Referring to claims 77 and 81:*

Regarding claims 77 and 81, Ostrovsky teaches the system of claim 75 wherein said system is designed to repeatedly relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application (Col 5, Line 32-59…. periodically shuffling the code resources from one

location (address) in memory to another location (address) in memory) during execution of the program).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to IZUNNA OKEKE whose telephone number is (571)270-3854. The examiner can normally be reached on Monday - Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on (571) 272-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/IZUNNA OKEKE/
Examiner, Art Unit 2432

Search Notes	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	Art Unit
	IZUNNA OKEKE	2432

CPC- SEARCHED		
Symbol	Date	Examiner

CPC COMBINATION SETS - SEARCHED		
Symbol	Date	Examiner

US CLASSIFICATION SEARCHED			
Class	Subclass	Date	Examiner

SEARCH NOTES		
Search Notes	Date	Examiner
UpdatedText Search (See Search History)	2/28/2014	IO
Updated Keyword + Classification Search (See Search History)	2/28/2014	IO
Updated Search (713/1, 713/151, 713/165, 713/166, 713/167, 713/193, 713/194, 713/152, 713/187, 713/189, 713/188, 713/190, 726/26, 380/201, 711/1, 711/100) (See Search History)	2/28/2014	IO
Updated NPL Database Search (Google Scolar database search : Search terms : "rearranging code in runtime memory during execution to prevent memory analysis")	2/28/2014	IO
Review of parent case 08,587,943 and related case 11,895,388	2/28/2014	IO

INTERFERENCE SEARCH			
US Class/ CPC Symbol	US Subclass / CPC Group	Date	Examiner

			EPO; JPO; DERWENT; IBM_TDB			
S83	2	S35 and S82	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 23:28
S88	37429	713/1,151,165,166,167,193,194,152,187,189,188,190;726/26;380/201;711/1,100.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:49
S89	65	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory or register)) and ((protect$3 or stop$4 or inhibit$3) with (memory or register) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S90	1	S88 and S89	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S91	27	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory or register)) and ((protect$3 or stop$4 or inhibit$3) with (memory or register) with analysis) and ((run or execution) adj time) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S92	0	S88 and S91	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S93	41	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or arrang$3) same (code or object) same (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S94	1	S88 and S93	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S95	52	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S96	1	S88 and S95	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S97	0	((shuffl$3 or rearrang$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR;	AND	ON	2014/02/28 19:51

			FPRS; EPO; JPO; DERWENT; IBM_TDB			
S98	0	S88 and S97	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S99	2	((shuffl$3 or arrang$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S100	0	S88 and S99	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S103	0	((shuffl$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S104	0	S88 and S103	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S105	0	((shuffl$3) same (code or object)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S106	0	S88 and S105	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S107	1355	((shuffl$3 or arrang$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S108	61	S88 and S107	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S109	4	((shuffl$3 or arrang$3) with (code) with (memory)) and ((modif$5 or alter$5 or chang$5) with (program adj counter)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S110	0	S88 and S109	US-PGPUB; USPAT;	AND	ON	2014/02/28 19:51

			USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			
S111	140	((shuffl$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S112	51	S88 and S111	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S113	95	((shuffl$3) near5 (code) near5 (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S114	50	S88 and S113	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S115	59	((shuffl$3 or arrang$3) same (code) same (memory)) and ((modif$5 or alter$5 or chang$5) same (program adj counter)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S116	1	S88 and S115	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S119	0	((shuffl$3 or (re near2 (arrang$3 or order)) or transpos$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S120	0	S88 and S119	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S121	37	((shuffl$3 or (arrang$3 or order) or transpos$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S122	1	S88 and S121	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S123	47	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) and ((protect$3 or stop$4 or	US-PGPUB;	AND	ON	2014/02/28

		inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			19:51
S124	2	S88 and S123	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S125	6	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) same ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S126	0	S88 and S125	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:51
S127	6	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) same ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:55
S128	0	S88 and S127	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:55
S129	47	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) and ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:55
S130	2	S88 and S129	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:55
S131	117	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or $3arrang$3) same (code or object) same (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:57
S132	2	S88 and S131	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2014/02/28 19:57

EAST Search History (Interference)

<This search history is empty>

3/13/2014 10:48:37 PM
C:\Users\iokeke\Documents\EAST\Workspaces\13556420.wsp

Index of Claims	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	Art Unit
	IZUNNA OKEKE	2432

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

☐ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

CLAIM		DATE								
Final	Original	03/21/2013	03/13/2014							
	58	✓	✓							
	59	✓	✓							
	60	✓	✓							
	61	✓	✓							
	62	✓	✓							
	63	✓	✓							
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	66	✓	✓							
	67	✓	✓							
	68	✓	✓							
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	87		✓							
	88		✓							
	89		✓							
	90		✓							
	91		✓							
	92		✓							
	93		✓							

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	13556420	MOSKOWITZ, SCOTT A.
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CLAIM		DATE								
Final	Original	03/21/2013	03/13/2014							
	94		✓							
	95		✓							
	96		✓							
	97		✓							
	98		✓							
	99		✓							
	100		✓							
	101		✓							
	102		✓							
	103		✓							
	104		✓							
	105		✓							
	106		✓							
	107		✓							

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**0
Number of independent claims previously paid for: 8
Number of independent claims in excess of 8: 0.
Number of total claims previously paid for: 26
Number of total claims in excess of 26: 0
3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
37 CFR 1.111 PRELIMINARY AMENDMENT - REMARKS
37 CFR 1.111 PRELIMINARY AMENDMENT - CLAIMS

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: PClaw BankAcct, G/L: **6, 5010**
PCLAW BILLING REFERENCE: [SCOT0001]
Check#, Entry date, Amount: [1916, 9/19/13, 4320.00]
Service Fees: Amount/CreditAtty/Entry date/Services: []

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: ran
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 1-23-2014 **SIGNATURE**: /RichardNeifeld/
Printed: January 23, 2014 (1:36pm) RICHARD NEIFELD, REG. NO. 35,299
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\Amendment_SCOT0014-6_1-23-2014.wpd

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE

37 CFR 1.111 AMENDMENT REMARKS

This is a response to the Non Final Office Action (NF)A) dated 1/14/2014.

SUMMARY OF NON FINAL OFFICE ACTION

The NFOA requires restriction of claims 58-107. Item 2 of the NFOA states that:

2. Restriction to one of the following inventions is required under 35 U.S.C. 121:
I. Claims 58-74, 78, 79, 80-92 and 104-107, drawn to a device for running of application software and shuffling code resources in memory during execution, classified in class 711, subclass 165.
II. Claims 75-77 and 93-103, drawn to a system for protecting a software application wherein an address identifying a code resource is relocated so that the code resource is specified by a new address, classified in class 711, subclass 202.

The NFOA further states that:

Invention I is distinct from invention II and is directed to a device for running an application wherein the code resources of the application are shuffled during execution.
Invention II is distinct from invention I and is directed to a system for protecting a software application by relocating a code resource of the application in such a way that an address identifying the code resource is changed to a new address.

In response, the applicant elects **Group II, Claims 75-77 and 93-103**, but with traverse of the requirement.

Traverse is on the grounds that there is no substantial burden in examining these claims, and no showing of such a burden, for several reasons. First, the examiner previously issued an action on the merits, which required both search and examination based upon the relevant art. Second, the requirement redefines the classification of Group I as class 711, subclass 165, a class not previously searched for Group I, and that redefinition is apparently incorrect. Third, the definition of Group II as class 711, subclass 202, provides no indication of a search burden, because the definition of class 711, subclass 202, to the NFOA's characterization of Groups I and II indicates, that if class 711, subclass 202 is relevant to Group II, then it is also relevant to Group I. In summary, even assuming the revised classification of Group I claims is correct, there is no indication of any search burden.

WHY IS GROUP I RECLASSIFIED TO CLASS 711, SUBCLASS 165?

The reasoning in the requirement that the Group I claims are classified in class 711, subclass 165, is not consistent with the prior determination of the examiner that the only subclasses of class 711 relevant to the Group I claims were subclasses 1 and 100.

The examiner previously searched the claims defined by group I and included in that search only 711/1 and 711/100 in class 711, as shown in the search notes of the prior office action. The definition of those classes and 711/165 follow.

711/1: "ADDRESSING COMBINED WITH SPECIFIC MEMORY CONFIGURATION OR SYSTEM:

This subclass is indented under the class definition. Subject matter comprising means or steps for determining one or more values (i.e., address data) that specify one or more locations in a storage medium wherein the means or steps are claimed in combination with a particular

configuration or system for storing data."

711/100: "STORAGE ACCESSING AND CONTROL:
This subclass is indented under the class definition. Subject matter comprising means (e.g., a processor, a controller, etc.) or steps for governing memory in a computer or digital data processing system or the passage (e.g., reading, writing) of data thereto. (1) Note. The subject matter of this subclass and the subclasses thereunder provides for details of how memory is accessed and controlled. Classification herein requires more than nominal recitation of accessing or controlling memory in the context of digital data processing systems or data processing. Examples of significant memory accessing and control data processing include transferring and modifying memory address data, selecting memory devices or memory locations, and scheduling memory accesses"

711/165: "This class/subclass definition merely repeats the 711/100 definition which includes the following Note 6: "(6) Note. Subject matter classified herein may include nominal recitations of reliability and availability in combination with memory accessing and control. The species of reliability and availability related to data archiving, backup, and device access limiting and security combined with memory accessing and controlling is classified herein. Other species of reliability and availability combined with memory accessing and controlling are classified elsewhere. See the SEARCH THIS CLASS, SUBCLASS notes below."

The "SEARCH THIS CLASS, SUBCLASS" for 711/165 note merely states:

"SEE OR SEARCH THIS CLASS, SUBCLASS: 161+, for reliability and availability combined with memory accessing and control provided for in this array. See the (6) Note for subclass 100 above. "

Significantly, the requirement's definition of Groups I and II includes nothing relating to "reliability and availability" in the conjunctive or alternative. Specifically, the requirement's characterization that "Invention I ... is directed to a device for running an application wherein the code resources of the application are shuffled during execution" and "Invention II ... is directed to a system for protecting a software application by relocating a code resource of the application in such a way that an address identifying the code resource is changed to a new address" facially have no relationship to "reliability and availability." Accordingly, the redefinition of classification of the Group I claims after examination to 711/165 appears to be incorrect. Consequently, the conclusion that a search burden exists because Group I is classified in 711/165 is based upon an incorrect premise, and therefore unfounded.

WHY IS ONLY GROUP II CLASSIFIED IN CLASS 711, SUBCLASS 202, AND HOW CAN THAT RESULT IN A SEARCH BURDEN?

The NFOA also identifies class 711/200 as the classification for Group II. The definition of 711/200 is:

711/200: "200 ADDRESS FORMATION: This subclass is indented under the class definition. Subject matter comprising means or steps for determining or modifying a value which specifies a location in at least one memory. (1) Note. The subject matter of this subclass and the subclasses thereunder includes, for example, virtual memory addressing, address translation, translation look-aside buffers (TLBs), boundary checking, and page-mode addressing. (2) Note. The subject matter also includes deriving new address data from existing address data. (3) Note. The location

in memory may include data for forming further an address (e.g., address mapping is classified herein). (4) Note. Means or steps for addressing or for storing data in one or more memory cells of a storage medium having one or more specific, internal cell elements is classified elsewhere. See the SEARCH CLASS notes below. "

The classification of both Groups I and II ("Invention I ... is directed to a device for running an application wherein the code resources of the application are shuffled during execution" and "Invention II ... is directed to a system for protecting a software application by relocating a code resource of the application) clearly indicate that addresses in memory are changed. Group I is characterized as "during execution" and Group II is characterized as "relocating". Accordingly, to the extent that class 711/200 is relevant to either Group I or Group II, it is also relevant to both groups and therefore would need to be searched for both groups. Accordingly, the requirement's classification of Group II as in 711/200 shows no search burden for examination of both groups of claims.

SUMMARY

Inspection of the requirements for search indicates that, based upon the prior search and based upon the class/subclass definitions in comparison go the requirement's characterizations of Groups I and II, there is no search burden in examining both groups together. Therefore, the restriction requirement should be withdrawn and all claims examined.

CLAIMS

Dependent withdrawn claims (59-63 and 87-92) have been amended to depend from elected claim 75 in the elected group, with changes to the language to conform to antecedent in claim 75. The examiner should determine if these claims are now elected, but if not, upon allowance of the elected claims, also examine and allow these claims pursuant to MPEP guidance.

The applicant notes that if the restriction is withdrawn and depending upon prosecution, the applicant may desire to restore the dependencies changed by this amendment.

An editorial error in claim 75 is corrected.

/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

Date/Time: January 23, 2014 (1:36pm)
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IN THE CLAIMS

1-57. (Canceled)

58. (Previously Presented) A computing device for running application software, comprising:

memory designed to store data in non transitory form;

an operating system;

wherein said memory stores an application software;

wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;

wherein said application software is designed to call said memory scheduler code resource;

wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory.

59. (Currently Amended) The ~~device~~ system of claim ~~58~~ 75 wherein said first one of said plurality of executable code resources is a memory scheduler code resource, wherein, when said memory scheduler code resource is called, said memory scheduler code resource functions to shuffle said memory scheduler code resource randomly in memory.

60. (Currently Amended) The device of claim 58 further comprising a program counter designed to store values and wherein said ~~computing device~~ system is designed to enable application software to modify a value stored by said program counter.

61. (Previously Presented) The device of claim 58 further comprising a program counter designed to store values and wherein said memory scheduler code resource is designed to modify a value stored by said program counter.

62. (Previously Presented) The device of claim 58 wherein said memory scheduler code resource is designed to modify a stack frame, to modify a calling address, and to copy itself to a memory location associated with said calling address.

63. (Previously Presented) The device of claim 58 wherein said system comprises an operating system, and wherein said operating system is designed to call said memory scheduler code resource.

64. (Previously Presented) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in memory, wherein each one of said addresses is associated with one of the code resources.

65. (Currently Amended) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in memory associated with said other code resources in memory.

66. (Previously Presented) The device of claim 58 further comprising a processor for processing instructions defined by said application software.

67. (Previously Presented) A system for executing application software code, comprising:

a memory designed to store data in non transitory form, and storing executable code resources;

wherein said executable code resources comprise a memory scheduler and other executable code resources; and

wherein said memory scheduler is designed to shuffle said other executable code resources in memory.

68. (Previously Presented) The system of claim 67, wherein said memory scheduler is designed to shuffle said other executable code resources randomly in memory.

69. (Previously Presented) The system of claim 67 wherein said shuffling results in a change in location in memory of at least one of said other executable code resources.

70. (Previously Presented) The system of claim 67 further comprising a program counter designed to store values and wherein said memory scheduler is designed to modify values stored by said program counter.

71. (Previously Presented) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of at least one code resource.

72. (Previously Presented) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of said other executable code resources.

73. (Previously Presented) The system of claim 67 designed to call said memory scheduler.

74. (Previously Presented) The system of claim 67 wherein said application software code is designed to call said memory scheduler.

75. (Currently Amended) A system for executing an application on a computer system, comprising:

a computer system comprising a processor for processing instructions and memory designed to store data in non transitory form;

a software application comprising a plurality of executable code resources loaded in said

memory;

said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources; and

wherein a first one of said plurality of executable code ~~resource~~ resources is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor.

76. (Previously Presented) The system of claim 75 wherein said first one is also designed to relocate itself to a different address during execution of said software application.

77. (Previously Presented) The system of claim 75 wherein said system is designed to repeatedly relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application.

78. (Previously Presented) A system comprising:

a processor;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least one code resource into a different location in said memory after said program has been loaded into memory.

79. (Previously Presented) A system comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load an executable program comprising at least two code resources into memory and to randomize the location of at least one of the at least two code resources in the memory, using said processor

80. (Previously Presented) A system for executing a program comprising code, comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

a memory scheduler configured to randomize location of code defining a code resource,

in memory, using said processor.

81. (Previously Presented) The system of claim 80 wherein said memory scheduler is configured to repeatedly randomize the location of at least a portion of the code in memory.

82. (Previously Presented) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that have sequential addresses in said memory.

83. (Previously Presented) The device of claim 82 wherein each one of said other code resources defines a function or procedure.

84. (Previously Presented) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that immediately follow one another in said memory.

85. (Previously Presented) The device of claim 84 wherein each one of said other code resources defines a function or procedure.

86. (Previously Presented) A system comprising:

a processor;

a memory coupled to said processor;

wherein said memory is designed to store data in non transitory form;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least a subset of said code resources into a different location in said memory after said program has been loaded into memory.

87. (Currently Amended) The system of claim ~~86~~ 75 wherein ~~each one of said subset of said code resources~~ each one of said plurality of executable code resources loaded in said memory comprises a sequence of instructions that have sequential addresses in said memory.

88. (Currently Amended) The system of claim 87 wherein each one of said ~~subset of said code resources~~ plurality of executable code resources loaded in said memory defines a function or procedure.

89. (Currently Amended) The system of claim 88 wherein each one of said ~~subset of said code resources~~ plurality of executable code resources loaded in said memory is an essential code resource.

90. (Currently Amended) The system of claim ~~86~~ 75 wherein each one of ~~said other code resources~~ plurality of executable code resources loaded in said memory comprises a sequence of instructions, and the sequence of instructions for one of said plurality of executable code resources loaded in said memory have memory addresses that immediately follow one another ~~in said memory~~.

91. (Currently Amended) The system of claim 90 wherein each one of said ~~other code resources~~ plurality of executable code resources loaded in said memory defines a function or procedure.

92. (Currently Amended) The system of claim 90 wherein said system comprises one of a set-top box; a cellular phone; a smart television; and a personal digital assistant (PDA).

93. (Previously Presented) A system for protecting a software application, said software application comprising a plurality of executable code resources, said system comprising:

a processor;

memory for storing data in non transitory form;

wherein said processor is coupled to said memory for transmitting data there between;

wherein, when executing said software application, said system is configured to store in said memory an original plurality of code resource addresses;

wherein, when executing said software application, said system is configured to store a plurality of executable code resources for executing said software application;

wherein each one of said original plurality of code resource addresses specifies an address in said memory of one of said plurality of executable code resources;

wherein, when executing said software application, said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

94. (Previously Presented) The system of claim 93, wherein said system is configured to repeatedly (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

95. (Previously Presented) The system of claim 93, wherein each one of said plurality of executable code resources are relocated to random addresses.

96. (Previously Presented) The system of claim 93, wherein each one said original plurality of code resource addresses specifies an entry point of an original location in memory of each one of said plurality of executable code resources.

97. (Previously Presented) The system of claim 93, wherein each one said new

plurality of code resource addresses specifies an entry point of a new location in memory of each one of said plurality of executable code resources.

98. (Previously Presented) The system of claim 93 wherein each one of said plurality of executable code resources is stored in sequential locations in said memory.

99. (Previously Presented) The system of claim 98 wherein each one of said plurality of executable code resources executes a function or procedure when called.

100. (Previously Presented) The system of claim 93 wherein each one of said plurality of executable code resources executes a function or procedure

101. (Previously Presented) The system of claim 93 wherein said system is configured to relocate each one of said plurality of executable code resources in said memory in response to a call to an executable code resource of said plurality of executable code resources.

102. (Previously Presented) The system of claim 101 wherein said system is configured to update a reference to said executable code resource in said memory of the computer system to reflect a new address in said memory of said executable code resource resulting from relocating said executable code resource.

103. (Previously Presented) The system of claim 102 wherein said executable code resource is a memory scheduler which is called periodically, or at random or pseudo random intervals.

104. (Previously Presented) A computer program product storing in non-transitory form instructions defining a software program, which instructions, when loaded into a computer system instruct the computer system to perform the following steps:

load said program into memory of said computer system;

wherein said program, as loaded into memory, comprises a plurality of code resources;

shuffle locations in memory of at least a subset of said plurality of code resources.

105. (Currently Amended) The ~~system~~ product of claim 104 wherein each one of said subset of said code resources comprises a sequence of instructions that have sequential addresses in said memory.

106. (Currently Amended) The ~~system~~ product of claim 104 wherein each one of said subset of said code resources defines a function or procedure.

107. (Currently Amended) The ~~system~~ product of claim 104 wherein said subset comprises at least two code resources.

Date/Time: January 23, 2014 (1:36pm)
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Electronic Acknowledgement Receipt	
EFS ID:	18002618
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Richard A. Neifeld
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	23-JAN-2014
Filing Date:	24-JUL-2012
Time Stamp:	13:39:45
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		Amendment_SCOT0014-6_1-23-2014.pdf	193828 6d49d34f205cc8a24810f19d3c6c06c28c7187de	yes	12

	Multipart Description/PDF files in .zip description		
	Document Description	**Start**	**End**
	Response to Election / Restriction Filed	1	5
	Claims	6	12
Warnings:			
Information:			
	Total Files Size (in bytes):	193828	

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**0
Number of independent claims previously paid for: 8
Number of independent claims in excess of 8: 0.
Number of total claims previously paid for: 26
Number of total claims in excess of 26: 0
3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
37 CFR 1.111 PRELIMINARY AMENDMENT - REMARKS
37 CFR 1.111 PRELIMINARY AMENDMENT - CLAIMS

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: PClaw BankAcct, G/L: **6, 5010**
PCLAW BILLING REFERENCE: [SCOT0001]
Check#, Entry date, Amount: [1916, 9/19/13, 4320.00]
Service Fees: Amount/CreditAtty/Entry date/Services: []

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: ran
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 1-23-2014 **SIGNATURE**: /RichardNeifeld/
Printed: January 23, 2014 (1:50pm) RICHARD NEIFELD, REG. NO. 35,299
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\Amendment_SCOT0014-6_1-23-2014.wpd

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE

37 CFR 1.111 AMENDMENT REMARKS

This is a response to the Non Final Office Action (NF)A) dated 1/14/2014.

SUMMARY OF NON FINAL OFFICE ACTION

This response updates the claim status indicators to show claims withdrawn by election, but is otherwise identical to the response filed earlier on the same date.

The NFOA requires restriction of claims 58-107. Item 2 of the NFOA states that:

2. Restriction to one of the following inventions is required under 35 U.S.C. 121:
I. Claims 58-74, 78, 79, 80-92 and 104-107, drawn to a device for running of application software and shuffling code resources in memory during execution, classified in class 711, subclass 165.
II. Claims 75-77 and 93-103, drawn to a system for protecting a software application wherein an address identifying a code resource is relocated so that the code resource is specified by a new address, classified in class 711, subclass 202.

The NFOA further states that:

Invention I is distinct from invention II and is directed to a device for running an application wherein the code resources of the application are shuffled during execution.
Invention II is distinct from invention I and is directed to a system for protecting a software application by relocating a code resource of the application in such a way that an address identifying the code resource is changed to a new address.

In response, the applicant elects **Group II, Claims 75-77 and 93-103**, but with traverse of the requirement.

Traverse is on the grounds that there is no substantial burden in examining these claims, and no showing of such a burden, for several reasons. First, the examiner previously issued an action on the merits, which required both search and examination based upon the relevant art. Second, the requirement redefines the classification of Group I as class 711, subclass 165, a class not previously searched for Group I, and that redefinition is apparently incorrect. Third, the definition of Group II as class 711, subclass 202, provides no indication of a search burden, because the definition of class 711, subclass 202, to the NFOA's characterization of Groups I and II indicates, that if class 711, subclass 202 is relevant to Group II, then it is also relevant to Group I. In summary, even assuming the revised classification of Group I claims is correct, there is no indication of any search burden.

WHY IS GROUP I RECLASSIFIED TO CLASS 711, SUBCLASS 165?

The reasoning in the requirement that the Group I claims are classified in class 711, subclass 165, is not consistent with the prior determination of the examiner that the only subclasses of class 711 relevant to the Group I claims were subclasses 1 and 100.

The examiner previously searched the claims defined by group I and included in that search only 711/1 and 711/100 in class 711, as shown in the search notes of the prior office action. The definition of those classes and 711/165 follow.

711/1: "ADDRESSING COMBINED WITH SPECIFIC MEMORY CONFIGURATION OR SYSTEM:
This subclass is indented under the class definition. Subject matter comprising means or steps

for determining one or more values (i.e., address data) that specify one or more locations in a storage medium wherein the means or steps are claimed in combination with a particular configuration or system for storing data."

711/100: "STORAGE ACCESSING AND CONTROL:
This subclass is indented under the class definition. Subject matter comprising means (e.g., a processor, a controller, etc.) or steps for governing memory in a computer or digital data processing system or the passage (e.g., reading, writing) of data thereto. (1) Note. The subject matter of this subclass and the subclasses thereunder provides for details of how memory is accessed and controlled. Classification herein requires more than nominal recitation of accessing or controlling memory in the context of digital data processing systems or data processing. Examples of significant memory accessing and control data processing include transferring and modifying memory address data, selecting memory devices or memory locations, and scheduling memory accesses"

711/165: "This class/subclass definition merely repeats the 711/100 definition which includes the following Note 6: "(6) Note. Subject matter classified herein may include nominal recitations of reliability and availability in combination with memory accessing and control. The species of reliability and availability related to data archiving, backup, and device access limiting and security combined with memory accessing and controlling is classified herein. Other species of reliability and availability combined with memory accessing and controlling are classified elsewhere. See the SEARCH THIS CLASS, SUBCLASS notes below."

The "SEARCH THIS CLASS, SUBCLASS" for 711/165 note merely states:

"SEE OR SEARCH THIS CLASS, SUBCLASS: 161+, for reliability and availability combined with memory accessing and control provided for in this array. See the (6) Note for subclass 100 above. "

Significantly, the requirement's definition of Groups I and II includes nothing relating to "reliability and availability" in the conjunctive or alternative. Specifically, the requirement's characterization that "Invention I ... is directed to a device for running an application wherein the code resources of the application are shuffled during execution" and "Invention II ... is directed to a system for protecting a software application by relocating a code resource of the application in such a way that an address identifying the code resource is changed to a new address" facially have no relationship to "reliability and availability." Accordingly, the redefinition of classification of the Group I claims after examination to 711/165 appears to be incorrect. Consequently, the conclusion that a search burden exists because Group I is classified in 711/165 is based upon an incorrect premise, and therefore unfounded.

WHY IS ONLY GROUP II CLASSIFIED IN CLASS 711, SUBCLASS 202, AND HOW CAN THAT RESULT IN A SEARCH BURDEN?

The NFOA also identifies class 711/200 as the classification for Group II. The definition of 711/200 is:

711/200: "200 ADDRESS FORMATION: This subclass is indented under the class definition. Subject matter comprising means or steps for determining or modifying a value which specifies a location in at least one memory. (1) Note. The subject matter of this subclass and the subclasses thereunder includes, for example, virtual memory addressing, address translation, translation

look-aside buffers (TLBs), boundary checking, and page-mode addressing. (2) Note. The subject matter also includes deriving new address data from existing address data. (3) Note. The location in memory may include data for forming further an address (e.g., address mapping is classified herein). (4) Note. Means or steps for addressing or for storing data in one or more memory cells of a storage medium having one or more specific, internal cell elements is classified elsewhere. See the SEARCH CLASS notes below. "

The classification of both Groups I and II ("Invention I ... is directed to a device for running an application wherein the code resources of the application are shuffled during execution" and "Invention II ... is directed to a system for protecting a software application by relocating a code resource of the application) clearly indicate that addresses in memory are changed. Group I is characterized as "during execution" and Group II is characterized as "relocating". Accordingly, to the extent that class 711/200 is relevant to either Group I or Group II, it is also relevant to both groups and therefore would need to be searched for both groups. Accordingly, the requirement's classification of Group II as in 711/200 shows no search burden for examination of both groups of claims.

SUMMARY

Inspection of the requirements for search indicates that, based upon the prior search and based upon the class/subclass definitions in comparison go the requirement's characterizations of Groups I and II, there is no search burden in examining both groups together. Therefore, the restriction requirement should be withdrawn and all claims examined.

CLAIMS

Dependent withdrawn claims (59-63 and 87-92) have been amended to depend from elected claim 75 in the elected group, with changes to the language to conform to antecedent in claim 75. The examiner should determine if these claims are now elected, but if not, upon allowance of the elected claims, also examine and allow these claims pursuant to MPEP guidance.

The applicant notes that if the restriction is withdrawn and depending upon prosecution, the applicant may desire to restore the dependencies changed by this amendment.

An editorial error in claim 75 is corrected.

/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

Date/Time: January 23, 2014 (1:50pm)
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IN THE CLAIMS

1-57. (Canceled)

58. (Withdrawn) A computing device for running application software, comprising:

memory designed to store data in non transitory form;

an operating system;

wherein said memory stores an application software;

wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;

wherein said application software is designed to call said memory scheduler code resource;

wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory.

59. (Withdrawn, Currently Amended) The ~~device~~ system of claim ~~58~~ 75 wherein said first one of said plurality of executable code resources is a memory scheduler code resource, wherein, when said memory scheduler code resource is called, said memory scheduler code resource functions to shuffle said memory scheduler code resource randomly in memory.

60. (Withdrawn, Currently Amended) The device of claim 58 further comprising a program counter designed to store values and wherein said ~~computing device~~ system is designed to enable application software to modify a value stored by said program counter.

61. (Withdrawn) The device of claim 58 further comprising a program counter designed to store values and wherein said memory scheduler code resource is designed to modify a value stored by said program counter.

62. (Withdrawn) The device of claim 58 wherein said memory scheduler code resource is designed to modify a stack frame, to modify a calling address, and to copy itself to a memory location associated with said calling address.

63. (Withdrawn, Currently Amended) The device of claim 58 wherein said system comprises an operating system, and wherein said operating system is designed to call said memory scheduler code resource.

64. (Withdrawn) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in memory, wherein each one of said addresses is associated with one of the code resources.

65. (Withdrawn, Currently Amended) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in memory associated with said other

code resources in memory.

66. (Withdrawn) The device of claim 58 further comprising a processor for processing instructions defined by said application software.

67. (Withdrawn) A system for executing application software code, comprising:

a memory designed to store data in non transitory form, and storing executable code resources;

wherein said executable code resources comprise a memory scheduler and other executable code resources; and

wherein said memory scheduler is designed to shuffle said other executable code resources in memory.

68. (Withdrawn) The system of claim 67, wherein said memory scheduler is designed to shuffle said other executable code resources randomly in memory.

69. (Withdrawn) The system of claim 67 wherein said shuffling results in a change in location in memory of at least one of said other executable code resources.

70. (Withdrawn) The system of claim 67 further comprising a program counter designed to store values and wherein said memory scheduler is designed to modify values stored by said program counter.

71. (Withdrawn) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of at least one code resource.

72. (Withdrawn) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of said other executable code resources.

73. (Withdrawn) The system of claim 67 designed to call said memory scheduler.

74. (Withdrawn) The system of claim 67 wherein said application software code is designed to call said memory scheduler.

75. (Currently Amended) A system for executing an application on a computer system, comprising:

a computer system comprising a processor for processing instructions and memory designed to store data in non transitory form;

a software application comprising a plurality of executable code resources loaded in said memory;

said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources; and

wherein a first one of said plurality of executable code ~~resource~~ resources is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor.

76. (Previously Presented) The system of claim 75 wherein said first one is also designed to relocate itself to a different address during execution of said software application.

77. (Previously Presented) The system of claim 75 wherein said system is designed to repeatedly relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application.

78. (Withdrawn) A system comprising:

a processor;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least one code resource into a different location in said memory after said program has been loaded into memory.

79. (Withdrawn) A system comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load an executable program comprising at least two code resources into memory and to randomize the location of at least one of the at least two code resources in the memory, using said processor

80. (Withdrawn) A system for executing a program comprising code, comprising:

a processor designed to process instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

a memory scheduler configured to randomize location of code defining a code resource, in memory, using said processor.

81. (Withdrawn) The system of claim 80 wherein said memory scheduler is configured to repeatedly randomize the location of at least a portion of the code in memory.

82. (Withdrawn) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that have sequential addresses in said memory.

83. (Withdrawn) The device of claim 82 wherein each one of said other code resources defines a function or procedure.

84. (Withdrawn) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that immediately follow one another in said memory.

85. (Withdrawn) The device of claim 84 wherein each one of said other code resources defines a function or procedure.

86. (Withdrawn) A system comprising:

a processor;

a memory coupled to said processor;

wherein said memory is designed to store data in non transitory form;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least a subset of said code resources into a different location in said memory after said program has been loaded into memory.

87. (Withdrawn, Currently Amended) The system of claim ~~86~~ 75 wherein ~~each one of said subset of said code resources~~ each one of said plurality of executable code resources loaded in said memory comprises a sequence of instructions that have sequential addresses in said memory.

88. (Withdrawn, Currently Amended) The system of claim 87 wherein each one of said ~~subset of said code resources~~ plurality of executable code resources loaded in said memory defines a function or procedure.

89. (Withdrawn, Currently Amended) The system of claim 88 wherein each one of said ~~subset of said code resources~~ plurality of executable code resources loaded in said memory is an essential code resource.

90. (Withdrawn, Currently Amended) The system of claim ~~86~~ 75 wherein each one of ~~said other code resources~~ plurality of executable code resources loaded in said memory comprises a sequence of instructions, and the sequence of instructions for one of said plurality of executable code resources loaded in said memory have memory addresses that immediately follow one another ~~in said memory~~.

91. (Withdrawn, Currently Amended) The system of claim 90 wherein each one of said ~~other code resources~~ plurality of executable code resources loaded in said memory defines a function or procedure.

92. (Withdrawn, Currently Amended) The system of claim 90 wherein said system comprises one of a set-top box; a cellular phone; a smart television; and a personal digital

assistant (PDA).

93. (Previously Presented) A system for protecting a software application, said software application comprising a plurality of executable code resources, said system comprising:

a processor;

memory for storing data in non transitory form;

wherein said processor is coupled to said memory for transmitting data there between;

wherein, when executing said software application, said system is configured to store in said memory an original plurality of code resource addresses;

wherein, when executing said software application, said system is configured to store a plurality of executable code resources for executing said software application;

wherein each one of said original plurality of code resource addresses specifies an address in said memory of one of said plurality of executable code resources;

wherein, when executing said software application, said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

94. (Previously Presented) The system of claim 93, wherein said system is configured to repeatedly (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

95. (Previously Presented) The system of claim 93, wherein each one of said plurality of executable code resources are relocated to random addresses.

96. (Previously Presented) The system of claim 93, wherein each one said original plurality of code resource addresses specifies an entry point of an original location in memory of each one of said plurality of executable code resources.

97. (Previously Presented) The system of claim 93, wherein each one said new plurality of code resource addresses specifies an entry point of a new location in memory of each one of said plurality of executable code resources.

98. (Previously Presented) The system of claim 93 wherein each one of said plurality of executable code resources is stored in sequential locations in said memory.

99. (Previously Presented) The system of claim 98 wherein each one of said plurality of executable code resources executes a function or procedure when called.

100. (Previously Presented) The system of claim 93 wherein each one of said plurality of executable code resources executes a function or procedure

101. (Previously Presented) The system of claim 93 wherein said system is configured to relocate each one of said plurality of executable code resources in said memory in response to a call to an executable code resource of said plurality of executable code resources.

102. (Previously Presented) The system of claim 101 wherein said system is configured to update a reference to said executable code resource in said memory of the computer system to reflect a new address in said memory of said executable code resource resulting from relocating said executable code resource.

103. (Previously Presented) The system of claim 102 wherein said executable code resource is a memory scheduler which is called periodically, or at random or pseudo random intervals.

104. (Withdrawn) A computer program product storing in non-transitory form instructions defining a software program, which instructions, when loaded into a computer system instruct the computer system to perform the following steps:

load said program into memory of said computer system;

wherein said program, as loaded into memory, comprises a plurality of code resources;

shuffle locations in memory of at least a subset of said plurality of code resources.

105. (Withdrawn, Currently Amended) The ~~system~~ product of claim 104 wherein each one of said subset of said code resources comprises a sequence of instructions that have sequential addresses in said memory.

106. (Withdrawn, Currently Amended) The ~~system~~ product of claim 104 wherein each one of said subset of said code resources defines a function or procedure.

107. (Withdrawn, Currently Amended) The ~~system~~ product of claim 104 wherein said subset comprises at least two code resources.

Date/Time: January 23, 2014 (1:50pm)
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Electronic Acknowledgement Receipt

EFS ID:	18002951
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Richard A. Neifeld
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	23-JAN-2014
Filing Date:	24-JUL-2012
Time Stamp:	13:55:49
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		Amendment_SCOT0014-6_1-23-2014.pdf	198806 0d7928f088830af5adaaba45b89fd9e907b77d92	yes	11

	Multipart Description/PDF files in .zip description		
	Document Description	**Start**	**End**
	Response to Election / Restriction Filed	1	5
	Claims	6	11
Warnings:			
Information:			
	Total Files Size (in bytes):	198806	

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New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

PTO/SB/06 (09-11)
Approved for use through 1/31/2014. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 13/556,420	Filing Date 07/24/2012	☐ To be Mailed

ENTITY: ☒ LARGE ☐ SMALL ☐ MICRO

APPLICATION AS FILED – PART I

	(Column 1)	(Column 2)		
FOR	NUMBER FILED	NUMBER EXTRA	RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).			
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))				
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL	

APPLICATION AS AMENDED – PART II

		(Column 1)		(Column 2)	(Column 3)		
AMENDMENT	01/23/2014	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	* 50	Minus	** 50	= 0	X $80 =	0
	Independent (37 CFR 1.16(h))	* 9	Minus	***9	= 0	X $420 =	0
	☐ Application Size Fee (37 CFR 1.16(s))						
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
						TOTAL ADD'L FEE	0

		(Column 1)		(Column 2)	(Column 3)		
AMENDMENT		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =	
	☐ Application Size Fee (37 CFR 1.16(s))						
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
						TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

LIE
/LINDA A. WASHINGTON/

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

31518 7590 01/14/2014

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4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

EXAMINER	
OKEKE, IZUNNA	
ART UNIT	PAPER NUMBER
2432	

NOTIFICATION DATE	DELIVERY MODE
01/14/2014	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

general@neifeld.com
rneifeld@neifeld.com
rhahl@neifeld.com

PTOL-90A (Rev. 04/07)

Office Action Summary	Application No. 13/556,420	Applicant(s) MOSKOWITZ, SCOTT A.	
	Examiner IZUNNA OKEKE	Art Unit 2432	AIA (First Inventor to File) Status No

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTHS FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *09/23/2013*.

☐ A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on _____.

2a)☐ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.

4)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims*

5)☒ Claim(s) *58-107* is/are pending in the application.

5a) Of the above claim(s) _____ is/are withdrawn from consideration.

6)☐ Claim(s) _____ is/are allowed.

7)☐ Claim(s) _____ is/are rejected.

8)☐ Claim(s) _____ is/are objected to.

9)☒ Claim(s) *58-107* are subject to restriction and/or election requirement.

* If any claims have been determined allowable, you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to PPHfeedback@uspto.gov.

Application Papers

10)☐ The specification is objected to by the Examiner.

11)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

Certified copies:

a)☐ All b)☐ Some** c)☐ None of the:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

** See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)

2) ☐ Information Disclosure Statement(s) (PTO/SB/08a and/or PTO/SB/08b) Paper No(s)/Mail Date _____.

3) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .

4) ☐ Other: _____.

DETAILED ACTION

1. The present application is being examined under the pre-AIA first to invent provisions.

Election/Restrictions

2. Restriction to one of the following inventions is required under 35 U.S.C. 121:

I. Claims 58-74, 78, 79, 80-92 and 104-107, drawn to a device for running of application software and shuffling code resources in memory during execution, classified in class 711, subclass 165.

II. Claims 75-77 and 93-103, drawn to a system for protecting a software application wherein an address identifying a code resource is relocated so that the code resource is specified by a new address, classified in class 711, subclass 202.

3. The inventions are distinct, each from the other because of the following reason: Inventions I and II are directed to related processes.

Invention I is distinct from invention II and is directed to a device for running an application wherein the code resources of the application are shuffled during execution.

Invention II is distinct from invention I and is directed to a system for protecting a software application by relocating a code resource of the application in such a way that an address identifying the code resource is changed to a new address.

4. The related inventions are distinct if: (1) the inventions as claimed are either not capable of use together or can have a materially different design, mode of operation, function, or effect; (2) the inventions do not overlap in scope, i.e., are mutually exclusive; and (3) the inventions as claimed are not obvious variants. See MPEP § 806.05(j). In the instant case, the inventions as claimed has separate utility as Invention I is directed to a device for shuffling code resources

during execution. Invention II has separate utility as it is directed to a system for changing the address of code resources in memory.

5. Restriction for examination purposes as indicated is proper because all these inventions listed in this action are independent or distinct for the reasons given above and there would be a serious search and/or examination burden if restriction were not required because at least the following reason(s) apply:

(a) the inventions have acquired a separate status in the art in view of their different classification;

(b) the inventions have acquired a separate status in the art due to their recognized divergent subject matter;

(c) the inventions require a different field of search (for example, searching different classes/subclasses or electronic resources, or employing different search queries);

(d) the prior art applicable to one invention would not likely be applicable to another invention;

(e) the inventions are likely to raise different non-prior art issues under 35 U.S.C. 101 and/or 35 U.S.C. 112, first paragraph.

Applicant is advised that the reply to this requirement to be complete must include (i) an election of an invention to be examined even though the requirement may be traversed (37 CFR 1.143) **and (ii) identification of the claims encompassing the elected invention**.

The election of an invention may be made with or without traverse. To reserve a right to petition, the election must be made with traverse. If the reply does not distinctly and specifically point out supposed errors in the restriction requirement, the election shall be treated as an

election without traverse. Traversal must be presented at the time of election in order to be considered timely. Failure to timely traverse the requirement will result in the loss of right to petition under 37 CFR 1.144. If claims are added after the election, applicant must indicate which of these claims are readable upon the elected invention.

Should applicant traverse on the ground that the inventions are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the inventions to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103 or pre-AIA 35 U.S.C. 103(a) of the other invention.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to IZUNNA OKEKE whose telephone number is (571)270-3854. The examiner can normally be reached on Monday - Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on (571) 272-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/IZUNNA OKEKE/
Examiner, Art Unit 2432

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE

37 CFR 1.111 AMENDMENT REMARKS

SUMMARY

This amendment responds to the non final office action (NFOA) dated 3/28/2013.

Claims 58-81 were pending and examined. Claims 82-107 are new, including the new independent claims noted in the subsection titled "INDEPENDENT CLAIMS: 67; 75; 78; 79; 80 93; and 104."

The NFOA rejects Claims 58-81 under 35 USC 102(b) based upon Ostrovsky et al., USP 5,123,045. In summary, Ostrovsky does not disclose or suggest shuffling in memory of code resources of a software program. Our claims define those limitations.

OSTROVSKY

Ostrovsky's goal is to "provide[] protection of the pattern of access to memory during execution of a program." Abstract. Ostrovsky accomplishes that goal by providing a system in which "values stored in the general purpose memory are hidden ... [and] the sequence in which memory location are accessed during execution is hidden." Abstract.

Ostrovsky discloses a data processing system including protected CPU and protected memory (Fig. 1, elements 10, 12; col. 1 lines 39-47) which communicates over unprotected data bus 20 with unprotected random access memory (RAM) (fig. 1, element 14; col. 4 lines 53-57). Ostrovsky discloses that "The data processing system further includes a plurality of 'buffer' data structures for storing encrypted software and data in an unprotected memory." Col. 1 lines 44-47. This refers to data structures in the unprotected RAM, element 14. Col. 4 lines 60-61.

Ostrovsky discloses storing values defining a program in a random order (as generated by a pseudo random number generator) in unprotected RAM. Ostrovsky states "The method of protection comprises initially permuting the order in which values are stored in the unprotected addressable locations prior to the beginning of execution of the program." Col. 2 lines 21-24. These values include the data and instructions of the software program. Ostrovsky states "The types of protection embodied with the preferred embodiment include protection of the access pattern to memory by the program and protection of *values held in memory such as data and instructions*." Col. 4 lines 15-18.

Ostrovsky defines a virtual address as follows: "A virtual address is the address referenced by the program and is distinct from a physical address which identifies the physical location where the virtual memory location resides." Col. 6 lines 23-27. Ostrovsky states "The virtual memory locations that make up the program and the data are stored in physical addresses specified by a pseudo-random function of the virtual addresses." Col. 3 lines 3-6. In the Detailed Description, Ostrovsky clarifies that "The code and data constitute a plurality of virtual memory locations. They are not stored in the order in which their corresponding virtual addresses

are sequenced; rather, they are stored in a pseudo-random sequence." Col. 6 lines 33-36.

STORING OF *PROGRAM VALUES* IN "A PSEUDO-RANDOM SEQUENCE"

Ostrovsky's storing of *program values* in "a pseudo-random sequence" in the unprotected RAM does not disclose our claimed shuffling of *code resources* in memory. This is because our application discloses that a "code resource" has an entry point and comprises more than one instruction that immediately follows from that entry point. Our specification states that "[0052] The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. **The rest of the instructions comprising that sub-object *immediately follow from the entry point*.** ... These sub-objects can be packaged into what are referred to in certain systems as **'code resources'**,...".

Ostrovsky discloses compiling and executing the program. Ostrovsky states that "[t]he present invention specifies a cryptographic compiler which transforms source-code programs into equivalent compiled programs which run on a general-purpose computer using the protected chip." Col. 1 lines 58-62. And that it is this cryptographically compiled program that resides in the unprotected RAM. "It should be stressed that ... the compiled programs reside in the unprotected, general-purpose random access memory." Col. 2 lines 5-8. Regarding executing the program, Ostrovsky discloses that "the values at the unprotected adjustable locations are accessed in light of the order imposed by the permuting step and by the partially permuting step." Col. 2 lines 30-33.

Ostrovsky then discloses that, because of the permutation, "[s]pecific accesses are achieved within an access pattern which is independent of the original access pattern." Col. 2 lines 33-35. However, Ostrovsky discloses that those accesses of unprotected RAM for executing the software program are interspersed with accesses of unprotected RAM to dummy addresses that do not contain a program instruction or data, in order to fool an adversary. Ostrovsky states "Next, execution of the program is begun. Each time a memory access is desired, the CPU 10 examines each of the buffers for the memory location that is sought. *Only one of the buffers will have the memory location truly sought*. The other memory accesses are dummy accesses designed to fool adversaries." Col. 5 lines 38-43.

MOVING A SINGLE VALUE OF THE SOFTWARE PROGRAM TO A DIFFERENT MEMORY LOCATION

Ostrovsky discloses moving the "desired memory location" that is "truly sought" for "execution of the program," once it is identified. Ostrovsky states "Next, execution of the program is begun. Each time a memory access is desired, the CPU 10 examines each of the buffers for the memory location that is sought. Only one of the buffers will have the memory location truly sought. The other memory accesses are dummy accesses designed to fool adversaries. *When a desired memory location is found, it is moved up to the level 1 buffer.*" Col. 5 lines 38-44. This passage in Ostrovsky clarifies that individual memory locations, which correspond to an individual machine instructions of data associated with the software program, are moved, *one at a time*, independent of movement of any other instruction or data associated with the memory. Furthermore, Ostrovsky discloses that sequential values of the program found in the foregoing process and stored in buffer 1 are not stored in sequential locations in buffer 1. Instead, Ostrovsky discloses that each found value of the program is stored in the next sequential empty bucket in buffer 1, and that buckets are not a single memory location but a set or sequental locations. "Store value v of the virtual memory location L from B into first empty bucket of level 1 buffer" and that "each bucket constitutes a set of memory locations". Col. 9 lines 37-38. Thus, Ostrovsky discloses that sequentially accessed instructions in the compiled software

program are not stored in the unprotected RAM in sequential memory addresses.

Moreover, Ostrovsky discloses that when a buffer becomes full, the contents of that buffer are transferred to another buffer and then "obliviously rehashed" which means pseudo randomly reordering the contents of the memory locations containing each value of the program. Col. 9 lines 1-11.

Ostrovsky's moving of single found value of the software program, independent of other values of the software program, in the unprotected RAM, does not disclose our claimed shuffling of code resources in memory. This is because our application discloses that a "code resource" has an entry point and comprises more than one instruction that immediately follows from that entry point. Our specification states that "[0052] The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. **The rest of the instructions comprising that sub-object immediately follow from the entry point**. ... These sub-objects can be packaged into what are referred to in certain systems as '**code resources**',...". Ostrovsky's moving values one at a time to non sequential locations in memory is not our claimed shuffling of a code resource because it does not maintain a sequence of instructions that **immediately follow from the entry point**.

PARTIALLY PERMUTING THE ORDER OF SOFTWARE PROGRAM VALUES

Ostrovsky discloses "the order in which values are stored in subsets of the unprotected adjustable locations is partially permuted at various times during the execution of the program." Col. 2 lines 27-30. Regarding the partial permutation, Ostrovsky states "The partial permuting step includes the step of transferring values from one subset of the unprotected adjustable locations to another subset of the unprotected adjustable locations," (col. 2 lines 42-44) and "Given that each subset may be of a different size, the frequency with which the various subsets are permuted differs" (col. 2 lines 48-49) and "Each time the accessible memory is shuffled (permuted), a counter is incremented" (col. 3 lines 48-49) and "All the non-empty buckets are moved together by obliviously repermuting B at the bucket level" (col. lines 53-54) and "As fixed intervals (i.e. when a buffer may be potentially full) the buffer's contents are moved to a next higher priority buffer and are pseudo-randomly rearranged so that the order in which the virtual memory locations are held in the next higher priority buffer is shuffled. The movement from the lower order buffer to a higher order buffer is achieved preferably using a pseudo-random function" (col. 3 line 16-24) and finally "Thus, if the second level buffer 17 gets full, it is passed on to the third level buffer 19, and the third level buffer 19 is shuffled" (col. 9 lines 8-10). In the foregoing, Ostrovsky discloses partially permuting meaning transferring of values from one buffer (or bucket, which is a subset of a buffer) to another buffer (or bucket, which is a subset of a buffer). However, Ostrovsky discloses that each buffer or bucket subset of a buffer contains a disordered subset of the values (code and instruction) defining the software program. Further, Ostrovsky fails to disclose any relation between buckets and what we define as code resources, and Ostrovsky fails to disclose any relation between buffers and what we define as code resources. Consequently, Ostrovsky's moving contents of one buffer or bucket to another, moves a disordered set of the software program's instructions from one location to another. There is no entry point immediately followed by the other values required to execute a code resource. Even of a bucket or buffer happened to have all instructions for some code resources, the ordered movement of those bucket or buffer values to another bucket or buffer would not result in a shuffle of the order of the corresponding code resources. **Therefore, Ostrovsky's moving the contents of one buffer to another, or of one bucket to another, does not correspond to our claimed shuffling of code resources.**

Our application discloses that a "code resource" has an entry point and comprises more

than one instruction that immediately follows from that entry point. Our specification states that "[0052] The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. **The rest of the instructions comprising that sub-object immediately follow from the entry point**. ... These sub-objects can be packaged into what are referred to in certain systems as **'code resources'**,...". **Ostrovsky's shuffling of buffers or buckets does not relocate in memory, preserving the sequence in memory, of an entry point of a function or procedure and instructions comprising that sub-object immediately following the entry point**.

OSTROVSKY'S PERMUTING, PARTIALLY PERMUTING, AND MOVING OF VALUES OF A SOFTWARE PROGRAM DOES NOT DISCLOSE WHAT WE CLAIM

In summary, Ostrovsky's initial permuting is limited to permuting of values, "permuting the order in which values are stored". Ostrovsky's relocating of an instruction to buffer 1 does not reorder instructions in sequence because they are move to sequential buckets, not sequential memory locations. Ostrovsky's partially permuting merely shifts values of one buffer to another, en masse, and that moving is not shuffling of a code resource as defined in our specification because the buffers do not include an entry point followed immediately by the instructions defining a subroutine or procedure.

Ostrovsky discloses more details, but these details are not relevant. For example, Ostrovsky discloses encryption by the CPU, association by the CPU of a virtual address with each value of the program/data, and storing that virtual address in association with the corresponding value in unprotected RAM. Ostrovsky discloses plural buffers and buckets intended to confuse an adversary by making calls to RAM unrelated to execution of the program, and shuffling the buffers and buckets to confuse an adversary.

"CODE RESOURCES" DISCLOSURE

Paragraphs [0051] and [0052] from US patent publication 20130014271 of application 13556420, the subject application, are copied below, with more relevant passages emphasized, for the examiner's convenience.

> [0051] An executable computer program is variously referred to as an application, from the point of view of a user, or executable object code from the point of view of the engineer. A collection of smaller, atomic (or indivisible) chunks of object code typically comprise the complete executable object code or application which may also require the presence of certain data resources. These indivisible portions of object code correspond with the programmers' function or procedure implementations in higher level languages, such as C or Pascal. In creating an application, a programmer writes "code" in a higher level language, which is then compiled down into "machine language," or, the executable object code, which can actually be run by a computer, general purpose or otherwise. Each function, or procedure, written in the programming language, represents a self-contained portion of the larger program, and implements, typically, a very small piece of its functionality. The order in which the programmer types the code for the various functions or procedures, and the distribution of and arrangement of these implementations in various files which hold them is unimportant. Within a function or procedure, however, the order of individual language constructs, which correspond to particular machine instructions is important, and so functions or procedures are considered indivisible for purposes of this discussion. **That is,**

once a function or procedure is compiled, the order of the machine instructions which comprise the executable object code of the function is important and their order in the computer memory is of vital importance. Note that many "compilers" perform "optimizations" within functions or procedures, which determine, on a limited scale, if there is a better arrangement for executable instructions which is more efficient than that constructed by the programmer, but does not change the result of the function or procedure**. Once these optimizations are performed, however, making random changes to the order of instructions is very likely to "break" the function. When a program is compiled, then, it consists of a collection of these sub-objects,** whose exact order or arrangement in memory is not important, so long as any sub-object which uses another sub-object knows where in memory it can be found.

[0052] ***The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. The rest of the instructions comprising that sub-object immediately follow from the entry point***. Some systems may prefix information to the entry point which describes calling and return conventions for the code which follows, an example is the Apple Macintosh Operating System (MacOS). ***These sub-objects can be packaged into what are referred to in certain systems as "code resources,"*** which may be stored separately from the application, or shared with other applications, although not necessarily. Within an application there are also data objects, which consist of some data to be operated on by the executable code. These data objects are not executable. That is, they do not consist of executable instructions. The data objects can be referred to in certain systems as "resources."

INDEPENDENT CLAIM 58

58. (New) A computing device for running application software, comprising:
memory designed to store data in non transitory form;
an operating system;
wherein said memory stores an application software;
wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;
wherein said application software is designed to call said memory scheduler code resource;
wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory.

Shuffle means to "make a random order of arrangement." or "A rearrangement of elements, where none are lost, added, or changed" See for example: http://education.yahoo.com/reference/dictionary/entry/shuffle attachment 1, verb definition 5. Ostrovsky does not disclose shuffling code resources in memory; and http://xlinux.nist.gov/dads//HTML/permutation.html attachment 2.

As noted above, our specification defines a "code resource" as having an entry point and comprises more than one instruction that immediately follows from that entry point. Ostrovsky does not disclose making a random order or arrangement of sets of instructions each set of which immediately follow from its entry point. Nothing in Ostrovsky suggests that function.

Accordingly, Ostrovsky neither discloses nor suggests claim 58.

INDEPENDENT CLAIMS: 67; 75; 78; 79; 80 93; and 104

Regarding the other independent claims: claim 75 recites " designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application"; claim 78 recites " shuffle at least one code resource into a different location in said memory after it has been loaded into memory"; claim 79 recites " to randomize the location of at least one of the at least two code resources in the memory "; claim 80 recites "configured to randomize the location of at least a portion of the code defining a code resource"; and claim 93 recites "said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses" and claim 104 recites "shuffle locations in memory of at least a subset of said plurality of code resources". These claims are not anticipated and are not obvious based upon Ostrovsky for at least the foregoing reasons.

DEPENDENT CLAIMS

Dependent claims such as 87 and 88 clarify the code resources comprise a sequence of instructions that have sequential addresses and define functions or procedures, support for which was discussed above.

Allowance of this application, as amended, is requested.

/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

Date/Time: September 19, 2013 (4:27pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\Amendment_SCOT0014-6_6-27-2013.wpd

IN THE CLAIMS

1-57. (Canceled)

58. (Previously Presented) A computing device for running application software, comprising:

memory designed to store data in non transitory form;

an operating system;

wherein said memory stores an application software;

wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;

wherein said application software is designed to call said memory scheduler code resource;

wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory.

59. (Previously Presented) The device of claim 58 wherein said memory scheduler code resource, when called, functions to shuffle said randomly in memory.

60. (Previously Presented) The device of claim 58 further comprising a program counter designed to store values and wherein said computing device is designed to enable application software to modify a value stored by said program counter.

61. (Currently Amended) The device of claim 58 further comprising a program counter designed to store values and wherein said memory scheduler code resource is designed to modify a value stored by said program counter.

62. (Currently Amended) The device of claim 58 wherein said memory scheduler code resource is designed to modify a stack frame, to modify a calling address, and to copy itself to a memory location associated with said calling address.

63. (Currently Amended) The device of claim 58 wherein said operating system is designed to call said memory scheduler code resource.

64. (Currently Amended) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in memory, wherein each one of said addresses is associated with ~~at least~~ one of the code resources.

65. (Currently Amended) The device of claim 58 wherein said memory scheduler code resource maintains a list of addresses in memory associated with said .

66. (Previously Presented) The device of claim 58 further comprising a processor for processing instructions defined by said application software.

67. (Previously Presented) A system for executing application software code, comprising:

a memory designed to store data in non transitory form, and storing executable code resources;

wherein said executable code resources comprise a memory scheduler and other executable code resources; and

wherein said memory scheduler is designed to shuffle said other executable code resources in memory.

68. (Previously Presented) The system of claim 67, wherein said memory scheduler is designed to shuffle said other executable code resources randomly in memory.

69. (Previously Presented) The system of claim 67 wherein said shuffling results in a change in location in memory of at least one of said other executable code resources.

70. (Previously Presented) The system of claim 67 further comprising a program counter designed to store values and wherein said memory scheduler is designed to modify values stored by said program counter.

71. (Previously Presented) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of at least one code resource.

72. (Currently Amended) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of said other executable code resources.

73. (Previously Presented) The system of claim 67 designed to call said memory scheduler.

74. (Currently Amended) The system of claim 67 wherein said application software code is designed to call said memory scheduler.

75. (Previously Presented) A system for executing an application on a computer system, comprising:

a computer system comprising a processor for processing instructions and memory designed to store data in non transitory form;

a software application comprising a plurality of executable code resources loaded in said memory;

said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources;

and

wherein a first one of said plurality of executable code resource is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor.

76. (Previously Presented) The system of claim 75 wherein said first one is also designed to relocate itself to a different address during execution of said software application.

77. (Previously Presented) The system of claim 75 wherein said system is designed to repeatedly relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application.

78. (Currently Amended) A system comprising:

a processor;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least one code resource into a different location in said memory after ~~it~~ said program has been loaded into memory.

79. (Currently Amended) A system comprising:

a processor ~~for~~ designed to process ~~processing~~ instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load an executable program comprising at least two code resources into memory and to randomize the location of at least one of the at least two code resources in the memory ~~address~~, using said processor

80. (Currently Amended) A system for executing a program comprising code, comprising:

a processor ~~for~~ designed to process ~~processing~~ instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

a memory scheduler configured to randomize ~~the~~ location of ~~at least a portion of the~~ code defining a code resource, in memory, using said processor.

81. (Previously Presented) The system of claim 80 wherein said memory scheduler is configured to repeatedly randomize the location of at least a portion of the code in memory.

82. (New) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that have sequential addresses in said memory.

83. (New) The device of claim 82 wherein each one of said other code resources defines a function or procedure.

84. (New) The device of claim 58 wherein each one of said other code resources comprises a sequence of instructions that immediately follow one another in said memory.

85. (New) The device of claim 84 wherein each one of said other code resources defines a function or procedure.

86. (New) A system comprising:

a processor;

a memory coupled to said processor;

wherein said memory is designed to store data in non transitory form;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least a subset of said code resources into a different location in said memory after said program has been loaded into memory.

87. (New) The system of claim 86 wherein each one of said subset of said code resources comprises a sequence of instructions that have sequential addresses in said memory.

88. (New) The system of claim 87 wherein each one of said subset of said code resources defines a function or procedure.

89. (New) The system of claim 88 wherein each one of said subset of said code resources is an essential code resource.

90. (New) The system of claim 86 wherein each one of said other code resources comprises a sequence of instructions that immediately follow one another in said memory.

91. (New) The system of claim 90 wherein each one of said other code resources defines a function or procedure.

92. (New) The system of claim 90 wherein said system comprises one of a set-top box; a cellular phone; a smart television; and a personal digital assistant (PDA).

93. (New) A system for protecting a software application, said software application comprising a plurality of executable code resources, said system comprising:

a processor;

memory for storing data in non transitory form;

wherein said processor is coupled to said memory for transmitting data there between;

wherein, when executing said software application, said system is configured to store in said memory an original plurality of code resource addresses;

wherein, when executing said software application, said system is configured to store a

plurality of executable code resources for executing said software application;

wherein each one of said original plurality of code resource addresses specifies an address in said memory of one of said plurality of executable code resources;

wherein, when executing said software application, said system is configured to (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

94. (New) The system of claim 93, wherein said system is configured to repeatedly (1) relocate each one of said plurality of executable code resources in said memory so that each one of said plurality of executable code resources is specified by each one of a new plurality of code resource addresses and (2) store in said memory each one of said new plurality of code resource addresses.

95. (New) The system of claim 93, wherein each one of said plurality of executable code resources are relocated to random addresses.

96. (New) The system of claim 93, wherein each one said original plurality of code resource addresses specifies an entry point of an original location in memory of each one of said plurality of executable code resources.

97. (New) The system of claim 93, wherein each one said new plurality of code resource addresses specifies an entry point of a new location in memory of each one of said plurality of executable code resources.

98. (New) The system of claim 93 wherein each one of said plurality of executable code resources is stored in sequential locations in said memory.

99. (New) The system of claim 98 wherein each one of said plurality of executable code resources executes a function or procedure when called.

100. (New) The system of claim 93 wherein each one of said plurality of executable code resources executes a function or procedure

101. (New) The system of claim 93 wherein said system is configured to relocate each one of said plurality of executable code resources in said memory in response to a call to an executable code resource of said plurality of executable code resources.

102. (New) The system of claim 101 wherein said system is configured to update a reference to said executable code resource in said memory of the computer system to reflect a new address in said memory of said executable code resource resulting from relocating said

executable code resource.

103. (New) The system of claim 102 wherein said executable code resource is a memory scheduler which is called periodically, or at random or pseudo random intervals.

104. (New) A computer program product storing in non-transitory form instructions defining a software program, which instructions, when loaded into a computer system instruct the computer system to perform the following steps:

load said program into memory of said computer system;

wherein said program, as loaded into memory, comprises a plurality of code resources;

shuffle locations in memory of at least a subset of said plurality of code resources.

105. (New) The system of claim 104 wherein each one of said subset of said code resources comprises a sequence of instructions that have sequential addresses in said memory.

106. (New) The system of claim 104 wherein each one of said subset of said code resources defines a function or procedure.

107. (New) The system of claim 104 wherein said subset comprises at least two code resources.

ATTACHMENT 1

SCOT0014-6

Definition of shuffle

shuf·fle (shŭf'əl) *sov*

VERB:
shuf·fled, shuf·fling, shuf·fles
VERB:
tr.

1. To slide (the feet) along the floor or ground while walking.
2. To move (something) from one place to another; transfer or shift.
3. To put aside or under cover quickly; shunt: *shuffled the bill under a pile of junk mail.*
4. To mix together; jumble.
5. To mix together (playing cards or tiles, for example) so as to make a random order of arrangement.

VERB:
intr.

1. To move with short sliding steps, without or barely lifting the feet: *The crowd shuffled out of the theater.*
2. To dance casually with sliding and tapping steps.
3. To move about from place to place; shift: *shuffled around looking for work.*
4. To act in a shifty or deceitful manner; equivocate.
5. *Games* To mix playing cards, tiles, or dominoes together so as to make their order random.

NOUN:

1. A short sliding step or movement, or a walk characterized by such steps.
2. A dance in which the feet slide along or move close to the floor.
3. An evasive or deceitful action; an equivocation.
4. A confused mixture; a jumble.
5. *Games*
   a. An act of shuffling cards, dominoes, or tiles.
   b. A player's right or turn to do this.

PHRASAL VERB:
shuffle off

1. To get rid of; dispose of.
2. To evade or shirk (a responsibility, for example).
3. *Informal* To leave; depart.

ETYMOLOGY:
Middle English `shovelen`, *probably of Middle Dutch or Middle Low German origin*

OTHER FORMS:
shuf'fler *(Noun)*

Thesaurus: synonyms for shuffle

ATTACHMENT 2
SCOT0014-6

permutation

(algorithm)

Definition: A rearrangement of elements, where none are lost, added, or changed. The *Fisher-Yates shuffle* randomly permutes elements.

Also known as shuffle.

Generalization (I am a kind of ...)
combination.

Specialization (... is a kind of me.)
ideal random shuffle, *Fisher-Yates shuffle*, *Johnson-Trotter*, *sort*, *derangement*.

Aggregate parent (I am a part of or used in ...)
American flag sort.

See also *pseudo-random number generator*.

Note: A sort is a permutation where the items are arranged in some order. A derangement is a permutation where no item is in its original position.

There are n! permutations of n (distinguishable) elements.

Author: PEB

Implementation

The (Combinatorial) Object Server's information on Permutations (Pascal and C); Michael Gilleland's PermutationGenerator class (Java); (Pascal, Fortran, Mathematica, and C); (Fortran), Perfect shuffle (Haskell).

Go to the Dictionary of Algorithms and Data Structures home page.

If you have suggestions, corrections, or comments, please get in touch with Paul E. Black.

Entry modified 9 January 2009.
HTML page formatted Tue Dec 6 16:16:32 2011.

Cite this as:
Paul E. Black, "permutation", in *Dictionary of Algorithms and Data Structures* [online], Paul E. Black, ed., U.S. National Institute of Standards and Technology. 9 January 2009. (accessed TODAY) Available from: http://www.nist.gov/dads/HTML/permutation.html

Electronic Patent Application Fee Transmittal

Application Number:	13556420
Filing Date:	24-Jul-2012
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Filer:	Richard A. Neifeld
Attorney Docket Number:	SCOT0014-6

Filed as Large Entity

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Claims in Excess of 20	1202	26	80	2080
Independent claims in excess of 3	1201	2	420	840
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Extension-of-Time:				
Extension - 3 months with $0 paid	1253	1	1400	1400
Miscellaneous:				
		Total in USD ($)		**4320**

Electronic Acknowledgement Receipt	
EFS ID:	16904010
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Richard A. Neifeld
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	23-SEP-2013
Filing Date:	24-JUL-2012
Time Stamp:	12:44:51
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	$4320
RAM confirmation Number	13679
Deposit Account	502106
Authorized User	NEIFELD, RICHARD ALAN
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows: Charge any Additional Fees required under 37 C.F.R. Section 1.17 (Patent application and reexamination processing fees)	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		Amendment_SCOT0014-6_6-27-2013.pdf	514494 ac462fcf5c535c89bc2a3a8704891f66bc170cae	yes	16

Multipart Description/PDF files in .zip description

Document Description	Start	End
Transmittal Letter	1	2
Extension of Time	3	3
Amendment/Req. Reconsideration-After Non-Final Reject	4	9
Claims	10	16

Warnings:

Information:

2	Affidavit-not covered under specific rule	Attachment1_shuffle_FontsEmbedded_SCOT0014-6.pdf	117617 92ce9808de112b1e5da2de736134abb51c977f0e	no	2

Warnings:

Information:

3	Affidavit-not covered under specific rule	Attachment2_permutation_FontsEmbedded_SCOT0014-6.pdf	33517 d3cb270c95191b1319aa96a8410378e40d616e36	no	1

Warnings:

Information:

4	Fee Worksheet (SB06)	fee-info.pdf	33383 c2cb55077790a2c4e45f0f062d6a1bcbd9a9730f	no	2

Warnings:

Information:

Total Files Size (in bytes):	699011

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**$4320
Number of independent claims previously paid for: 6
Number of independent claims in excess of 6: 2.
(Fee codes 1201/2201 1.16(h)): 420
1*420=**840**
Number of total claims previously paid for: 24
Number of total claims in excess of 24: 26
(Fee codes 1202/2202 1.16(I)) $80
26*80= $**2080**
1253/2253/3253 1.17(a)(3) Extension for response within third month **$1,400.00**
840+2080+1400=$4320

3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
37 CFR 1.111 PRELIMINARY AMENDMENT - REMARKS
37 CFR 1.111 PRELIMINARY AMENDMENT - CLAIMS
37 CFR 1.136 EXTENSION OF TIME (3 MONTH)
Attachment 1, definition of "Shuffle"
Attachment 2, definition of "Permutation"

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: PClaw BankAcct, G/L: **6, 5010**
PCLAW BILLING REFERENCE: [SCOT0001]
Check#, Entry date, Amount: [1916, 9/19/13, 4320.00]

Service Fees: Amount/CreditAtty/Entry date/Services: [400/ran/payingfees]

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: ran
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 9-19-2013 **SIGNATURE**: /RichardNeifeld/
Printed: September 19, 2013 (4:27pm) RICHARD NEIFELD, REG. NO. 35,299
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\Amendment_SCOT0014-6_6-27-2013.wpd

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE

37 CFR 1.136 EXTENSION OF TIME - 3 MONTHS

This is an extension of time for 3 months, extending the deadline date to respond, from the 3/28/2013 office action to 9/28/2013.

/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

Date/Time: September 19, 2013 (4:27pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\Amendment_SCOT0014-6_6-27-2013.wpd

PTO/SB/06 (09-11)
Approved for use through 1/31/2014. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 13/556,420	Filing Date 07/24/2012	☐ To be Mailed

ENTITY: ☒ LARGE ☐ SMALL ☐ MICRO

APPLICATION AS FILED – PART I

	(Column 1)	(Column 2)		
FOR	NUMBER FILED	NUMBER EXTRA	RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).			
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))				
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL	

APPLICATION AS AMENDED – PART II

		(Column 1)		(Column 2)	(Column 3)		
	09/23/2013	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
AMENDMENT	Total (37 CFR 1.16(i))	* 50	Minus	** 24	= 26	X $80 =	2080
	Independent (37 CFR 1.16(h))	* 9	Minus	***7	= 2	X $420 =	840
	☐ Application Size Fee (37 CFR 1.16(s))						
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
						TOTAL ADD'L FEE	**2920**

		(Column 1)		(Column 2)	(Column 3)		
		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)
AMENDMENT	Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =	
	☐ Application Size Fee (37 CFR 1.16(s))						
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))						
						TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

LIE
/BONNIE COLE/

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

31518 7590 07/18/2013

NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

EXAMINER	
OKEKE, IZUNNA	
ART UNIT	**PAPER NUMBER**
2432	

NOTIFICATION DATE	DELIVERY MODE
07/18/2013	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

general@neifeld.com
rneifeld@neifeld.com
rhahl@neifeld.com

Applicant-Initiated Interview Summary	Application No.	Applicant(s)	
	13/556,420	MOSKOWITZ, SCOTT A.	
	Examiner	Art Unit	
	IZUNNA OKEKE	2432	

All participants (applicant, applicant's representative, PTO personnel):

(1) *IZUNNA OKEKE*. (3)_____.

(2) *RICHARD NEIFELD*. (4)_____.

Date of Interview: *11 July 2013*.

Type: ☒ Telephonic ☐ Video Conference
☐ Personal [copy given to: ☐ applicant ☐ applicant's representative]

Exhibit shown or demonstration conducted: ☐ Yes ☒ No.
If Yes, brief description: _____.

Issues Discussed ☐101 ☐112 ☒102 ☐103 ☐Others
(For each of the checked box(es) above, please describe below the issue and detailed description of the discussion)

Claim(s) discussed: *51*.

Identification of prior art discussed: *Ostrovsky (US-5123045)*.

Substance of Interview
(For each issue discussed, provide a detailed description and indicate if agreement was reached. Some topics may include: identification or clarification of a reference or a portion thereof, claim interpretation, proposed amendments, arguments of any applied references etc...)

applicant's representative discussed the reference and argues that it fails to shuffle the code resources in memory. the instant invention shuffle code resources by moving the from one location to another location in memory. Examiner explained that Ostrovsky likewise shuffle code resources by moving them from one location to another as disclosed in Col 5, Line 32-59. No agreement with respect to the claims was reached. .

Applicant recordation instructions: The formal written reply to the last Office action must include the substance of the interview. (See MPEP section 713.04). If a reply to the last Office action has already been filed, applicant is given a non-extendable period of the longer of one month or thirty days from this interview date, or the mailing date of this interview summary form, whichever is later, to file a statement of the substance of the interview

Examiner recordation instructions: Examiners must summarize the substance of any interview of record. A complete and proper recordation of the substance of an interview should include the items listed in MPEP 713.04 for complete and proper recordation including the identification of the general thrust of each argument or issue discussed, a general indication of any other pertinent matters discussed regarding patentability and the general results or outcome of the interview, to include an indication as to whether or not agreement was reached on the issues raised.

☐ Attachment

/IZUNNA OKEKE/ Examiner, Art Unit 2432	

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record
A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) § 1.133 Interviews
Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.
All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiner's responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicant's correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:
- Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant, attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:
1) A brief description of the nature of any exhibit shown or any demonstration conducted,
2) an identification of the claims discussed,
3) an identification of the specific prior art discussed,
4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
5) a brief identification of the general thrust of the principal arguments presented to the examiner,
   (The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
6) a general indication of any other pertinent matters discussed, and
7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicant's record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiner's version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, "Interview Record OK" on the paper recording the substance of the interview along with the date and the examiner's initials.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

OUTLINE FOR INTERVIEW SCHEDULED FOR THURSDAY 7/11/13, 11 AM

Examiner Okeke telephone: (571)270-3854.

I. SUMMARY

This amendment responds to the non final office action (NFOA) dated 3/28/2013.

Claims 58-81 were pending and examined. Claims 82-107 are new, including the new independent claims noted in the subsection titled "INDEPENDENT CLAIMS: 67; 75; 78; 79; 80 93; and 104."

The NFOA rejects Claims 58-81 under 35 USC 102(b) based upon Ostrovsky et al., USP 5,123,045. In summary, Ostrovsky does not disclose or suggest shuffling in memory of code resources of a software program. Our claims define those limitations.

II. EXEMPLARY CLAIMS - INDEPENDENT CLAIMS 58, 86, DEPENDENTS 87, 88

58. (New) A computing device for running application software, comprising:
memory designed to store data in non transitory form;
an operating system;
wherein said memory stores an application software;
wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;
wherein said application software is designed to call said memory scheduler code resource;
wherein said memory scheduler code resource, when called, functions **to shuffle said other code resources in memory**.

86. (New) A system comprising:
a processor;
a memory coupled to said processor;

wherein said memory is designed to store data in non transitory form;

wherein said system is configured to load a program comprising code resources into said memory and to **shuffle at least a subset of said code resources into a different location in said memory** after said program has been loaded into memory.

87. (New) The system of claim 86 wherein each one of said subset of said code resources comprises a sequence of instructions that **have sequential addresses in said memory**.

88. (New) The system of claim 87 wherein each one of said subset of said code resources **defines a function or procedure**.

III. CLAIM MEANING

WORD DEFINITIONS

Shuffle means to "make a random order of arrangement." or "A rearrangement of elements, where none are lost, added, or changed" See for example: http://education.yahoo.com/reference/dictionary/entry/shuffle attachment 1, verb definition 5. Ostrovsky does not disclose shuffling code resources in memory; and http://xlinux.nist.gov/dads//HTML/permutation.html attachment 2.

DISCLOSURE DEFINITIONS - "CODE RESOURCES"

Paragraphs [0051] and [0052] from US patent publication 20130014271 of application 13556420, the subject application, are copied below, with more relevant passages emphasized, for the examiner's convenience.

> [0051] An executable computer program is variously referred to as an application, from the point of view of a user, or executable object code from the point of view of the engineer. A collection of smaller, atomic (or indivisible) chunks of object code typically comprise the complete executable object code or application which may also require the presence of certain data resources. These indivisible portions of object code correspond with the programmers' function or procedure implementations in higher level languages, such as C or Pascal. In creating an application, a programmer writes "code" in a higher level language, which is then compiled down into "machine language," or, the executable object code, which can actually be run by a computer, general purpose or otherwise. Each function, or procedure, written in the programming language, represents a self-contained portion of the larger program, and implements, typically, a very small piece of its functionality. The order in which the programmer types the code for the various functions or procedures, and the distribution of and arrangement of these implementations in various files which hold them is unimportant. Within a function or procedure, however, the order of individual language constructs, which correspond to particular machine instructions is important, and so functions or procedures are considered indivisible for purposes of this discussion. **That is, once a function or procedure is compiled, the order of the machine instructions which comprise the executable object code of the function is important and their order in the computer memory is of vital importance**. Note that many "compilers" perform "optimizations" within functions or procedures, which determine, on a limited scale, if there is a better arrangement for executable instructions which is more efficient than that constructed by the programmer, but does not change the result of the function or procedure**. Once**

these optimizations are performed, however, making random changes to the order of instructions is very likely to "break" the function. When a program is compiled, then, it consists of a collection of these sub-objects, whose exact order or arrangement in memory is not important, so long as any sub-object which uses another sub-object knows where in memory it can be found.

[0052] ***The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. The rest of the instructions comprising that sub-object immediately follow from the entry point***. Some systems may prefix information to the entry point which describes calling and return conventions for the code which follows, an example is the Apple Macintosh Operating System (MacOS). ***These sub-objects can be packaged into what are referred to in certain systems as "code resources,"*** which may be stored separately from the application, or shared with other applications, although not necessarily. Within an application there are also data objects, which consist of some data to be operated on by the executable code. These data objects are not executable. That is, they do not consist of executable instructions. The data objects can be referred to in certain systems as "resources."

As noted above, our specification defines a "code resource" as having an entry point and comprises more than one instruction that immediately follows from that entry point. Ostrovsky does not disclose making a random order or arrangement of sets of instructions each set of which immediately follow from its entry point. Nothing in Ostrovsky suggests that function. Accordingly, Ostrovsky neither discloses nor suggests claim 58.

IV. OSTROVSKY REJECTIONS

INITIALLY STORING OF *PROGRAM VALUES* IN "A PSEUDO-RANDOM SEQUENCE"

Ostrovsky's storing of *program values* in "a pseudo-random sequence" in the unprotected RAM does not disclose our claimed **shuffling of *code resources* in memory**.

MOVING A SINGLE VALUE OF THE SOFTWARE PROGRAM TO A DIFFERENT MEMORY LOCATION

Ostrovsky discloses moving the "desired memory location" that is "truly sought" for "execution of the program," once it is identified does not disclose our claimed **shuffling of *code resources* in memory**.

PARTIALLY PERMUTING THE ORDER OF SOFTWARE PROGRAM VALUES

Ostrovsky's partially permuting does not disclose our claimed shuffling of *code resources* in memory

Ostrovsky movement of disordered subset of the values (code and instruction) defining the software program from one bucket or buffer to another is not **shuffling of *code resources* in memory**.

/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

Date/Time: July 5, 2013 (3:12pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\InterviewAgenda_SCOT0014-6_7-5.wpd

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: OKEKE/2432
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

OUTLINE FOR INTERVIEW SCHEDULED FOR THURSDAY 7/11/13, 11 AM

Examiner Okeke telephone: (571)270-3854.

I. SUMMARY

This amendment responds to the non final office action (NFOA) dated 3/28/2013.

Claims 58-81 were pending and examined. Claims 82-107 are new, including the new independent claims noted in the subsection titled "INDEPENDENT CLAIMS: 67; 75; 78; 79; 80 93; and 104."

The NFOA rejects Claims 58-81 under 35 USC 102(b) based upon Ostrovsky et al., USP 5,123,045. In summary, Ostrovsky does not disclose or suggest shuffling in memory of code resources of a software program. Our claims define those limitations.

II. EXEMPLARY CLAIMS - INDEPENDENT CLAIMS 58, 86, DEPENDENTS 87, 88

58. (New) A computing device for running application software, comprising:
memory designed to store data in non transitory form;
an operating system;
wherein said memory stores an application software;
wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;
wherein said application software is designed to call said memory scheduler code resource;
wherein said memory scheduler code resource, when called, functions **to shuffle said other code resources in memory**.

86. (New) A system comprising:
a processor;
a memory coupled to said processor;

wherein said memory is designed to store data in non transitory form;

wherein said system is configured to load a program comprising code resources into said memory and to **shuffle at least a subset of said code resources into a different location in said memory** after said program has been loaded into memory.

87. (New) The system of claim 86 wherein each one of said subset of said code resources comprises a sequence of instructions that **have sequential addresses in said memory**.

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Paragraphs [0051] and [0052] from US patent publication 20130014271 of application 13556420, the subject application, are copied below, with more relevant passages emphasized, for the examiner's convenience.

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these optimizations are performed, however, making random changes to the order of instructions is very likely to "break" the function. When a program is compiled, then, it consists of a collection of these sub-objects, whose exact order or arrangement in memory is not important, so long as any sub-object which uses another sub-object knows where in memory it can be found.

[0052] ***The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. The rest of the instructions comprising that sub-object immediately follow from the entry point***. Some systems may prefix information to the entry point which describes calling and return conventions for the code which follows, an example is the Apple Macintosh Operating System (MacOS). ***These sub-objects can be packaged into what are referred to in certain systems as "code resources,"*** which may be stored separately from the application, or shared with other applications, although not necessarily. Within an application there are also data objects, which consist of some data to be operated on by the executable code. These data objects are not executable. That is, they do not consist of executable instructions. The data objects can be referred to in certain systems as "resources."

As noted above, our specification defines a "code resource" as having an entry point and comprises more than one instruction that immediately follows from that entry point. Ostrovsky does not disclose making a random order or arrangement of sets of instructions each set of which immediately follow from its entry point. Nothing in Ostrovsky suggests that function. Accordingly, Ostrovsky neither discloses nor suggests claim 58.

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/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

Date/Time: July 5, 2013 (3:12pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\InterviewAgenda_SCOT0014-6_7-5.wpd

Electronic Acknowledgement Receipt

EFS ID:	16242284
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Richard A. Neifeld
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	05-JUL-2013
Filing Date:	24-JUL-2012
Time Stamp:	15:23:39
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Letter Requesting Interview with Examiner	InterviewAgenda_SCOT0014-6_7-5.pdf	485290 581b983ae664af5119d4268caffcbd4614365f61	no	3

Warnings:	
Information:	

Total Files Size (in bytes):	485290

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

UNITED STATES PATENT AND TRADEMARK OFFICE

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www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6	5811

31518 7590 03/28/2013

NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

EXAMINER	
OKEKE, IZUNNA	
ART UNIT	PAPER NUMBER
2432	

NOTIFICATION DATE	DELIVERY MODE
03/28/2013	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

general@neifeld.com
rneifeld@neifeld.com
rhahl@neifeld.com

Office Action Summary	Application No.	Applicant(s)	
	13/556,420	MOSKOWITZ, SCOTT A.	
	Examiner	Art Unit	
	IZUNNA OKEKE	2432	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *30 November 2012*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.

4)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

5)☒ Claim(s) *58-81* is/are pending in the application.

5a) Of the above claim(s) _____ is/are withdrawn from consideration.

6)☐ Claim(s) _____ is/are allowed.

7)☒ Claim(s) *58-81* is/are rejected.

8)☐ Claim(s) _____ is/are objected to.

9)☐ Claim(s) _____ are subject to restriction and/or election requirement.

* If any claims have been determined allowable, you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to PPHfeedback@uspto.gov.

Application Papers

10)☐ The specification is objected to by the Examiner.

11)☒ The drawing(s) filed on *24 September 2012* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date *10/04/2012*.

3) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .

4) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 58-81 are rejected under 35 U.S.C. 102(b) as being anticipated by Ostrovsky et al. (US-5123045).

a. *Referring to claims 58, 63, 67, 73 and 74:*

Regarding claims 58, 63, 67, 73 and 74, Ostrovsky teaches a computing device for running application software, comprising: memory designed to store data in non transitory form; an operating system; wherein said memory stores an application software (Col 4, Line 35-57….. data processing system comprising a memory to store an application software and an operating system as known in the art); wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources (Col 4, Line 10-30 and Col 5, Line 5-31…. Software protection mechanism and other code resources of the software); wherein said application software is designed to call said memory scheduler code resource; wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory (Col 5, Line 32-59…. periodically calling the protection mechanism to shuffle other code resources in memory to prevent memory analysis).

a. *Referring to claims 59, 68 and 69:*

Regarding claims 59, 68 and 69, Ostrovsky teaches the device of claim 58 wherein said memory scheduler code resource, when called, functions to shuffle said other code resources randomly in memory (Col 5, Line 32-59…. periodically calling the protection mechanism to shuffle other code resources randomly in memory to prevent memory analysis).

a. *Referring to claims 60, 61 and 70:*

Regarding claims 60, 61 and 70, Ostrovsky teaches the device of claim 58 further comprising a program counter designed to store values and wherein said computing device is designed to enable application software to modify a value stored by said program counter (Col 3, Line 43-51…. modifying a counter value to correspond with the shuffling).

a. *Referring to claim 62:*

Regarding claim 62, Ostrovsky teaches the device of claim 58 wherein said memory scheduler is designed to modify a stack frame, to modify a calling address, and to copy itself to a memory location associated with said calling address (Col 32-46…. during execution, the protection mechanism in memory is periodically called to shuffle the code contents of memory).

a. *Referring to claims 64, 65, 71 and 72:*

Regarding claims 64, 65, 71 and 72, Ostrovsky teaches the device of claim 58 wherein said memory scheduler maintains a list of addresses in memory, said addresses associated with at least one of the code resources (Col 6, Line 15-55…. maintaining addresses where the code resources are stored).

a. *Referring to claim 66:*

Regarding claim 66, Ostrovsky teaches the device of claim 58 further comprising a processor for processing instructions defined by said application software (Col 4, Line 39-41…. CPU).

a. *Referring to claims 75, 76, 78-80:*

Regarding claims 75, 76, 78-80, Ostrovsky teaches a system for executing an application on a computer system, comprising: a computer system comprising a processor for processing instructions and memory designed to store data in non transitory form (See the rejection in claim 58); a software application comprising a plurality of executable code resources loaded in said memory (See the rejection in claim 58); said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources (See the rejection in claim 58); and wherein a first one of said plurality of executable code resource is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor (Col 5, Line 32-59…. shuffling the code resources from one location (address) in memory to another location (address) in memory) during execution of the program).

a. *Referring to claims 77 and 81:*

Regarding claims 77 and 81, Ostrovsky teaches the system of claim 75 wherein said system is designed to repeatedly relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application (Col 5, Line 32-59…. periodically shuffling the code resources from one

location (address) in memory to another location (address) in memory) during execution of the program).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to IZUNNA OKEKE whose telephone number is (571)270-3854. The examiner can normally be reached on Monday - Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on (571) 270-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/IZUNNA OKEKE/
Examiner, Art Unit 2432

Notice of References Cited	Application/Control No. 13/556,420	Applicant(s)/Patent Under Reexamination MOSKOWITZ, SCOTT A.	
	Examiner IZUNNA OKEKE	Art Unit 2432	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,123,045	06-1992	Ostrovsky et al.	713/190
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Index of Claims	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	Art Unit
	IZUNNA OKEKE	2432

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

☐ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

CLAIM		DATE								
Final	Original	03/21/2013								
	58	✓								
	59	✓								
	60	✓								
	61	✓								
	62	✓								
	63	✓								
	64	✓								
	65	✓								
	66	✓								
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	75	✓								
	76	✓								
	77	✓								
	78	✓								
	79	✓								
	80	✓								
	81	✓								

Search Notes	Application/Control No.	Applicant(s)/Patent Under Reexamination
	13556420	MOSKOWITZ, SCOTT A.
	Examiner	Art Unit
	IZUNNA OKEKE	2432

CPC- SEARCHED		
Symbol	Date	Examiner

CPC COMBINATION SETS - SEARCHED		
Symbol	Date	Examiner

US CLASSIFICATION SEARCHED			
Class	Subclass	Date	Examiner

SEARCH NOTES		
Search Notes	Date	Examiner
Text Search (See Search History)	3/20/2013	IO
Keyword + Classification Search (See Search History)	3/21/2013	IO
Search (713/1, 713/151, 713/165, 713/166, 713/167, 713/193, 713/194, 713/152, 713/187, 713/189, 713/188, 713/190, 726/26, 380/201, 711/1, 711/100) (See Search History)	3/21/2013	IO
NPL Database Search (Google Scolar database search : Search terms : "rearranging code in runtime memory during execution to prevent memory analysis")	3/19/2013	IO
Review of parent case 08,587,943 and related case 11,895,388	3/19/2013	IO

INTERFERENCE SEARCH			
US Class/ CPC Symbol	US Subclass / CPC Group	Date	Examiner

Printed: October 4, 2012 (11:13am)
Path: Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ReferenceCitationList_SCOT0014-6_10-3-2012.wpd
37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET: 13/556,420 / SCOT0014-6
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

LISTING OF UNITED STATES PATENTS - U series

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	U 01	3947825	March 1976	Cassada	X
	U 02	3984624	October 1976	Waggener	X
	U 03	3986624	October 1976	Cates, Jr. et al.	X
	U 04	4038596	July 1977	Lee	X
	U 05	4200770	April 1980	Hellman et al.	X
	U 06	4218582	August 1980	Hellman et al.	X
	U 07	4339134	July 1982	Macheel	X
	U 08	4390898	June 1983	Bond et al.	X
	U 09	4405829	September 1983	Rivest et al.	X
	U 010	4424414	January 1984	Hellman et al.	X
	U 011	4528588	July 1985	Lofberg	X
	U 012	4672605	June 1987	Hustig et al.	X
	U 013	4748668	May 1988	Shamir et al.	X
	U 014	4789928	December 1988	Fujisaki	X
	U 015	4827508	May 1989	Shear	X
	U 016	4876617	October 1989	Best et al.	X
	U 017	4896275	January 1990	Jackson	X
	U 018	4908873	March 1990	Philibert et al.	X
	U 019	4939515	July 1990	Adelson	X
	U 020	4969204	November 1990	Melnychuk et al.	X
	U 021	4972471	November 1990	Gross et al.	X
	U 022	4977594	December 1990	Shear	X

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	U 023	4979210	December 1990	Nagata et al.	X
	U 024	4980782	December 1990	Ginkel	X
	U 025	5050213	September 1991	Shear	X
	U 026	5073925	December 1991	Nagata et al.	X
	U 027	5077665	December 1991	Silverman et al.	X
	U 028	5113437	May 1992	Best et al.	X
	U 029	5136581	August 1992	Muehrcke	X
	U 030	5136646	August 1992	Haber et al.	X
	U 031	5136647	August 1992	Haber et al.	X
	U 032	5142576	August 1992	Nadan	X
	U 033	5161210	November 1992	Druyvesteyn et al.	X
	U 034	5210820	May 1993	Kenyon	X
	U 035	5243423	September 1993	DeJean et al.	X
	U 036	5243515	September 1993	Lee	X
	U 037	5287407	February 1994	Holmes	X
	U 038	5319735	June 1994	Preuss et al.	X
	U 039	5341429	August 1994	Stringer et al.	X
	U 040	5341477	August 1994	Pitkin et al.	X
	U 041	5363448	November 1994	Koopman et al.	X
	U 042	5365586	November 1994	Indeck et al.	X
	U 043	5369707	November 1994	Follendore, III	X
	U 044	5379345	January 1995	Greenberg	X
	U 045	5394324	February 1995	Clearwater	X

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	U 046	5398285	March 1995	Borgelt et al.	X
	U 047	5406627	April 1995	Thompson et al.	X
	U 048	5408505	April 1995	Indeck et al.	X
	U 049	5410598	April 1995	Shear	X
	U 050	5412718	May 1995	Narasimhalv et al.	X
	U 051	5418713	May 1995	Allen	X
	U 052	5428606	June 1995	Moskowitz	X
	U 053	5450490	September 1995	Jensen et al.	X
	U 054	5469536	November 1995	Blank	X
	U 055	5471533	November 1995	Wang et al.	X
	U 056	5478990	December 1995	Montanari et al.	X
	U 057	5479210	December 1995	Cawley et al.	X
	U 058	5487168	January 1996	Geiner et al.	X
	U 059	5493677	February 1996	Balogh et al.	X
	U 060	5497419	March 1996	Hill	X
	U 061	5506795	April 1996	Yamakawa	X
	U 062	5513126	April 1996	Harkins et al.	X
	U 063	5513261	April 1996	Maher	X
	U 064	5530739	June 1996	Okada	X
	U 065	5530751	June 1996	Morris	X
	U 066	5530759	June 1996	Braudaway et al.	X
	U 067	5539735	July 1996	Moskowitz	X
	U 068	5548579	August 1996	Lebrun et al.	X

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	U 069	5568570	October 1996	Rabbani	X
	U 070	5579124	November 1996	Aijala et al.	X
	U 071	5581703	December 1996	Baugher et al.	X
	U 072	5583488	December 1996	Sala et al.	X
	U 073	5598470	January 1997	Cooper et al.	X
	U 074	5606609	February 1997	Houser et al.	X
	U 075	5613004	March 1997	Cooperman et al.	X
	U 076	5617119	April 1997	Briggs et al.	X
	U 077	5625690	April 1997	Michel et al.	X
	U 078	5629980	May 1997	Stefik et al.	X
	U 079	5633932	May 1997	Davis et al.	X
	U 080	5634040	May 1997	Her et al.	X
	U 081	5636276	June 1997	Brugger	X
	U 082	5636292	June 1997	Rhoads	X
	U 083	5640569	June 1997	Miller et al.	X
	U 084	5646997	July 1997	Barton	X
	U 085	5657461	August 1997	Harkins et al.	X
	U 086	5659726	August 1997	Sandford, II et al.	X
	U 087	5664018	September 1997	Leighton	X
	U 088	5673316	September 1997	Auerbach et al.	X
	U 089	5677952	October 1997	Blakely et al.	X
	U 090	5680462	October 1997	Miller et al.	X
	U 091	5687236	November 1997	Moskowitz et al.	X

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	U 092	5689587	November 1997	Bender et al.	X
	U 093	5696828	December 1997	Koopman, Jr.	X
	U 094	5719937	February 1998	Warren et al.	X
	U 095	5721788	February 1998	Powell et al.	X
	U 096	5734752	March 1998	Knox	X
	U 097	5737416	April 1998	Cooper et al.	X
	U 098	5737733	April 1998	Eller	X
	U 099	5740244	April 1998	Indeck et al.	X
	U 0100	5745569	April 1998	Moskowitz et al.	X
	U 0101	5748783	May 1998	Rhoads	X
	U 0102	5751811	May 1998	Magnotti et al.	X
	U 0103	5754697	May 1998	Fu et al.	X
	U 0104	5757923	May 1998	Koopman, Jr.	X
	U 0105	5765152	June 1998	Erickson	X
	U 0106	5768396	June 1998	Sone	X
	U 0107	5774452	June 1998	Wolosewicz	X
	U 0108	5790677	August 1998	Fox et al.	X
	U 0109	5799083	August 1998	Brothers et al.	X
	U 0110	5809139	September 1998	Grirod et al.	X
	U 0111	5809160	September 1998	Powell et al.	X
	U 0112	5822432	October 1998	Moskowitz et al.	X
	U 0113	5828325	October 1998	Wolosewicz et al.	X

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	U 0114	5832119	November 1998	Rhoads	X
	U 0115	5848155	December 1998	Cox	X
	U 0116	5850481	December 1998	Rhoads	X
	U 0117	5859920	January 1999	Daly et al.	X
	U 0118	5860099	January 1999	Milios et al.	X
	U 0119	5862260	January 1999	Rhoads	X
	U 0120	5870474	February 1999	Wasilewski et al.	X
	U 0121	5884033	March 1999	Duvall et al.	X
	U 0122	5889868	March 1999	Moskowitz et al.	X
	U 0123	5893067	April 1999	Bender et al.	X
	U 0124	5894521	April 1999	Conley	X
	U 0125	5903721	May 1999	Sixtus	X
	U 0126	5905800	May 1999	Moskowitz et al.	X
	U 0127	5905975	May 1999	Ausubel	X
	U 0128	5912972	June 1999	Barton	X
	U 0129	5915027	June 1999	Cox et al.	X
	U 0130	5917915	June 1999	Hirose	X
	U 0131	5918223	June 1999	Blum	X
	U 0132	5920900	July 1999	Poole et al.	X
	U 0133	5923763	July 1999	Walker et al.	X
	U 0134	5930369	July 1999	Cox et al.	X
	U 0135	5930377	July 1999	Powell et al	X
	U 0136	5940134	August 1999	Wirtz	X

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	U 0137	5943422	August 1999	Van Wie et al.	X
	U 0138	5963909	October 1999	Warren et al.	X
	U 0139	5973731	October 1999	Schwab	X
	U 0140	5974141	October 1999	Saito	X
	U 0141	5991426	November 1999	Cox et al.	X
	U 0142	5999217	December 1999	Berners-Lee	X
	U 0143	6009176	December 1999	Gennaro et al.	X
	U 0144	6029126	February 2000	Malvar	X
	U 0145	6041316	March 2000	Allen	X
	U 0146	6044471	March 2000	Colvin	X
	U 0147	6049838	April 2000	Miller et al.	X
	U 0148	6051029	April 2000	Paterson et al.	X
	U 0149	6061793	May 2000	Tewfik et al.	X
	U 0150	6069914	May 2000	Cox	X
	U 0151	6078664	June 2000	Moskowitz et al.	X
	U 0152	6081251	June 2000	Sakai et al.	X
	U 0153	6081587	June 2000	Reyes et al.	X
	U 0154	6088455	July 2000	Logan et al.	X
	U 0155	6131162	October 2000	Yoshiura et al.	X
	U 0156	6141753	October 2000	Zhao et al.	X
	U 0157	6141754	October 2000	Choy	X
	U 0158	6154571	November 2000	Cox et al.	X
	U 0159	6192138	February 2001	Yamadaji	X

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	U 0160	6199058	March 2001	Wong et al.	X
	U 0161	6205249	March 2001	Moskowitz	X
	U 0162	6208745	March 2001	Florenio et al.	X
	U 0163	6230268	May 2001	Miwa et al.	X
	U 0164	6233347	May 2001	Chen et al.	X
	U 0165	6233684	May 2001	Stefik et al.	X
	U 0166	6240121	May 2001	Senoh	X
	U 0167	6263313	July 2001	Milstead et al.	X
	U 0168	6272634	August 2001	Tewfik et al.	X
	U 0169	6275988	August 2001	Nagashima et al.	X
	U 0170	6278780	August 2001	Shimada	X
	U 0171	6278791	August 2001	Honsinger et al.	X
	U 0172	6282300	August 2001	Bloom et al.	X
	U 0173	6282650	August 2001	Davis	X
	U 0174	6285775	September 2001	Wu et al.	X
	U 0175	6301663	October 2001	Kato et al.	X
	U 0176	6310962	October 2001	Chung et al.	X
	U 0177	6330335	December 2001	Rhoads	X
	U 0178	6330672	December 2001	Shur	X
	U 0179	6345100	February 2002	Levine	X
	U 0180	6351765	February 2002	Pietropaolo et al.	X
	U 0181	6363483	March 2002	Keshav	X
	U 0182	6373892	April 2002	Ichien et al.	X

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	U 0183	6373960	April 2002	Conover et al.	X
	U 0184	6374036	April 2002	Ryan et al.	X
	U 0185	6377625	April 2002	Kim	X
	U 0186	6381618	April 2002	Jones et al.	X
	U 0187	6381747	April 2002	Wonfor et al.	X
	U 0188	6385329	May 2002	Sharma et al.	X
	U 0189	6389538	May 2002	Gruse et al.	X
	U 0190	6405203	June 2002	Collart	X
	U 0191	6415041	July 2002	Oami et al.	X
	U 0192	6425081	July 2002	Iwamura	X
	U 0193	6430301	August 2002	Petrovic	X
	U 0194	6430302	August 2002	Rhoads	X
	U 0195	6442283	August 2002	Tewfik et al.	X
	U 0196	6446211	September 2002	Colvin	X
	U 0197	6453252	September 2002	Laroche	X
	U 0198	6457058	September 2002	Ullum et al.	X
	U 0199	6463468	October 2002	Buch et al.	X
	U 0200	6484264	November 2002	Colvin	X
	U 0201	6493457	December 2002	Quackenbush	X
	U 0202	6502195	December 2002	Colvin	X
	U 0203	6522767	February 2003	Moskowitz et al.	X
	U 0204	6522769	February 2003	Rhoads et al.	X
	U 0205	6523113	February 2003	Wehrenberg	X

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	U 0206	6530021	March 2003	Epstein et al.	X
	U 0207	6532284	March 2003	Walker et al.	X
	U 0208	6539475	March 2003	Cox et al.	X
	U 0209	6557103	April 2003	Boncelet, Jr. et al.	X
	U 0210	6584125	June 2003	Katto	X
	U 0211	6587837	July 2003	Spagna et al.	X
	U 0212	6598162	July 2003	Moskowitz	X
	U 0213	6606393	August 2003	Xie et al.	X
	U 0214	6647424	November 2003	Pearson et al.	X
	U 0215	6658010	December 2003	Enns et al.	X
	U 0216	6665489	December 2003	Collart	X
	U 0217	6668246	December 2003	Yeung et al.	X
	U 0218	6668325	December 2003	Collberg et al	X
	U 0219	6687683	February 2004	Harada et al.	X
	U 0220	6725372	April 2004	Lewis et al	X
	U 0221	6754822	June 2004	Zhao	X
	U 0222	6775772	August 2004	Binding et al.	X
	U 0223	6784354	August 2004	Lu et al.	X
	U 0224	6785815	August 2004	Serret-Avila et al.	X
	U 0225	6785825	August 2004	Colvin	X
	U 0226	6792548	September 2004	Colvin	X
	U 0227	6792549	September 2004	Colvin	X
	U 0228	6795925	September 2004	Colvin	X

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	U 0229	6799277	September 2004	Colvin	X
	U 0230	6813717	November 2004	Colvin	X
	U 0231	6813718	November 2004	Colvin	X
	U 0232	6823455	November 2004	Macy et al.	X
	U 0233	6834308	December 2004	Ikezoye et al.	X
	U 0234	6842862	January 2005	Chow et al.	X
	U 0235	6853726	February 2005	Moskowitz et al.	X
	U 0236	6857078	February 2005	Colvin	X
	U 0237	6931534	August 2005	Jandel et al.	X
	U 0238	6966002	November 2005	Torrubia-Saez	X
	U 0239	6983337	November 2005	Wold	X
	U 0240	6977894	December 2005	Achilles et al.	X
	U 0241	6978370	December 2005	Kocher	X
	U 0242	6986063	January 2006	Colvin	X
	U 0243	7007166	February 2006	Moskowitz et al.	X
	U 0244	7020285	March 2006	Kirovski et al.	X
	U 0245	7035409	April 2006	Moskowitz	X
	U 0246	7043050	May 2006	Yuval	X
	U 0247	7046808	May 2006	Metois et al.	X
	U 0248	7050396	May 2006	Cohen et al.	X
	U 0249	7051208	May 2006	Venkatesan et al.	X
	U 0250	7058570	June 2006	Yu et al.	X
	U 0251	7093295	August 2006	Saito	X

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	U 0252	7095874	August 2006	Moskowitz et al	X
	U 0253	7103184	September 2006	Jian	X
	U 0254	7107451	September 2006	Moskowitz	X
	U 0255	7123718	October 2006	Moskowitz et al.	X
	U 0256	7127615	October 2006	Moskowitz	X
	U 0257	7150003	December 2006	Naumovich et al.	X
	U 0258	7152162	December 2006	Moskowitz et al.	X
	U 0259	7159116	January 2007	Moskowitz	X
	U 0260	7162642	January 2007	Schumann et al.	X
	U 0261	7177429	February 2007	Moskowitz et al.	X
	U 0262	7177430	February 2007	Kim	X
	U 0263	7206649	April 2007	Kirovski et al.	X
	U 0264	7231524	June 2007	Bums	X
	U 0265	7233669.	June 2007	Candelore	X
	U 0266	7240210	July 2007	Michak et al.	X
	U 0267	7266697	September 2007	Kirovski et al	X
	U 0268	7287275	October 2007	Moskowitz	X
	U 0269	7289643	October 2007	Brunk et al.	X
	U 0270	7343492	March 2008	Moskowitz et al.	X
	U 0271	7346472	March 2008	Moskowitz et al.	X
	U 0272	7362775	April 2008	Moskowitz	X
	U 0273	7363278	April 2008	Schmelzer et al.	X
	U 0274	7409073	August 2008	Moskowitz et al.	X

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EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	U 0275	7457962	November 2008	Moskowitz	X
	U 0276	7460994	December 2008	Herre et al.	X
	U 0277	7475246	January 2009	Moskowitz	X
	U 0278	7530102	May 2009	Moskowitz	X
	U 0279	7532725	May 2009	Moskowitz et al.	X
	U 0280	7568100	July 2009	Moskowitz et al.	X
	U 0281	7647502	January 2010	Moskowitz	X
	U 0282	7647503	January 2010	Moskowitz	X
	U 0283	7779261	August 2010	Moskowitz	X
	U 0284	6990453	January 2006	Wang	X
	U 0285	6081597	June 2000	Hoffstein	X
	U 0286	7035049	Apr 2006	Yamamoto	X
	U 0287	7664263	Feb 2010	Moskowitz	X
	U 0288	7286451	Oct 2007	Wirtz	X
	U 0289	6385324	May 2002	Koppen	X
	U 0290	6674858	Jan 2004	Kimura	X
	U 0291	6148333	Nov 2000	Guedalia	X
	U 0292	6418421	Jun 2002	Hurtado	X
	U 0293	6385596	May 2002	Wiser	X
	U 0294	6226618	May 2001	Downs	X
	U 0295	6957330	Oct 2005	Hughes	X
	U 0296	5842213	Nov 1998	Odom	X
	U 0297	5818818	Oct 1998	Soumiya	X

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	U 0298	6590996	Jun 2003	Reed	X
	U 0299	5949055	Sept 1999	Fleet	X
	U 0300	6067622	May 2000	Moore	X
	U 0301	7761712	Jun 2010	Moskowitz	X
	U 0302	7743001	Jun 2010	Vermeulen	X
	U 0303	6865747	Mar 2005	Mercier	
	U 0304	6611599	Aug 2003	Natarajan	
	U 0305	6480937	Nov 2002	Vorbach	
	U 0306	6398245	Jun 2002	Gruse	
	U 0307	6950941	Sept 2005	Lee	
	U 0308	6983058	Jan 2006	Fukuoka	
	U 0309	5675653	Oct 1997	Nelson	
	U 0310	6804453	Oct 2004	Sasamoto	
	U 0311	6178405	Jan 2001	Ouyang	
	U 0312	5839100	Nov 1998	Wegener	
	U 0313	5781184	Jul 1998	Wasserman	
	U 0314	5617506	Apr 1997	Burk	
	U 0315	5327520	Jul 1994	Chen	
	U 0316	5111530	May 1992	Kutaragi	
	U 0317	7095715	Aug 2006	Buckman	
	U 0318	6173322	Jan 2001	Hu	
	U 0319	5754938	May 1998	Herz	

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LISTING OF UNITED STATES PUBLISHED APPLICATIONS - P Series

EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	P 01	20010010078	July 2001	Moskowitz	X
	P 02	20010043594	November 2001	Ogawa et al.	X
	P 03	20020010684	January 2002	Moskowitz	X
	P 04	20020026343	February 2002	Duenke	X
	P 05	20020056041	May 2002	Moskowitz	X
	P 06	20020071556	June 2002	Moskowitz et al.	X
	P 07	20020073043	June 2002	Herman et al.	X
	P 08	20020097873	July 2002	Petrovic	X
	P 09	20020103883	August 2002	Haverstock et al.	X
	P 010	20020161741	October 2002	Wang et al.	X
	P 011	20030126445	July 2003	Wehrenberg	X
	P 012	20030133702	July 2003	Collart	X
	P 013	20030200439	October 2003	Moskowitz	X
	P 014	20030219143	November 2003	Moskowitz et al.	X
	P 015	20040028222	February 2004	Sewell et al.	X
	P 016	20040037449	February 2004	Davis et al.	X
	P 017	20040049695	March 2004	Choi et al.	X
	P 018	20040059918	March 2004	Xu	X
	P 019	20040083369	April 2004	Erlingsson et al.	X
	P 020	20040086119	May 2004	Moskowitz	X
	P 021	20040093521	May 2004	Hamadeh et al.	X
	P 022	20040117628	June 2004	Colvin	X

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	P 023	20040117664	June 2004	Colvin	X
	P 024	20040125983	July 2004	Reed et al.	X
	P 025	20040128514.	July 2004	Rhoads	X
	P 026	20040225894	November 2004	Colvin	X
	P 027	20040243540	December 2004	Moskowitz et al.	X
	P 028	20050135615	June 2005	Moskowitz et al.	X
	P 029	20050160271	July 2005	Brundage et al.	X
	P 030	20050177727	August 2005	Moskowitz et al.	X
	P 031	20050246554	November 2005	Batson	X
	P 032	20060005029	January 2006	Petrovic et al.	X
	P 033	20060013395	January 2006	Brundage et al.	X
	P 034	20060013451	January 2006	Haitsma	X
	P 035	20060041753	February 2006	Haitsma	X
	P 036	20060101269	May 2006	Moskowitz et al.	X
	P 037	20060140403	June 2006	Moskowitz	X
	P 038	20060285722	December 2006	Moskowitz et al.	X
	P 039	20070011458	January 2007	Moskowitz	X
	P 040	20070028113	February 2007	Moskowitz	X
	P 041	20070064940	March 2007	Moskowitz et al.	X
	P 042	20070079131.	April 2007	Moskowitz et al.	X
	P 043	20070083467	April 2007	Lindahl et al.	X
	P 044	20070110240	May 2007	Moskowitz et al.	X
	P 045	20070113094	May 2007	Moskowitz et al.	X

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	P 046	20070127717	June 2007	Herre et al.	X
	P 047	20070226506	September 2007	Moskowitz	X
	P 048	20070253594	November 2007	Lu et al.	X
	P 049	20070294536.	December 2007	Moskowitz et al.	X
	P 050	20070300072	December 2007	Moskowitz	X
	P 051	20070300073	December 2007	Moskowitz	X
	P 052	20080005571	January 2008	Moskowitz	X
	P 053	20080005572	January 2008	Moskowitz	X
	P 054	20080016365	January 2008	Moskowitz	X
	P 055	20080022113	January 2008	Moskowitz	X
	P 056	20080022114	January 2008	Moskowitz	X
	P 057	20080028222	January 2008	Moskowitz	X
	P 058	20080046742	February 2008	Moskowitz	X
	P 059	20080075277	March 2008	Moskowitz et al.	X
	P 060	20080109417	May 2008	Moskowitz	X
	P 061	20080133927	June 2008	Moskowitz et al.	X
	P 062	20080151934	June 2008	Moskowitz et al.	X
	P 063	20090037740	February 2009	Moskowitz	X
	P 064	20090089427	April 2009	Moskowitz et al.	X
	P 065	20090190754	July 2009	Moskowitz et al.	X
	P 066	20090210711	August 2009	Moskowitz	X
	P 067	20090220074	September 2009	Moskowitz et al.	X
	P 068	20100002904	January 2010	Moskowitz	X

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EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	P 069	20100005308	January 2010	Moskowit	X
	P 070	20100098251	Apr 2010	Moskowitz	X
	P 071	20100220861	Sept 2010	Moskowitz	X
	P 072	20100202607	Aug 2010	Moskowitz	X
	P 073	20020047873	June 2002	Petrovic	X
	P 074	20020009208	Jan 2002	Alattar	X
	P 075	20010029580	October 2001	Moskowitz	X
	P 076	20100182570	July 2010	Chota	X
	P 077	20100077220	March 2010	Moskowitz	X
	P 078	20100077219	March 2010	Moskowitz	X
	P 079	20100064140	March 2010	Moskowitz	X
	P 080	20100153734	June 2010	Moskowitz	X
	P 081	20100106736	April 2010	Moskowitz	X
	P 082	20060251291	November 2006	Rhoads	X
	P 083	20030002862	January 2003	Rodriguez	
	P 084	2003005780	May 2003	Hansen	
	P 085	20020152179	Oct 2002	Racov	
	P 086	20030027549	Feb 2003	Kiel	
	P 087	20020057651	May 2002	Roberts	

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LISTING OF FOREIGN AND INTERNATIONAL PATENT DOCUMENTS - F Series

EXAMINER INITIALS	REFERENCE NUMBER (F SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	COUNTRY OR REGION	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL	ENGLISH LANGUAGE TRANSLATION AND/OR IDENTIFICATION OF PRIORXITY APPLICATION IN
	F 01-	EP0372601	Jun., 1990	EP		X
	F 02-	EP0565947	Oct., 1993	EP		X
	F 03-	EP0581317	Feb., 1994	EP		X
	F 04-	EP0649261	Apr., 1995	EP		X
	F 05-	EP0651554	May., 1995	EP		X
	F 06-	EP1354276	Dec., 2007	EP		X
	F 07-	NL 1005523	Sep., 1998	NL		X
	F 08-	WO 9514289	May., 1995	WO		X
	F 09-	WO 9629795	Sep., 1996	WO		X
	F 010-	WO 9724833	Jul., 1997	WO		X
	F 011-	WO 9744736	Nov., 1997	WO		X
	F 012-	WO9837513	Aug., 1998	WO		X
	F 013-	WO 9952271	Oct., 1999	WO		X
	F 014-	WO 9962044	Dec., 1999	WO		X
	F 015-	WO 9963443	Dec., 1999	WO		X
	F 016-	WO9726733	Jan. 1997	WO		X
	F 017-	WO98002864	Jul. 1997	WO		X
	F 018-	WO 0057643	Sept 2000	WO		X
	F 019-	WO 9642151	Dec 1996	WO		X
	F 020-	EP0872073	July 1996	EP		X
	F 021-	WO0118628	March 2001	WO		X
	F 022-	WO0143026	June 2001	WO		X

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	F 023-	WO0203385	Jan 2002	WO		X
	F 024-	WO9701892	June 1995	WO		X
	F 025-	WO9726732	July 1997	WO		X
	F 026-	WO9802864	Jan 1998	WO		X
	F 027-	EP1547337	Mar 2006	EP		X
	F 028-	EP0581317A2	Feb 1994	EP		X
	F 029-	WO023385A1	Oct 2002	WO		X

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LISTING OF NON PATENT LITERATURE - L Series

EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a publication	ENGLISH LANGUAGE TRANSLATION AND/OR OR
	1	L- 01	N/A	US. Appl. No. 08/999,766, filed Jul. 23, 1997, entitled "Steganographic Method and Device", published as 7568100 07-28-2009, cited as U280.	X
	2	L- 02	N/A	EPO Application No. 96919405.9, entitled "Steganographic Method and Device"; published as EP0872073 (A2), 10-21-1998, cited herein as F20.	X
	3	L- 03	N/A	U.S. Appl. No. 11/050,779, filed Feb. 7, 2005, entitled "Steganographic Method and Device", published as 20050177727 A1 08-11-2005, cited herein as P30.	X
	4	L- 04	N/A	U.S. Appl. No. 08/674,726, filed Jul. 2, 1996, entitled "Exchange Mechanisms for Digital Information Packages with Bandwidth Securitization, Multichannel Digital Watermarks, and Key Management", published as 7362775 04-22-2008, cited herein as U272 .	X
	5	L- 05	N/A	U.S. Appl. No. 09/545,589, filed Apr. 7, 2000, entitled "Method and System for Digital Watermarking", published as 7007166 02-28-2006, cited herein as U243	X
	6	L- 06	N/A	U.S. Appl. No. 11/244,213, filed Oct. 5, 2005, entitled "Method and System for Digital Watermarking", published as 2006-0101269 A1 05-11-2006, cited herein as P36	X
	7	L- 07	N/A	U.S. Appl. No. 11/649,026, filed Jan. 3, 2007, entitled "Method and System for Digital Watermarking", published as 2007-0113094 A1 05-17-2007, cited herein as P45.	X
	8	L- 08	N/A	U.S. Appl. No. 09/046,627, filed Mar. 24, 1998, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 6,598,162 07-22-2003, cited herein as U212.	X
	9	L- 09	N/A	U.S. Appl. No. 10/602,777, filed Jun. 25, 2003, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 2004-0086119 A1 05-06-2004, cited herein P20.	X

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EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a publication	ENGLISH LANGUAGE TRANSLATION AND/OR OR
	10	L- 010	N/A	U.S. Appl. No. 09/053,628, filed Apr. 2, 1998, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", 6,205,249 03-20-2001, cited herein as U161.	X
	11	L- 011	N/A	U.S. Appl. No. 09/644,098, filed Aug. 23, 2000, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 7,035,409 04-25-2006, cited herein as U245.	X
	12	L- 012	N/A	Jap. App. No. 2000-542907, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking"; which is a JP national stage of PCT/US1999/007262, published as WO/1999/052271, 10/14/1999, F13 here in above..	X
	13	L- 013	N/A	U.S. Appl. No. 09/767,733, filed Jan. 24, 2001 entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 2001-0010078 A1 07-26-2001, cited herein as P1.	X
	14	L- 014	N/A	U.S. Appl. No. 11/358,874, filed Feb. 21, 2006, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 2006-0140403 A1 06-29-2006, cited herein as P37.	X
	15	L- 015	N/A	U.S. Appl. No. 10/417,231, filed Apr. 17, 2003, entitled "Methods, Systems And Devices For Packet Watermarking And Efficient Provisioning Of Bandwidth", published as 2003-0200439 A1 10-23-2003, cited herein as P13,	X
	16	L- 016	N/A	U.S. Appl. No. 09/789,711, filed Feb. 22, 2001, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2001-0029580 A1 10-11-2001, cited herein as P75.	X
	17	L- 017	N/A	U.S. Appl. No. 11/497,822, filed Aug. 2, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2007-0011458 A1 01-11-2007, cited herein as P39.	X
	18	L- 018	N/A	U.S. Appl. No. 11/599,964, filed Nov. 15, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2008-0046742 A1 02-21-2008, cited herein as P58.	X

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	19	L- 019	N/A	U.S. Appl. No. 11/599,838, filed Nov. 15, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2007-0226506 A1 09-27-2007, cited herein as P47.	X
	20	L- 020	N/A	U.S. Appl. No. 10/369,344, filed Feb. 18, 2003, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digitized Data", published as 2003-0219143 A1 11-27-2003, cited herein as P14.	X
	21	L- 021	N/A	U.S. Appl. No. 11/482,654, filed Jul. 7, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digitized Data", published as 2006-0285722 A1 12-21-2006, cited herein as P38.	X
	22	L- 022	N/A	U.S. Appl. No. 09/594,719, filed Jun. 16, 2000, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 7,123,718 10-17-2006, cited herein as U255.	X
	23	L- 023	N/A	U.S. Appl. No. 11/519,467, filed Sep. 12, 2006, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 2007-0064940 A1 03-22-2007, cited herein as P41.	X
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	25	L- 025	N/A	U.S. Appl. No. 11/512,701, filed Aug. 29, 2006, entitled "Systems, Methods And Devices For Trusted Transactions", published as 2007-0028113 A1 02-01-2007, cited herein as P40.	X
	26	L- 026	N/A	U.S. Appl. No. 10/049,101, filed Feb. 8, 2002, entitled "A Secure Personal Content Server", published as 7,475,246 01-06-2009, cited herein as U277.	X
	27	L- 027	N/A	PCT Application No. PCT/US00/21189, filed Aug. 4, 2000, entitled, "A Secure Personal Content Server", Pub. No.: WO/2001/018628 ; Publication Date: 15.03.2001, cited herein as F21.	X

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	122	L- 0122		DUPLICATE OF L-73, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	123	L- 0123		DUPLICATE OF L-74, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	124	L- 0124		DUPLICATE OF L-75, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	125	L- 0125		DUPLICATE OF L-076, REMOVED. RN. 11/16/2010	X
	126	L- 0126		DUPLICATE OF L-77, REMOVED. RN. 11/16/2010	X
	127	L- 0127		DUPLICATE OF L-78, REMOVED. RN. 11/16/2010	X
	128	L- 0128		DUPLICATE OF L-79, REMOVED. RN. 11/16/2010	X
	129	L- 0129		EP0581317A2, MOVED TO FOREIGN PATENT PUBS as F-028	X
	130	L- 0130		DUPLICATE OF L-52, REMOVED. RN. 11/16/2010	X

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37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET: 13/556,420 / SCOT0014-6
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a publication	ENGLISH LANGUAGE TRANSLATION AND/OR OR
	131	L- 0131		DUPLICATE OF L-36, REMOVED. RN. 11/16/2010	X
	132	L- 0132		DUPLICATE OF L-38, REMOVED. RN. 11/16/2010.	X
	133	L- 0133		DUPLICATE OF L-37, REMOVED. RN. 11/16/2010	X
	134	L- 0134		DUPLICATE OF L-36, REMOVED. RN. 11/16/2010	X
	135	L- 0135		DUPLICATE OF L-37, REMOVED. RN. 11/16/2010	X
	136	L- 0136		DUPLICATE OF L-38, REMOVED. RN. 11/16/2010	X
	137	L- 0137		DUPLICATE OF L-39, REMOVED. RN. 11/16/2010	X
	138	L- 0138		DUPLICATE OF L-40, REMOVED. RN. 11/16/2010	X
	139	L- 0139		DUPLICATE OF L-41, REMOVED. RN. 11/16/2010	X
	140	L- 0140		DUPLICATE OF L-42, REMOVED. RN. 11/16/2010	X
	141	L- 0141		DUPLICATE OF L-43, REMOVED. RN. 11/16/2010	X
	142	L- 0142		DUPLICATE OF L-44, REMOVED. RN. 11/16/2010	X
	143	L- 0143		DUPLICATE OF L-45, REMOVED. RN. 11/16/2010.	X
	144	L- 0144		DUPLICATE OF L-46, REMOVED. RN. 11/16/2010.	X

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	145	L- 0145		DUPLICATE OF L-47, REMOVED. RN. 11/16/2010	X
	146	L- 0146		DUPLICATE OF L-48, REMOVED. RN. 11/16/2010	X
	147	L- 0147		DUPLICATE OF L-49, REMOVED. RN. 11/16/2010	X
	148	L- 0148		DUPLICATE OF L-50, REMOVED. RN. 11/16/2010	X
	149	L- 0149		DUPLICATE OF L-51, REMOVED. RN. 11/16/2010	X
	150	L- 0150		DUPLICATE OF L-52, REMOVED. RN. 11/16/2010	X
	151	L- 0151		DUPLICATE OF L-63, REMOVED. RN. 11/16/2010	X
	152	L- 0152		DUPLICATE OF L-54, REMOVED. RN. 11/16/2010	X
	153	L- 0153		DUPLICATE OF L-55, REMOVED. RN. 11/16/2010.	X
	154	L- 0154		DUPLICATE OF L-80, REMOVED. RN. 11/16/2010.	X
	155	L- 0155	N/A	PCT International Search Report in PCT/US95/08159.	X
	156	L- 0156	N/A	PCT International Search Report in PCT/US96/10257.	X
	157	L- 0157	N/A	Supplementary European Search Report in EP 96919405.	X
	158	L- 0158	N/A	PCT International Search Report in PCT/US97/00651.	X
	159	L- 0159	N/A	PCT International Search Report in PCT/US97/00652	X

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	160	L- 0160	N/A	PCT International Search Report in PCT/US97/11455.	X
	161	L- 0161		PCT International Search Report in PCT/US99/07262.	X
	162	L- 0162		PCT International Search Report in PCT/US00/06522	X
	163	L- 0163		Supplementary European Search Report in EP00919398	X
	164	L- 0164		PCT International Search Report in PCT/US00/18411.	X
	165	L- 0165		PCT International Search Report in PCT/US00/18411.	X
	166	L- 0166		PCT International Search Report in PCT/US00/33126	X
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	191	L- 0191	N/A	PCT Application No. PCT/US95/08159, filed Jun. 26, 1995, entitled, "Digital Information Commodities Exchange with Virtual Menuing", published as WO/1997/001892; Publication Date: 16.01.1997, F24.	X
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	194	L- 0194	N/A	PCT Application No. PCT/US97/00652, filed Jan. 17, 1997, entitled, "Method for an Encrypted Digital Watermark", published as WO/1997/026733; Publication Date: 24.07.1997	X
	195	L- 0195	N/A	PCT Application No. PCT/US97/11455, filed Jul. 2, 1997, entitled, "Optimization Methods for the Insertion, Protection and Detection of Digital Watermarks in Digitized Data", published as WO/1998/002864; Publication Date: 22.01.1998	X
	196	L- 0196	N/A	PCT Application No. PCT/US99/07262, filed Apr. 2, 1999, entitled, "Multiple Transform Utilization and Applications for Secure Digital Watermarking", published as WO/1999/052271; Publication Date: 14.10.1999.	X
	197	L- 0197	N/A	PCT Application No. PCT/US00/06522, filed Mar. 14, 2000, entitled, "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as WO/2000/057643; Publication Date: 28.09.2000.	X
	198	L- 0198	N/A	PCT Application No. PCT/US00/18411, filed Jul. 5, 2000, entitled, "Copy Protection of Digital Data Combining Steganographic and Cryptographic Techniques"	X
	199	L- 0199	N/A	PCT Application No. PCT/US00/33126, filed Dec. 7, 2000, entitled "Systems, Methods and Devices for Trusted Transactions", published as WO/2001/043026; Publication Date: 14.06.2001.	X
	200	L- 0200	N/A	EPO Divisional Patent Application No. 07112420.0, entitled "Steganographic Method and Device" corresponding to PCT Application No. PCT/US96/10257, published as WO/1996/042151, 12/27/1996, cited herein above as F019.	X
	201	L- 0201	N/A	US Provisional Application 60/222,023 filed July 31, 2007 entitled "Method and apparatus for recognizing sound and signals in high noise and distortion"	X

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	203	L- 0203	1995	"Techniques for Data Hiding in Audio Files," by Morimoto, 1995	X
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CONFIRMATION NO. 5811

SERIAL NUMBER	FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
13/556,420	07/24/2012 RULE	726	2432	SCOT0014-6

APPLICANTS

INV001Scott A. Moskowitz, Sunny Isles Beach, FL;

**** CONTINUING DATA ***************************

This application is a CON of 11/895,388 08/24/2007
which is a DIV of 10/602,777 06/25/2003 PAT 7664263
which is a CON of 09/046,627 03/24/1998 PAT 6598162

**** FOREIGN APPLICATIONS ***************************

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****

08/03/2012

Foreign Priority claimed ☐ Yes ☑ No 35 USC 119(a-d) conditions met ☐ Yes ☑ No Verified and Acknowledged /IZUNNA OKEKE/ Examiner's Signature	☐ Met after Allowance Initials	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
		FL	1	1	1

ADDRESS

NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304
UNITED STATES

TITLE

Data protection method and device

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BIB (Rev. 05/07).

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13/556,420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: 2819
ENTITY STATUS: LARGE
Priority claims and PCT Intl data:
This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162, which is a continuation-in-part of U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943 are hereby incorporated by reference in their entireties

37 CFR 1.97 INFORMATION DISCLOSURE STATEMENT

This application is:
XXX within 3 months of the US or 371 national stage filing date;
XXX before first action on the merits (no fee required);
___ after first action on the merits and before final action (1.17(P) fee required);
____ after final action;
____ after notice of allowance and before payment of the issue fee; or
____ after payment of the issue fee.

____ The applicant is paying herewith the fee for obtaining consideration of an IDS filed after a first action on the merits.

IDENTIFICATION OF REFERENCES CITED IN APPLICATIONS TO WHICH 13/556,420 CLAIMS CONTINUING STATUS

REGARDING CITED REFERENCES

This IDS is an attempt to compile all references previously cited in Scott Moskowitz's cases. Upon compilation, some of the reference citations were vague, and some were to filed patent applications instead of published documents. This IDS attempts to account for each item to provide all citations to the examiner. References which have been cited and considered in applications which application 13/556,420 claim 35 USC 120 priority are identified by placement

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of an "X" in the far right had column.

CITED US PATENTS AND US PATENT APPLICATION PUBLICATIONS

Most pending Scott Moskowitz cases claim 35 USC 120 priority to prior cases containing a large number of cited US patents and published US applications. The citations list herein should incorporate all of those documents and may incorporate any additional documents found in other patent applications in patent families not linked by 35 USC 120 to this application. Since no US patent or US published applications need to be filed in order for the examiner to consider citations thereto; the applicant may attempt to correlate the US patents and publications cited herein to those already of record due to citations in applications to which this application claims priority, if the examiner so requests.

FOREIGN PATENT REFERENCES

The IDS cites foreign patent references identified herewith as F01- F029 .

The table below identifies F references cited in this application or an application to which this application claims 35 USC 120 priority.

DOCKET NO	APPLICATION NUMBER	CITED F REFERENCES
SCOT0014-4	11/895,388	F01-F029

Accordingly, the following F references are not yet of record and are submitted herewith: N/A

NON PATENT LITERATURE REFERENCES

The IDS cites foreign patent references identified herewith as L001- L213.

The table below identifies L references cited in this application or in an application to which this application claims 35 USC 120 priority.

DOCKET NO	APPLICATION NUMBER	CITED L REFERENCES
SCOT0014-4	11/895,388	L01-L212
	L reference citations of patent applications as filed for which a subsequent publication of the application is identified and cited herein.	L1-35, L101; L104-L105; L185- L202.
	L reference citation numbers that have no associated reference.	L98; L100; L102; L103; L106-L114; L116-L154

References previously cited, applications for which a subsequent publication is cited, and reference numbers having no associated reference:
L1-L212

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

Accordingly, the following L references are not yet of record and are submitted herewith: L213.

MASTER LIST OF RELATED CASES IN WHICH THE SAME INFORMATION MAY BE CITED

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0010-4	11/599,838	11/15/2006	10/15/2010 JRE
SCOT0010-5	11/899,662	9/7/2007	10/15/2010 JRE
SCOT0010-6	10/369,344	2/18/2003	08/1/2011 JRE
SCOT0010-7	11/482,654	7/7/2006	08/1/2011 JRE
SCOT0010-8	12/215,812	6/30/2008	10/15/2010 JRE
SCOT0010-10	12/901,568	10/10/2010	11/4/2010 JRE
SCOT0010-11	11/497,822	8/2/2006	08/1/2011 JRE
SCOT0010-12	12/217,834	7/9/2008	11/8/2010 JRE
SCOT0010-13	11/897,790	8/31/2007	08/1/2011 JRE
SCOT0010-14	12/462,799	8/10/2009	12/15/2010 JRE
SCOT0010-16	11/899,661	9/7/2007	08/1/2011 JRE
SCOT0010-17	12/590,681	11/19/2009	12/15/2010 JRE
SCOT0010-18	11/897,791	8/31/2007	08/1/2011 JRE
SCOT0010-19	12/590,553	11/10/2009	08/1/2011 JRE
SCOT0010-20	12/592,331	11/23/2009	08/1/2011 JRE
SCOT0010-21	11/599,964	11/15/2006	08/1/2011 JRE
SCOT0010-22	13/212,264	8/18/2011	1/11/2012 JRE
SCOT0011-1	08/674,726	7/2/1996	08/1/2011 JRE
SCOT0011-2	09/545,589	4/7/2000	1/11/2012 JRE
SCOT0011-3	11/244,213	10/5/2005	1/11/2012 JRE
SCOT0011-4	12/009,914	1/23/2008	10/15/2010 JRE

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0011-5	12/005,230	12/26/2007	10/15/2010 JRE
SCOT0011-6	12/803,168	6/21/2010	10/15/2010 JRE
SCOT0011-7	11/649,026	1/3/2007	08/1/2011 JRE
SCOT0011-8	12/803,194	06/21/2010	10/15/2010 JRE
SCOT0011-9	12/892,900	9/28/2010	11/8/2010 JRE
SCOT0012-1	08/489,172	6/7/1995	08/1/2011 JRE
SCOT0012-2	08/775,216	12/31/1996	01/11/2011 JRE
SCOT0012-3	08/999,766	7/23/1997	10/15/2010 JRE
SCOT0012-4	11/894,476	8/21/2007	10/15/2010 JRE
SCOT0012-5	11/050,779	2/7/2005	10/15/2010 JRE
SCOT0012-6	12/802,519	6/8/2010	11/4/2010 JRE
SCOT0012-7	12/383,916	3/30/2009	10/15/2010 JRE
SCOT0012-8	11/894,443	8/21/2007	10/15/2010 JRE
SCOT0012-9	12/913,751	10/27/2010	11/8/2010 JRE
SCOT0013-1	08/587,943	1/17/1996	1/11/2012 JRE
SCOT0014-1	09/046,627	3/24/1998	1/11/2012 JRE
SCOT0014-2	10/602,777	6/25/2003	08/1/2011 JRE
SCOT0014-3 redocketed as SCOT0020-2	11/512,701	8/29/2006	10/15/2010 JRE
SCOT0014-4	11/895,388	8/24/2007	10/15/2010 JRE
SCOT0014-5	12/655,002	12/22/2009	08/1/2011 JRE
SCOT0014-6	13/556,420	7/24/2012	9/17/2012 JRE
SCOT0015-1	09/731,039	12/7/2000	1/11/2012 JRE
SCOT0015-2	11/647/861	12/29/2006	1/11/2012 JRE
SCOT0015-3	12/383,879	3/30/2009	10/15/2010 JRE
SCOT0015-4	12/886,732	9/21/2010	10/15/2010 JRE
SCOT0016-1	10/049,101	7/23/2002	1/11/2012 JRE
SCOT0016-2	12/287,443	10/9/2008	10/15/2010 JRE
SCOT0016-3	13/413,691	3/7/2012	8/30/2012 JRE
SCOT0017-1	09.657,181	9/7/2000	1/11/2012 JRE

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0017-2	12/005,229	12/26/2007	1/11/2012 JRE
SCOT0017-3	12/655,357	12/22/2009	10/15/2010 JRE
SCOT0017-4	13/035,964	2/26/2011	08/1/2011 JRE
SCOT0018-1	10/417/231	4/17/2003	01/11/2011 JRE
SCOT0018-2	11/900,065	9/10/2007	10/15/2010 JRE
SCOT0018-3	11/900,066	9/10/2007	1/11/2012 JRE
SCOT0018-4	12/383,289	3/23/2009	08/1/2011 JRE
SCOT0018-5	13/273,930	10/14/2011	1/11/2012 JRE
SCOT0018-6	13/551,097	7/17/2012	9/18/2012 JRE
SCOT0018-7	13/488,357	6/4/2012	9/9/2012 JRE
SCOT0018-8	13/488,395	6/4/2012	9/9/2012 JRE
SCOT0019-1	09/053,628	4/2/1998	1/11/2012 JRE
SCOT0019-2	09/644,098	8/23/2000	1/11/2012 JRE
SCOT0019-3	11/358,874	2/21/2006	1/11/2012 JRE
SCOT0019-4	12/799,894	5/4/2010	12/13/2010 JRE
SCOT0020-1	09/731,040	12/7/2000	1/11/2012 JRE
SCOT0020-2	11/512,701	8/29/2006	08/1/2011 JRE
SCOT0022-3	12/655,036	12/22/2009	08/1/2011 JRE
SCOT0022-4	13/423,650	3/19/2012	7/26/2012 JRE
SCOT0023-5	12/798,959	4/14/2010	08/1/2011 JRE
SCOT0024-2	11/518,806	9/11/2006	08/1/2011 JRE
SCOT0024-3	13/429,396	3/25/2012	7/26/2012 JRE

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /I.O./

AS OF 1/12/2012, THE FOLLOWING TABLE COLLATES ADDITIONAL REFERENCES CITED IN ANY SCOT (SCOTT MOSKOWITZ) CASE

Date of Docuemnt Citing Referece	Atty Ref	Application Number	ID of paper which references were cited	References checked to see if they existed in the master IDS (initials of person checking)	Reference Identifiers of New references in document, now added to master IDS
Sept 14, 2010	SCOT0012-7	12/383,916	892	JRE	U#299
11/17/2010	ALL	N/A	Review of draft master IDS, correction to cite publications in lieu of filed applications, per RAN instructions.	JRE	P76-P82
12/9/2010	SCOT0018-2	11/900,065	892	JRE	U303 & P83
11/30/2010	SCOT0019-4	12/799,894	892	JRE	U304
11/21/2011	SCOT0016-2	12/287,443	892	JRE	U305, U306 & U307
1/12/2012	SCOT0011-8	12/803,194	892	JRE	U308
1/12/2012	SCOT0014-5	12/655,002	892	JRE	U309
1/12/2012	SCOT0017-4	13/035,964	892	JRE	U310-U316
1/12/2012	SCOT0018-2	11/900,065	892	JRE	P84-P85
3/7/2012	SCOT0018-2	11/900,065	892	JRE	P86 -P87 & U317
8/30/2012	SCOT0016-3	13/413,691	892	JRE	U318 & U319

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9/17/2012	SCOT0014-6			JRE	L213

NOTE: MPEP 609.02 Information Disclosure Statements in Continued Examinations or Continuing Applications [R-5] states in part that:

"2. Continuation Applications, Divisional Applications, or Continuation-in-Part Applications Filed Under 37 CFR 1.53(b) The examiner will consider information which has been considered by the Office in a parent application when examining: (A) a continuation application filed under 37 CFR 1.53(b), (B) a divisional application filed under 37 CFR 1.53(b), or (C) a continuation-in-part application filed under 37 CFR 1.53(b). A listing of the information need not be resubmitted in the continuing application unless the applicant desires the information to be printed on the patent."

See http://www.uspto.gov/web/offices/pac/mpep/documents/0600_609_02.htm.

Accordingly, we are submitting only references not cited in applications to which this application claims priority under 35 USC 120.

Please consider the references cited herein.

DATE: 10-4-2012 SIGNATURE: /RichardNeifeld/
PRINTED NAME: RICHARD NEIFELD, REG. NO. 35,299
Printed: October 4, 2012 (11:12am)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\IDS_SCOT0014-6_10-3-2012.wpd

/Izunna Okeke/ 03/21/2013

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EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S6	112	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or arrang$3) same (code or object) same (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 08:38
S10	62	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory or register)) and ((protect$3 or stop$4 or inhibit$3) with (memory or register) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 09:25
S11	27	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory or register)) and ((protect$3 or stop$4 or inhibit$3) with (memory or register) with analysis) and ((run or execution) adj time) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 09:26
S12	41	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or arrang$3) same (code or object) same (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 09:28
S13	50	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 09:28
S15	2	((shuffl$3 or arrang$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 09:29
S19	1256	((shuffl$3 or arrang$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 11:40
S20	4	((shuffl$3 or arrang$3) with (code) with (memory)) and ((modif$5 or alter$5 or chang$5) with (program adj counter)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 11:42
S21	134	((shuffl$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 11:44
S22	90	((shuffl$3) near5 (code) near5 (memory)) and (@ad<"19960117" or	US-PGPUB;	AND	ON	2013/03/20

		@rlad<"19960117")	USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			11:44
S23	54	((shuffl$3 or arrang$3) same (code) same (memory)) and ((modif$5 or alter$5 or chang$5) same (program adj counter)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 11:45
S26	36	((shuffl$3 or (arrang$3 or order) or transpos$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 13:50
S27	46	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) and ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 13:57
S28	5	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) same ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/20 13:58
S35	28320	713/1,151,165,166,167,193,194,152,187,189,188,190;726/26;380/201;711/1,100.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:08
S36	112	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or arrang$3) same (code or object) same (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:08
S37	2	S35 and S36	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:08
S38	62	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory or register)) and ((protect$3 or stop$4 or inhibit$3) with (memory or register) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:08
S39	1	S35 and S38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:08
S40	27	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory or register)) and ((protect$3 or stop$4 or inhibit$3) with (memory or register) with analysis) and ((run or execution) adj time) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:08

S42	41	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or arrang$3) same (code or object) same (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S43	1	S35 and S42	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S44	50	((shuffl$3 or rearrang$3 or modif$5 or alter$5 or chang$5) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S45	1	S35 and S44	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S46	2	((shuffl$3 or arrang$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S48	1256	((shuffl$3 or arrang$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S49	57	S35 and S48	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S50	4	((shuffl$3 or arrang$3) with (code) with (memory)) and ((modif$5 or alter$5 or chang$5) with (program adj counter)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S52	134	((shuffl$3) with (code) with (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S53	47	S35 and S52	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S54	90	((shuffl$3) near5 (code) near5 (memory)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO;	AND	ON	2013/03/21 22:09

			DERWENT; IBM_TDB			
S55	46	S35 and S54	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S56	54	((shuffl$3 or arrang$3) same (code) same (memory)) and ((modif$5 or alter$5 or chang$5) same (program adj counter)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S57	1	S35 and S56	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S58	36	((shuffl$3 or (arrang$3 or order) or transpos$3) same (code or object) same (memory)) and ((protect$3 or stop$4 or inhibit$3) with (memory) with analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S59	1	S35 and S58	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S60	46	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) and ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S61	2	S35 and S60	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S62	5	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3) same ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:09
S64	46	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) and ((protect$3 or stop$4 or inhibit$3 or prevent$3) near5 (memory) near5 analysis) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:17
S68	5479	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) with ((code or software) near5 (memory or register or buffer)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:18
S69	3581	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) with ((code or software) near3 (memory or register or buffer)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR;	AND	ON	2013/03/21 22:18

			FPRS; EPO; JPO; DERWENT; IBM_TDB			
S70	3277	(shuffl$3 or transpos$5 or $3arrang$3 or $3order$3 or permut$5) with ((code or software) near3 (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:19
S71	111	S35 and S68	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:19
S72	2	S35 and S64	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:19
S73	74	S35 and S70	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:19
S74	759	(shuffl$3 or transpos$5 or $3arrang$3 or permut$5) with ((code or software) near3 (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:19
S75	675	(shuffl$3 or transpos$5 or $3arrang$3 or permut$5) near5 ((code or software) near5 (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:19
S76	51	S35 and S74	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:20
S77	50	S35 and S75	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:20
S78	314	(shuffl$3 or transpos$5 or $3arrang$3 or permut$5) near3 ((code or software) near3 (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:20
S79	49	S35 and S78	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 22:20
S82	117	((modif$5 or alter$5 or chang$5) with (program adj counter)) and ((shuffl$3 or	US-PGPUB;	AND	ON	2013/03/21

		$3arrang$3) same (code or object) same (memory or register)) and (@ad<"19960117" or @rlad<"19960117")	USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			23:28
S83	2	S35 and S82	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2013/03/21 23:28

EAST Search History (Interference)

< This search history is empty>

3/21/2013 11:32:16 PM
C:\Users\iokeke\Documents\EAST\Workspaces\13556420.wsp

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APPLICATION NUMBER	FILING OR 371(C) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6

CONFIRMATION NO. 5811

31518
NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

PUBLICATION NOTICE

OC000000058580792

Title: Data protection method and device

Publication No.US-2013-0014271-A1
Publication Date:01/10/2013

NOTICE OF PUBLICATION OF APPLICATION

The above-identified application will be electronically published as a patent application publication pursuant to 37 CFR 1.211, et seq. The patent application publication number and publication date are set forth above.

The publication may be accessed through the USPTO's publically available Searchable Databases via the Internet at www.uspto.gov. The direct link to access the publication is currently http://www.uspto.gov/patft/.

The publication process established by the Office does not provide for mailing a copy of the publication to applicant. A copy of the publication may be obtained from the Office upon payment of the appropriate fee set forth in 37 CFR 1.19(a)(1). Orders for copies of patent application publications are handled by the USPTO's Office of Public Records. The Office of Public Records can be reached by telephone at (703) 308-9726 or (800) 972-6382, by facsimile at (703) 305-8759, by mail addressed to the United States Patent and Trademark Office, Office of Public Records, Alexandria, VA 22313-1450 or via the Internet.

In addition, information on the status of the application, including the mailing date of Office actions and the dates of receipt of correspondence filed in the Office, may also be accessed via the Internet through the Patent Electronic Business Center at www.uspto.gov using the public side of the Patent Application Information and Retrieval (PAIR) system. The direct link to access this status information is currently http://pair.uspto.gov/. Prior to publication, such status information is confidential and may only be obtained by applicant using the private side of PAIR.

Further assistance in electronically accessing the publication, or about PAIR, is available by calling the Patent Electronic Business Center at 1-866-217-9197.

Office of Data Managment, Application Assistance Unit (571) 272-4000, or (571) 272-4200, or 1-888-786-0101

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: Unknown
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**$998
Number of independent claims in excess of 3: 3 * 250=$750
(Fee codes 1201/2201 1.16(h))
Number of dependent claims in excess of 20: 4*62+248
(Fee codes 1202/2202 1.16(I))
3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
37 CFR 1.113 PRELIMINARY AMENDMENT - CLAIMS
4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: BankAcct#6, PClaw billing matter, G/L, check, amount, and entry date:
6, 5010, SCOT0001, 1584, $998, 11/30/12
Service Fees: Amount/CreditAtty/entry date/Services: $400

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: ran
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 11-30-2012 **SIGNATURE**: /RichardNeifeld/
Printed: November 30, 2012 (2:06pm) RICHARD NEIFELD, RE
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\PreliminaryAmendment_Claims__SCOT0014-6_11-30-2012.wpd

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: Unknown
ENTITY STATUS: LARGE

37 CFR 1.113 PRELIMINARY AMENDMENT

REMARKS

This amendment presents claims for examination.

/RichardNeifeld/
RICHARD NEIFELD, REG. NO. 35,299
ATTORNEY OF RECORD

IN THE CLAIMS

1-57. (Canceled)

58. (New) A computing device for running application software, comprising:

memory designed to store data in non transitory form;

an operating system;

wherein said memory stores an application software;

wherein said application software comprises (1) a memory scheduler code resource and (2) other code resources;

wherein said application software is designed to call said memory scheduler code resource;

wherein said memory scheduler code resource, when called, functions to shuffle said other code resources in memory.

59. (New) The device of claim 58 wherein said memory scheduler code resource, when called, functions to shuffle said other code resources randomly in memory.

60. (New) The device of claim 58 further comprising a program counter designed to store values and wherein said computing device is designed to enable application software to modify a value stored by said program counter.

61. (New) The device of claim 58 further comprising a program counter designed to store values and wherein said memory scheduler is designed to modify a value stored by said program counter.

62. (New) The device of claim 58 wherein said memory scheduler is designed to modify a stack frame, to modify a calling address, and to copy itself to a memory location associated with said calling address.

63. (New) The device of claim 58 wherein said operating system is designed to call said memory scheduler.

64. (New) The device of claim 58 wherein said memory scheduler maintains a list of addresses in memory, said addresses associated with at least one of the code resources.

65. (New) The device of claim 58 wherein said memory scheduler maintains a list of addresses in memory associated with said other code resources.

66. (New) The device of claim 58 further comprising a processor for processing instructions defined by said application software.

67. (New) A system for executing application software code, comprising:

a memory designed to store data in non transitory form, and storing executable code resources;

wherein said executable code resources comprise a memory scheduler and other executable code resources; and

wherein said memory scheduler is designed to shuffle said other executable code resources in memory.

68. (New) The system of claim 67, wherein said memory scheduler is designed to shuffle said other executable code resources randomly in memory.

69. (New) The system of claim 67 wherein said shuffling results in a change in location in memory of at least one of said other executable code resources.

70. (New) The system of claim 67 further comprising a program counter designed to store values and wherein said memory scheduler is designed to modify values stored by said program counter.

71. (New) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of at least one code resource.

72. (New) The system of claim 67 wherein said memory scheduler maintains a list of addresses in memory of said other code resources.

73. (New) The system of claim 67 designed to call said memory scheduler.

74. (New) The system of claim 67 wherein said application software is designed to call said memory scheduler.

75. (New) A system for executing an application on a computer system, comprising:

a computer system comprising a processor for processing instructions and memory designed to store data in non transitory form;

a software application comprising a plurality of executable code resources loaded in said memory;

said memory storing a plurality of addresses, each one of said plurality of addresses associated with an address of a corresponding one of said plurality of executable code resources; and

wherein a first one of said plurality of executable code resource is designed to relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application, using said processor.

76. (New) The system of claim 75 wherein said first one is also designed to relocate itself to a different address during execution of said software application.

77. (New) The system of claim 75 wherein said system is designed to repeatedly relocate at least each of the other ones of said plurality of executable code resources to different addresses within said memory during execution of said software application.

78. (New) A system comprising:

processor;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load a program comprising code resources into said memory and to shuffle at least one code resource into a different location in said memory after it has been loaded into memory.

79. (New) A system comprising:

processor for processing instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

wherein said system is configured to load an executable comprising at least two code resources into memory and to randomize the location of at least one of the at least two code resources in the memory address, using said processor

80. (New) A system for executing a program comprising code, comprising:

processor for processing instructions;

a memory designed to store data in non transitory form;

wherein said processor is coupled to said memory;

a memory scheduler configured to randomize the location of at least a portion of the code in memory, using said processor.

81. (New) The system of claim 80 wherein said memory scheduler is configured to repeatedly randomize the location of at least a portion of the code in memory.

Electronic Patent Application Fee Transmittal

Application Number:	13556420
Filing Date:	24-Jul-2012
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Filer:	Richard A. Neifeld
Attorney Docket Number:	SCOT0014-6

Filed as Large Entity

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Claims in excess of 20	1202	4	62	248
Independent claims in excess of 3	1201	3	250	750
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Extension-of-Time:				
Miscellaneous:				
		Total in USD ($)		**998**

Electronic Acknowledgement Receipt	
EFS ID:	14352177
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Richard A. Neifeld
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	30-NOV-2012
Filing Date:	24-JUL-2012
Time Stamp:	14:11:20
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	$998
RAM confirmation Number	616
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)

1		PreliminaryAmendment_Claims__SCOT0014-6_11-30-2012.pdf	362066 f7ce00610f4add1518b037c223fcd246f9c7b51f	yes	7
	Multipart Description/PDF files in .zip description				
	Document Description		**Start**	**End**	
	Miscellaneous Incoming Letter		1	1	
	Applicant Arguments/Remarks Made in an Amendment		2	2	
	Claims		3	7	
Warnings:					
Information:					
2	Fee Worksheet (SB06)	fee-info.pdf	31748 9bb037610337eb9b54df3edffbc99e9a17393946	no	2
Warnings:					
Information:					
		Total Files Size (in bytes):	393814		

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

PTO/SB/06 (07-06)
Approved for use through 1/31/2007. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
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PATENT APPLICATION FEE DETERMINATION RECORD

Substitute for Form PTO-875

Application or Docket Number	Filing Date	☐ To be Mailed
13/556,420	07/24/2012	

APPLICATION AS FILED – PART I

	(Column 1)	(Column 2)	SMALL ENTITY ☐		OR	OTHER THAN SMALL ENTITY	
FOR	NUMBER FILED	NUMBER EXTRA	RATE ($)	FEE ($)		RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A			N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A			N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A			N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =		OR	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =			X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $250 ($125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))							
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	

APPLICATION AS AMENDED – PART II

AMENDMENT	11/30/2012	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	SMALL ENTITY RATE ($)	ADDITIONAL FEE ($)	OR	OTHER THAN SMALL ENTITY RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	* 24	Minus	** 20	= 4	X $ =		OR	X $62=	248
	Independent (37 CFR 1.16(h))	* 6	Minus	***3	= 3	X $ =		OR	X $250=	750
	☐ Application Size Fee (37 CFR 1.16(s))									
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	998

AMENDMENT		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)	OR	RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =		OR	X $ =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =		OR	X $ =	
	☐ Application Size Fee (37 CFR 1.16(s))									
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".

*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".

The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

Legal Instrument Examiner:
/BRENDA J. DENNY/

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13/556,420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: 2819
ENTITY STATUS: LARGE
Priority claims and PCT Intl data:
This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162, which is a continuation-in-part of U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943 are hereby incorporated by reference in their entireties

37 CFR 1.97 INFORMATION DISCLOSURE STATEMENT

This application is:
XXX within 3 months of the US or 371 national stage filing date;
XXX before first action on the merits (no fee required);
___ after first action on the merits and before final action (1.17(P) fee required);
____ after final action;
____ after notice of allowance and before payment of the issue fee; or
____ after payment of the issue fee.

____ The applicant is paying herewith the fee for obtaining consideration of an IDS filed after a first action on the merits.

IDENTIFICATION OF REFERENCES CITED IN APPLICATIONS TO WHICH 13/556,420 CLAIMS CONTINUING STATUS

REGARDING CITED REFERENCES

This IDS is an attempt to compile all references previously cited in Scott Moskowitz's cases. Upon compilation, some of the reference citations were vague, and some were to filed patent applications instead of published documents. This IDS attempts to account for each item to provide all citations to the examiner. References which have been cited and considered in applications which application 13/556,420 claim 35 USC 120 priority are identified by placement

of an "X" in the far right had column.

CITED US PATENTS AND US PATENT APPLICATION PUBLICATIONS

Most pending Scott Moskowitz cases claim 35 USC 120 priority to prior cases containing a large number of cited US patents and published US applications. The citations list herein should incorporate all of those documents and may incorporate any additional documents found in other patent applications in patent families not linked by 35 USC 120 to this application. Since no US patent or US published applications need to be filed in order for the examiner to consider citations thereto; the applicant may attempt to correlate the US patents and publications cited herein to those already of record due to citations in applications to which this application claims priority, if the examiner so requests.

FOREIGN PATENT REFERENCES

The IDS cites foreign patent references identified herewith as F01- F029 .

The table below identifies F references cited in this application or an application to which this application claims 35 USC 120 priority.

DOCKET NO	APPLICATION NUMBER	CITED F REFERENCES
SCOT0014-4	11/895,388	F01-F029

Accordingly, the following F references are not yet of record and are submitted herewith: N/A

NON PATENT LITERATURE REFERENCES

The IDS cites foreign patent references identified herewith as L001- L213.

The table below identifies L references cited in this application or in an application to which this application claims 35 USC 120 priority.

DOCKET NO	APPLICATION NUMBER	CITED L REFERENCES
SCOT0014-4	11/895,388	L01-L212
	L reference citations of patent applications as filed for which a subsequent publication of the application is identified and cited herein.	L1-35, L101; L104-L105; L185- L202.
	L reference citation numbers that have no associated reference.	L98; L100; L102; L103; L106-L114; L116-L154

References previously cited, applications for which a subsequent publication is cited, and reference numbers having no associated reference:
L1-L212

Accordingly, the following L references are not yet of record and are submitted herewith: L213.

MASTER LIST OF RELATED CASES IN WHICH THE SAME INFORMATION MAY BE CITED

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0010-4	11/599,838	11/15/2006	10/15/2010 JRE
SCOT0010-5	11/899,662	9/7/2007	10/15/2010 JRE
SCOT0010-6	10/369,344	2/18/2003	08/1/2011 JRE
SCOT0010-7	11/482,654	7/7/2006	08/1/2011 JRE
SCOT0010-8	12/215,812	6/30/2008	10/15/2010 JRE
SCOT0010-10	12/901,568	10/10/2010	11/4/2010 JRE
SCOT0010-11	11/497,822	8/2/2006	08/1/2011 JRE
SCOT0010-12	12/217,834	7/9/2008	11/8/2010 JRE
SCOT0010-13	11/897,790	8/31/2007	08/1/2011 JRE
SCOT0010-14	12/462,799	8/10/2009	12/15/2010 JRE
SCOT0010-16	11/899,661	9/7/2007	08/1/2011 JRE
SCOT0010-17	12/590,681	11/19/2009	12/15/2010 JRE
SCOT0010-18	11/897,791	8/31/2007	08/1/2011 JRE
SCOT0010-19	12/590,553	11/10/2009	08/1/2011 JRE
SCOT0010-20	12/592,331	11/23/2009	08/1/2011 JRE
SCOT0010-21	11/599,964	11/15/2006	08/1/2011 JRE
SCOT0010-22	13/212,264	8/18/2011	1/11/2012 JRE
SCOT0011-1	08/674,726	7/2/1996	08/1/2011 JRE
SCOT0011-2	09/545,589	4/7/2000	1/11/2012 JRE
SCOT0011-3	11/244,213	10/5/2005	1/11/2012 JRE
SCOT0011-4	12/009,914	1/23/2008	10/15/2010 JRE

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0011-5	12/005,230	12/26/2007	10/15/2010 JRE
SCOT0011-6	12/803,168	6/21/2010	10/15/2010 JRE
SCOT0011-7	11/649,026	1/3/2007	08/1/2011 JRE
SCOT0011-8	12/803,194	06/21/2010	10/15/2010 JRE
SCOT0011-9	12/892,900	9/28/2010	11/8/2010 JRE
SCOT0012-1	08/489,172	6/7/1995	08/1/2011 JRE
SCOT0012-2	08/775,216	12/31/1996	01/11/2011 JRE
SCOT0012-3	08/999,766	7/23/1997	10/15/2010 JRE
SCOT0012-4	11/894,476	8/21/2007	10/15/2010 JRE
SCOT0012-5	11/050,779	2/7/2005	10/15/2010 JRE
SCOT0012-6	12/802,519	6/8/2010	11/4/2010 JRE
SCOT0012-7	12/383,916	3/30/2009	10/15/2010 JRE
SCOT0012-8	11/894,443	8/21/2007	10/15/2010 JRE
SCOT0012-9	12/913,751	10/27/2010	11/8/2010 JRE
SCOT0013-1	08/587,943	1/17/1996	1/11/2012 JRE
SCOT0014-1	09/046,627	3/24/1998	1/11/2012 JRE
SCOT0014-2	10/602,777	6/25/2003	08/1/2011 JRE
SCOT0014-3 redocketed as SCOT0020-2	11/512,701	8/29/2006	10/15/2010 JRE
SCOT0014-4	11/895,388	8/24/2007	10/15/2010 JRE
SCOT0014-5	12/655,002	12/22/2009	08/1/2011 JRE
SCOT0014-6	13/556,420	7/24/2012	9/17/2012 JRE
SCOT0015-1	09/731,039	12/7/2000	1/11/2012 JRE
SCOT0015-2	11/647/861	12/29/2006	1/11/2012 JRE
SCOT0015-3	12/383,879	3/30/2009	10/15/2010 JRE
SCOT0015-4	12/886,732	9/21/2010	10/15/2010 JRE
SCOT0016-1	10/049,101	7/23/2002	1/11/2012 JRE
SCOT0016-2	12/287,443	10/9/2008	10/15/2010 JRE
SCOT0016-3	13/413,691	3/7/2012	8/30/2012 JRE
SCOT0017-1	09.657,181	9/7/2000	1/11/2012 JRE

DOCKET REFERENCE	APPLICATION	FILING DATE	DATE CASE ADDED TO THIS MASTER LIST OF RELATED CASES
SCOT0017-2	12/005,229	12/26/2007	1/11/2012 JRE
SCOT0017-3	12/655,357	12/22/2009	10/15/2010 JRE
SCOT0017-4	13/035,964	2/26/2011	08/1/2011 JRE
SCOT0018-1	10/417/231	4/17/2003	01/11/2011 JRE
SCOT0018-2	11/900,065	9/10/2007	10/15/2010 JRE
SCOT0018-3	11/900,066	9/10/2007	1/11/2012 JRE
SCOT0018-4	12/383,289	3/23/2009	08/1/2011 JRE
SCOT0018-5	13/273,930	10/14/2011	1/11/2012 JRE
SCOT0018-6	13/551,097	7/17/2012	9/18/2012 JRE
SCOT0018-7	13/488,357	6/4/2012	9/9/2012 JRE
SCOT0018-8	13/488,395	6/4/2012	9/9/2012 JRE
SCOT0019-1	09/053,628	4/2/1998	1/11/2012 JRE
SCOT0019-2	09/644,098	8/23/2000	1/11/2012 JRE
SCOT0019-3	11/358,874	2/21/2006	1/11/2012 JRE
SCOT0019-4	12/799,894	5/4/2010	12/13/2010 JRE
SCOT0020-1	09/731,040	12/7/2000	1/11/2012 JRE
SCOT0020-2	11/512,701	8/29/2006	08/1/2011 JRE
SCOT0022-3	12/655,036	12/22/2009	08/1/2011 JRE
SCOT0022-4	13/423,650	3/19/2012	7/26/2012 JRE
SCOT0023-5	12/798,959	4/14/2010	08/1/2011 JRE
SCOT0024-2	11/518,806	9/11/2006	08/1/2011 JRE
SCOT0024-3	13/429,396	3/25/2012	7/26/2012 JRE

AS OF 1/12/2012, THE FOLLOWING TABLE COLLATES ADDITIONAL REFERENCES CITED IN ANY SCOT (SCOTT MOSKOWITZ) CASE

Date of Docuemnt Citing Referece	Atty Ref	Application Number	ID of paper which references were cited	References checked to see if they existed in the master IDS (initials of person checking)	Reference Identifiers of New references in document, now added to master IDS
Sept 14, 2010	SCOT0012-7	12/383,916	892	JRE	U#299
11/17/2010	ALL	N/A	Review of draft master IDS, correction to cite publications in lieu of filed applications, per RAN instructions.	JRE	P76-P82
12/9/2010	SCOT0018-2	11/900,065	892	JRE	U303 & P83
11/30/2010	SCOT0019-4	12/799,894	892	JRE	U304
11/21/2011	SCOT0016-2	12/287,443	892	JRE	U305, U306 & U307
1/12/2012	SCOT0011-8	12/803,194	892	JRE	U308
1/12/2012	SCOT0014-5	12/655,002	892	JRE	U309
1/12/2012	SCOT0017-4	13/035,964	892	JRE	U310-U316
1/12/2012	SCOT0018-2	11/900,065	892	JRE	P84-P85
3/7/2012	SCOT0018-2	11/900,065	892	JRE	P86 -P87 & U317
8/30/2012	SCOT0016-3	13/413,691	892	JRE	U318 & U319

9/17/2012	SCOT0014-6			JRE	L213

NOTE: MPEP 609.02 Information Disclosure Statements in Continued Examinations or Continuing Applications [R-5] states in part that:

"2. Continuation Applications, Divisional Applications, or Continuation-in-Part Applications Filed Under 37 CFR 1.53(b) The examiner will consider information which has been considered by the Office in a parent application when examining: (A) a continuation application filed under 37 CFR 1.53(b), (B) a divisional application filed under 37 CFR 1.53(b), or (C) a continuation-in-part application filed under 37 CFR 1.53(b). A listing of the information need not be resubmitted in the continuing application unless the applicant desires the information to be printed on the patent."

See http://www.uspto.gov/web/offices/pac/mpep/documents/0600_609_02.htm.

Accordingly, we are submitting only references not cited in applications to which this application claims priority under 35 USC 120.

Please consider the references cited herein.

DATE: 10-4-2012 SIGNATURE: /RichardNeifeld/
PRINTED NAME: RICHARD NEIFELD, REG. NO. 35,299
Printed: October 4, 2012 (11:12am)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\IDS_SCOT0014-6_10-3-2012.wpd

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37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET: 13/556,420 / SCOT0014-6
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

LISTING OF UNITED STATES PATENTS - U series

EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	U 01	3947825	March 1976	Cassada	X
	U 02	3984624	October 1976	Waggener	X
	U 03	3986624	October 1976	Cates, Jr. et al.	X
	U 04	4038596	July 1977	Lee	X
	U 05	4200770	April 1980	Hellman et al.	X
	U 06	4218582	August 1980	Hellman et al.	X
	U 07	4339134	July 1982	Macheel	X
	U 08	4390898	June 1983	Bond et al.	X
	U 09	4405829	September 1983	Rivest et al.	X
	U 010	4424414	January 1984	Hellman et al.	X
	U 011	4528588	July 1985	Lofberg	X
	U 012	4672605	June 1987	Hustig et al.	X
	U 013	4748668	May 1988	Shamir et al.	X
	U 014	4789928	December 1988	Fujisaki	X
	U 015	4827508	May 1989	Shear	X
	U 016	4876617	October 1989	Best et al.	X
	U 017	4896275	January 1990	Jackson	X
	U 018	4908873	March 1990	Philibert et al.	X
	U 019	4939515	July 1990	Adelson	X
	U 020	4969204	November 1990	Melnychuk et al.	X
	U 021	4972471	November 1990	Gross et al.	X
	U 022	4977594	December 1990	Shear	X

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	U 023	4979210	December 1990	Nagata et al.	X
	U 024	4980782	December 1990	Ginkel	X
	U 025	5050213	September 1991	Shear	X
	U 026	5073925	December 1991	Nagata et al.	X
	U 027	5077665	December 1991	Silverman et al.	X
	U 028	5113437	May 1992	Best et al.	X
	U 029	5136581	August 1992	Muehrcke	X
	U 030	5136646	August 1992	Haber et al.	X
	U 031	5136647	August 1992	Haber et al.	X
	U 032	5142576	August 1992	Nadan	X
	U 033	5161210	November 1992	Druyvesteyn et al.	X
	U 034	5210820	May 1993	Kenyon	X
	U 035	5243423	September 1993	DeJean et al.	X
	U 036	5243515	September 1993	Lee	X
	U 037	5287407	February 1994	Holmes	X
	U 038	5319735	June 1994	Preuss et al.	X
	U 039	5341429	August 1994	Stringer et al.	X
	U 040	5341477	August 1994	Pitkin et al.	X
	U 041	5363448	November 1994	Koopman et al.	X
	U 042	5365586	November 1994	Indeck et al.	X
	U 043	5369707	November 1994	Follendore, III	X
	U 044	5379345	January 1995	Greenberg	X
	U 045	5394324	February 1995	Clearwater	X

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	U 046	5398285	March 1995	Borgelt et al.	X
	U 047	5406627	April 1995	Thompson et al.	X
	U 048	5408505	April 1995	Indeck et al.	X
	U 049	5410598	April 1995	Shear	X
	U 050	5412718	May 1995	Narasimhalv et al.	X
	U 051	5418713	May 1995	Allen	X
	U 052	5428606	June 1995	Moskowitz	X
	U 053	5450490	September 1995	Jensen et al.	X
	U 054	5469536	November 1995	Blank	X
	U 055	5471533	November 1995	Wang et al.	X
	U 056	5478990	December 1995	Montanari et al.	X
	U 057	5479210	December 1995	Cawley et al.	X
	U 058	5487168	January 1996	Geiner et al.	X
	U 059	5493677	February 1996	Balogh et al.	X
	U 060	5497419	March 1996	Hill	X
	U 061	5506795	April 1996	Yamakawa	X
	U 062	5513126	April 1996	Harkins et al.	X
	U 063	5513261	April 1996	Maher	X
	U 064	5530739	June 1996	Okada	X
	U 065	5530751	June 1996	Morris	X
	U 066	5530759	June 1996	Braudaway et al.	X
	U 067	5539735	July 1996	Moskowitz	X
	U 068	5548579	August 1996	Lebrun et al.	X

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	U 069	5568570	October 1996	Rabbani	X
	U 070	5579124	November 1996	Aijala et al.	X
	U 071	5581703	December 1996	Baugher et al.	X
	U 072	5583488	December 1996	Sala et al.	X
	U 073	5598470	January 1997	Cooper et al.	X
	U 074	5606609	February 1997	Houser et al.	X
	U 075	5613004	March 1997	Cooperman et al.	X
	U 076	5617119	April 1997	Briggs et al.	X
	U 077	5625690	April 1997	Michel et al.	X
	U 078	5629980	May 1997	Stefik et al.	X
	U 079	5633932	May 1997	Davis et al.	X
	U 080	5634040	May 1997	Her et al.	X
	U 081	5636276	June 1997	Brugger	X
	U 082	5636292	June 1997	Rhoads	X
	U 083	5640569	June 1997	Miller et al.	X
	U 084	5646997	July 1997	Barton	X
	U 085	5657461	August 1997	Harkins et al.	X
	U 086	5659726	August 1997	Sandford, II et al.	X
	U 087	5664018	September 1997	Leighton	X
	U 088	5673316	September 1997	Auerbach et al.	X
	U 089	5677952	October 1997	Blakely et al.	X
	U 090	5680462	October 1997	Miller et al.	X
	U 091	5687236	November 1997	Moskowitz et al.	X

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	U 092	5689587	November 1997	Bender et al.	X
	U 093	5696828	December 1997	Koopman, Jr.	X
	U 094	5719937	February 1998	Warren et al.	X
	U 095	5721788	February 1998	Powell et al.	X
	U 096	5734752	March 1998	Knox	X
	U 097	5737416	April 1998	Cooper et al.	X
	U 098	5737733	April 1998	Eller	X
	U 099	5740244	April 1998	Indeck et al.	X
	U 0100	5745569	April 1998	Moskowitz et al.	X
	U 0101	5748783	May 1998	Rhoads	X
	U 0102	5751811	May 1998	Magnotti et al.	X
	U 0103	5754697	May 1998	Fu et al.	X
	U 0104	5757923	May 1998	Koopman, Jr.	X
	U 0105	5765152	June 1998	Erickson	X
	U 0106	5768396	June 1998	Sone	X
	U 0107	5774452	June 1998	Wolosewicz	X
	U 0108	5790677	August 1998	Fox et al.	X
	U 0109	5799083	August 1998	Brothers et al.	X
	U 0110	5809139	September 1998	Grirod et al.	X
	U 0111	5809160	September 1998	Powell et al.	X
	U 0112	5822432	October 1998	Moskowitz et al.	X
	U 0113	5828325	October 1998	Wolosewicz et al.	X

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	U 0114	5832119	November 1998	Rhoads	X
	U 0115	5848155	December 1998	Cox	X
	U 0116	5850481	December 1998	Rhoads	X
	U 0117	5859920	January 1999	Daly et al.	X
	U 0118	5860099	January 1999	Milios et al.	X
	U 0119	5862260	January 1999	Rhoads	X
	U 0120	5870474	February 1999	Wasilewski et al.	X
	U 0121	5884033	March 1999	Duvall et al.	X
	U 0122	5889868	March 1999	Moskowitz et al.	X
	U 0123	5893067	April 1999	Bender et al.	X
	U 0124	5894521	April 1999	Conley	X
	U 0125	5903721	May 1999	Sixtus	X
	U 0126	5905800	May 1999	Moskowitz et al.	X
	U 0127	5905975	May 1999	Ausubel	X
	U 0128	5912972	June 1999	Barton	X
	U 0129	5915027	June 1999	Cox et al.	X
	U 0130	5917915	June 1999	Hirose	X
	U 0131	5918223	June 1999	Blum	X
	U 0132	5920900	July 1999	Poole et al.	X
	U 0133	5923763	July 1999	Walker et al.	X
	U 0134	5930369	July 1999	Cox et al.	X
	U 0135	5930377	July 1999	Powell et al	X
	U 0136	5940134	August 1999	Wirtz	X

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	U 0137	5943422	August 1999	Van Wie et al.	X
	U 0138	5963909	October 1999	Warren et al.	X
	U 0139	5973731	October 1999	Schwab	X
	U 0140	5974141	October 1999	Saito	X
	U 0141	5991426	November 1999	Cox et al.	X
	U 0142	5999217	December 1999	Berners-Lee	X
	U 0143	6009176	December 1999	Gennaro et al.	X
	U 0144	6029126	February 2000	Malvar	X
	U 0145	6041316	March 2000	Allen	X
	U 0146	6044471	March 2000	Colvin	X
	U 0147	6049838	April 2000	Miller et al.	X
	U 0148	6051029	April 2000	Paterson et al.	X
	U 0149	6061793	May 2000	Tewfik et al.	X
	U 0150	6069914	May 2000	Cox	X
	U 0151	6078664	June 2000	Moskowitz et al.	X
	U 0152	6081251	June 2000	Sakai et al.	X
	U 0153	6081587	June 2000	Reyes et al.	X
	U 0154	6088455	July 2000	Logan et al.	X
	U 0155	6131162	October 2000	Yoshiura et al.	X
	U 0156	6141753	October 2000	Zhao et al.	X
	U 0157	6141754	October 2000	Choy	X
	U 0158	6154571	November 2000	Cox et al.	X
	U 0159	6192138	February 2001	Yamadaji	X

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	U 0160	6199058	March 2001	Wong et al.	X
	U 0161	6205249	March 2001	Moskowitz	X
	U 0162	6208745	March 2001	Florenio et al.	X
	U 0163	6230268	May 2001	Miwa et al.	X
	U 0164	6233347	May 2001	Chen et al.	X
	U 0165	6233684	May 2001	Stefik et al.	X
	U 0166	6240121	May 2001	Senoh	X
	U 0167	6263313	July 2001	Milstead et al.	X
	U 0168	6272634	August 2001	Tewfik et al.	X
	U 0169	6275988	August 2001	Nagashima et al.	X
	U 0170	6278780	August 2001	Shimada	X
	U 0171	6278791	August 2001	Honsinger et al.	X
	U 0172	6282300	August 2001	Bloom et al.	X
	U 0173	6282650	August 2001	Davis	X
	U 0174	6285775	September 2001	Wu et al.	X
	U 0175	6301663	October 2001	Kato et al.	X
	U 0176	6310962	October 2001	Chung et al.	X
	U 0177	6330335	December 2001	Rhoads	X
	U 0178	6330672	December 2001	Shur	X
	U 0179	6345100	February 2002	Levine	X
	U 0180	6351765	February 2002	Pietropaolo et al.	X
	U 0181	6363483	March 2002	Keshav	X
	U 0182	6373892	April 2002	Ichien et al.	X

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	U 0183	6373960	April 2002	Conover et al.	X
	U 0184	6374036	April 2002	Ryan et al.	X
	U 0185	6377625	April 2002	Kim	X
	U 0186	6381618	April 2002	Jones et al.	X
	U 0187	6381747	April 2002	Wonfor et al.	X
	U 0188	6385329	May 2002	Sharma et al.	X
	U 0189	6389538	May 2002	Gruse et al.	X
	U 0190	6405203	June 2002	Collart	X
	U 0191	6415041	July 2002	Oami et al.	X
	U 0192	6425081	July 2002	Iwamura	X
	U 0193	6430301	August 2002	Petrovic	X
	U 0194	6430302	August 2002	Rhoads	X
	U 0195	6442283	August 2002	Tewfik et al.	X
	U 0196	6446211	September 2002	Colvin	X
	U 0197	6453252	September 2002	Laroche	X
	U 0198	6457058	September 2002	Ullum et al.	X
	U 0199	6463468	October 2002	Buch et al.	X
	U 0200	6484264	November 2002	Colvin	X
	U 0201	6493457	December 2002	Quackenbush	X
	U 0202	6502195	December 2002	Colvin	X
	U 0203	6522767	February 2003	Moskowitz et al.	X
	U 0204	6522769	February 2003	Rhoads et al.	X
	U 0205	6523113	February 2003	Wehrenberg	X

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	U 0206	6530021	March 2003	Epstein et al.	X
	U 0207	6532284	March 2003	Walker et al.	X
	U 0208	6539475	March 2003	Cox et al.	X
	U 0209	6557103	April 2003	Boncelet, Jr. et al.	X
	U 0210	6584125	June 2003	Katto	X
	U 0211	6587837	July 2003	Spagna et al.	X
	U 0212	6598162	July 2003	Moskowitz	X
	U 0213	6606393	August 2003	Xie et al.	X
	U 0214	6647424	November 2003	Pearson et al.	X
	U 0215	6658010	December 2003	Enns et al.	X
	U 0216	6665489	December 2003	Collart	X
	U 0217	6668246	December 2003	Yeung et al.	X
	U 0218	6668325	December 2003	Collberg et al	X
	U 0219	6687683	February 2004	Harada et al.	X
	U 0220	6725372	April 2004	Lewis et al	X
	U 0221	6754822	June 2004	Zhao	X
	U 0222	6775772	August 2004	Binding et al.	X
	U 0223	6784354	August 2004	Lu et al.	X
	U 0224	6785815	August 2004	Serret-Avila et al.	X
	U 0225	6785825	August 2004	Colvin	X
	U 0226	6792548	September 2004	Colvin	X
	U 0227	6792549	September 2004	Colvin	X
	U 0228	6795925	September 2004	Colvin	X

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EXAMINER INITIALS	REFERENCE NUMBER (U SERIES)	PATENT NUMBER	ISSUE DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	U 0229	6799277	September 2004	Colvin	X
	U 0230	6813717	November 2004	Colvin	X
	U 0231	6813718	November 2004	Colvin	X
	U 0232	6823455	November 2004	Macy et al.	X
	U 0233	6834308	December 2004	Ikezoye et al.	X
	U 0234	6842862	January 2005	Chow et al.	X
	U 0235	6853726	February 2005	Moskowitz et al.	X
	U 0236	6857078	February 2005	Colvin	X
	U 0237	6931534	August 2005	Jandel et al.	X
	U 0238	6966002	November 2005	Torrubia-Saez	X
	U 0239	6983337	November 2005	Wold	X
	U 0240	6977894	December 2005	Achilles et al.	X
	U 0241	6978370	December 2005	Kocher	X
	U 0242	6986063	January 2006	Colvin	X
	U 0243	7007166	February 2006	Moskowitz et al.	X
	U 0244	7020285	March 2006	Kirovski et al.	X
	U 0245	7035409	April 2006	Moskowitz	X
	U 0246	7043050	May 2006	Yuval	X
	U 0247	7046808	May 2006	Metois et al.	X
	U 0248	7050396	May 2006	Cohen et al.	X
	U 0249	7051208	May 2006	Venkatesan et al.	X
	U 0250	7058570	June 2006	Yu et al.	X
	U 0251	7093295	August 2006	Saito	X

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	U 0252	7095874	August 2006	Moskowitz et al	X
	U 0253	7103184	September 2006	Jian	X
	U 0254	7107451	September 2006	Moskowitz	X
	U 0255	7123718	October 2006	Moskowitz et al.	X
	U 0256	7127615	October 2006	Moskowitz	X
	U 0257	7150003	December 2006	Naumovich et al.	X
	U 0258	7152162	December 2006	Moskowitz et al.	X
	U 0259	7159116	January 2007	Moskowitz	X
	U 0260	7162642	January 2007	Schumann et al.	X
	U 0261	7177429	February 2007	Moskowitz et al.	X
	U 0262	7177430	February 2007	Kim	X
	U 0263	7206649	April 2007	Kirovski et al.	X
	U 0264	7231524	June 2007	Bums	X
	U 0265	7233669.	June 2007	Candelore	X
	U 0266	7240210	July 2007	Michak et al.	X
	U 0267	7266697	September 2007	Kirovski et al	X
	U 0268	7287275	October 2007	Moskowitz	X
	U 0269	7289643	October 2007	Brunk et al.	X
	U 0270	7343492	March 2008	Moskowitz et al.	X
	U 0271	7346472	March 2008	Moskowitz et al.	X
	U 0272	7362775	April 2008	Moskowitz	X
	U 0273	7363278	April 2008	Schmelzer et al.	X
	U 0274	7409073	August 2008	Moskowitz et al.	X

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	U 0275	7457962	November 2008	Moskowitz	X
	U 0276	7460994	December 2008	Herre et al.	X
	U 0277	7475246	January 2009	Moskowitz	X
	U 0278	7530102	May 2009	Moskowitz	X
	U 0279	7532725	May 2009	Moskowitz et al.	X
	U 0280	7568100	July 2009	Moskowitz et al.	X
	U 0281	7647502	January 2010	Moskowitz	X
	U 0282	7647503	January 2010	Moskowitz	X
	U 0283	7779261	August 2010	Moskowitz	X
	U 0284	6990453	January 2006	Wang	X
	U 0285	6081597	June 2000	Hoffstein	X
	U 0286	7035049	Apr 2006	Yamamoto	X
	U 0287	7664263	Feb 2010	Moskowitz	X
	U 0288	7286451	Oct 2007	Wirtz	X
	U 0289	6385324	May 2002	Koppen	X
	U 0290	6674858	Jan 2004	Kimura	X
	U 0291	6148333	Nov 2000	Guedalia	X
	U 0292	6418421	Jun 2002	Hurtado	X
	U 0293	6385596	May 2002	Wiser	X
	U 0294	6226618	May 2001	Downs	X
	U 0295	6957330	Oct 2005	Hughes	X
	U 0296	5842213	Nov 1998	Odom	X
	U 0297	5818818	Oct 1998	Soumiya	X

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	U 0298	6590996	Jun 2003	Reed	X
	U 0299	5949055	Sept 1999	Fleet	X
	U 0300	6067622	May 2000	Moore	X
	U 0301	7761712	Jun 2010	Moskowitz	X
	U 0302	7743001	Jun 2010	Vermeulen	X
	U 0303	6865747	Mar 2005	Mercier	
	U 0304	6611599	Aug 2003	Natarajan	
	U 0305	6480937	Nov 2002	Vorbach	
	U 0306	6398245	Jun 2002	Gruse	
	U 0307	6950941	Sept 2005	Lee	
	U 0308	6983058	Jan 2006	Fukuoka	
	U 0309	5675653	Oct 1997	Nelson	
	U 0310	6804453	Oct 2004	Sasamoto	
	U 0311	6178405	Jan 2001	Ouyang	
	U 0312	5839100	Nov 1998	Wegener	
	U 0313	5781184	Jul 1998	Wasserman	
	U 0314	5617506	Apr 1997	Burk	
	U 0315	5327520	Jul 1994	Chen	
	U 0316	5111530	May 1992	Kutaragi	
	U 0317	7095715	Aug 2006	Buckman	
	U 0318	6173322	Jan 2001	Hu	
	U 0319	5754938	May 1998	Herz	

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LISTING OF UNITED STATES PUBLISHED APPLICATIONS - P Series

EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	P 01	20010010078	July 2001	Moskowitz	X
	P 02	20010043594	November 2001	Ogawa et al.	X
	P 03	20020010684	January 2002	Moskowitz	X
	P 04	20020026343	February 2002	Duenke	X
	P 05	20020056041	May 2002	Moskowitz	X
	P 06	20020071556	June 2002	Moskowitz et al.	X
	P 07	20020073043	June 2002	Herman et al.	X
	P 08	20020097873	July 2002	Petrovic	X
	P 09	20020103883	August 2002	Haverstock et al.	X
	P 010	20020161741	October 2002	Wang et al.	X
	P 011	20030126445	July 2003	Wehrenberg	X
	P 012	20030133702	July 2003	Collart	X
	P 013	20030200439	October 2003	Moskowitz	X
	P 014	20030219143	November 2003	Moskowitz et al.	X
	P 015	20040028222	February 2004	Sewell et al.	X
	P 016	20040037449	February 2004	Davis et al.	X
	P 017	20040049695	March 2004	Choi et al.	X
	P 018	20040059918	March 2004	Xu	X
	P 019	20040083369	April 2004	Erlingsson et al.	X
	P 020	20040086119	May 2004	Moskowitz	X
	P 021	20040093521	May 2004	Hamadeh et al.	X
	P 022	20040117628	June 2004	Colvin	X

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EXAMINER INITIALS	REFERENCE NUMBER (P SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL AND/OR IDENTIFICATION OF PRIORITY APPLICATION IN
	P 023	20040117664	June 2004	Colvin	X
	P 024	20040125983	July 2004	Reed et al.	X
	P 025	20040128514.	July 2004	Rhoads	X
	P 026	20040225894	November 2004	Colvin	X
	P 027	20040243540	December 2004	Moskowitz et al.	X
	P 028	20050135615	June 2005	Moskowitz et al.	X
	P 029	20050160271	July 2005	Brundage et al.	X
	P 030	20050177727	August 2005	Moskowitz et al.	X
	P 031	20050246554	November 2005	Batson	X
	P 032	20060005029	January 2006	Petrovic et al.	X
	P 033	20060013395	January 2006	Brundage et al.	X
	P 034	20060013451	January 2006	Haitsma	X
	P 035	20060041753	February 2006	Haitsma	X
	P 036	20060101269	May 2006	Moskowitz et al.	X
	P 037	20060140403	June 2006	Moskowitz	X
	P 038	20060285722	December 2006	Moskowitz et al.	X
	P 039	20070011458	January 2007	Moskowitz	X
	P 040	20070028113	February 2007	Moskowitz	X
	P 041	20070064940	March 2007	Moskowitz et al.	X
	P 042	20070079131.	April 2007	Moskowitz et al.	X
	P 043	20070083467	April 2007	Lindahl et al.	X
	P 044	20070110240	May 2007	Moskowitz et al.	X
	P 045	20070113094	May 2007	Moskowitz et al.	X

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	P 046	20070127717	June 2007	Herre et al.	X
	P 047	20070226506	September 2007	Moskowitz	X
	P 048	20070253594	November 2007	Lu et al.	X
	P 049	20070294536.	December 2007	Moskowitz et al.	X
	P 050	20070300072	December 2007	Moskowitz	X
	P 051	20070300073	December 2007	Moskowitz	X
	P 052	20080005571	January 2008	Moskowitz	X
	P 053	20080005572	January 2008	Moskowitz	X
	P 054	20080016365	January 2008	Moskowitz	X
	P 055	20080022113	January 2008	Moskowitz	X
	P 056	20080022114	January 2008	Moskowitz	X
	P 057	20080028222	January 2008	Moskowitz	X
	P 058	20080046742	February 2008	Moskowitz	X
	P 059	20080075277	March 2008	Moskowitz et al.	X
	P 060	20080109417	May 2008	Moskowitz	X
	P 061	20080133927	June 2008	Moskowitz et al.	X
	P 062	20080151934	June 2008	Moskowitz et al.	X
	P 063	20090037740	February 2009	Moskowitz	X
	P 064	20090089427	April 2009	Moskowitz et al.	X
	P 065	20090190754	July 2009	Moskowitz et al.	X
	P 066	20090210711	August 2009	Moskowitz	X
	P 067	20090220074	September 2009	Moskowitz et al.	X
	P 068	20100002904	January 2010	Moskowitz	X

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	P 069	20100005308	January 2010	Moskowit	X
	P 070	20100098251	Apr 2010	Moskowitz	X
	P 071	20100220861	Sept 2010	Moskowitz	X
	P 072	20100202607	Aug 2010	Moskowitz	X
	P 073	20020047873	June 2002	Petrovic	X
	P 074	20020009208	Jan 2002	Alattar	X
	P 075	20010029580	October 2001	Moskowitz	X
	P 076	20100182570	July 2010	Chota	X
	P 077	20100077220	March 2010	Moskowitz	X
	P 078	20100077219	March 2010	Moskowitz	X
	P 079	20100064140	March 2010	Moskowitz	X
	P 080	20100153734	June 2010	Moskowitz	X
	P 081	20100106736	April 2010	Moskowitz	X
	P 082	20060251291	November 2006	Rhoads	X
	P 083	20030002862	January 2003	Rodriguez	
	P 084	2003005780	May 2003	Hansen	
	P 085	20020152179	Oct 2002	Racov	
	P 086	20030027549	Feb 2003	Kiel	
	P 087	20020057651	May 2002	Roberts	

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LISTING OF FOREIGN AND INTERNATIONAL PATENT DOCUMENTS - F Series

EXAMINER INITIALS	REFERENCE NUMBER (F SERIES)	PUBLICATION NUMBER	PUBLICATION DATE	COUNTRY OR REGION	PAGE/LINE AND FIGURE/ELEMENT OF RELEVANT MATERIAL	ENGLISH LANGUAGE TRANSLATION AND/OR IDENTIFICATION OF PRIORXITY APPLICATION IN
	F 01-	EP0372601	Jun., 1990	EP		X
	F 02-	EP0565947	Oct., 1993	EP		X
	F 03-	EP0581317	Feb., 1994	EP		X
	F 04-	EP0649261	Apr., 1995	EP		X
	F 05-	EP0651554	May., 1995	EP		X
	F 06-	EP1354276	Dec., 2007	EP		X
	F 07-	NL 1005523	Sep., 1998	NL		X
	F 08-	WO 9514289	May., 1995	WO		X
	F 09-	WO 9629795	Sep., 1996	WO		X
	F 010-	WO 9724833	Jul., 1997	WO		X
	F 011-	WO 9744736	Nov., 1997	WO		X
	F 012-	WO9837513	Aug., 1998	WO		X
	F 013-	WO 9952271	Oct., 1999	WO		X
	F 014-	WO 9962044	Dec., 1999	WO		X
	F 015-	WO 9963443	Dec., 1999	WO		X
	F 016-	WO9726733	Jan. 1997	WO		X
	F 017-	WO98002864	Jul. 1997	WO		X
	F 018-	WO 0057643	Sept 2000	WO		X
	F 019-	WO 9642151	Dec 1996	WO		X
	F 020-	EP0872073	July 1996	EP		X
	F 021-	WO0118628	March 2001	WO		X
	F 022-	WO0143026	June 2001	WO		X

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	F 023-	WO0203385	Jan 2002	WO		X
	F 024-	WO9701892	June 1995	WO		X
	F 025-	WO9726732	July 1997	WO		X
	F 026-	WO9802864	Jan 1998	WO		X
	F 027-	EP1547337	Mar 2006	EP		X
	F 028-	EP0581317A2	Feb 1994	EP		X
	F 029-	WO023385A1	Oct 2002	WO		X

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LISTING OF NON PATENT LITERATURE - L Series

EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a publication	ENGLISH LANGUAGE TRANSLATION AND/OR OR
	1	L- 01	N/A	US. Appl. No. 08/999,766, filed Jul. 23, 1997, entitled "Steganographic Method and Device", published as 7568100 07-28-2009, cited as U280.	X
	2	L- 02	N/A	EPO Application No. 96919405.9, entitled "Steganographic Method and Device"; published as EP0872073 (A2), 10-21-1998, cited herein as F20.	X
	3	L- 03	N/A	U.S. Appl. No. 11/050,779, filed Feb. 7, 2005, entitled "Steganographic Method and Device", published as 20050177727 A1 08-11-2005, cited herein as P30.	X
	4	L- 04	N/A	U.S. Appl. No. 08/674,726, filed Jul. 2, 1996, entitled "Exchange Mechanisms for Digital Information Packages with Bandwidth Securitization, Multichannel Digital Watermarks, and Key Management", published as 7362775 04-22-2008, cited herein as U272 .	X
	5	L- 05	N/A	U.S. Appl. No. 09/545,589, filed Apr. 7, 2000, entitled "Method and System for Digital Watermarking", published as 7007166 02-28-2006, cited herein as U243	X
	6	L- 06	N/A	U.S. Appl. No. 11/244,213, filed Oct. 5, 2005, entitled "Method and System for Digital Watermarking", published as 2006-0101269 A1 05-11-2006, cited herein as P36	X
	7	L- 07	N/A	U.S. Appl. No. 11/649,026, filed Jan. 3, 2007, entitled "Method and System for Digital Watermarking", published as 2007-0113094 A1 05-17-2007, cited herein as P45.	X
	8	L- 08	N/A	U.S. Appl. No. 09/046,627, filed Mar. 24, 1998, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 6,598,162 07-22-2003, cited herein as U212.	X
	9	L- 09	N/A	U.S. Appl. No. 10/602,777, filed Jun. 25, 2003, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 2004-0086119 A1 05-06-2004, cited herein P20.	X

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	10	L- 010	N/A	U.S. Appl. No. 09/053,628, filed Apr. 2, 1998, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", 6,205,249 03-20-2001, cited herein as U161.	X
	11	L- 011	N/A	U.S. Appl. No. 09/644,098, filed Aug. 23, 2000, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 7,035,409 04-25-2006, cited herein as U245.	X
	12	L- 012	N/A	Jap. App. No. 2000-542907, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking"; which is a JP national stage of PCT/US1999/007262, published as WO/1999/052271, 10/14/1999, F13 here in above..	X
	13	L- 013	N/A	U.S. Appl. No. 09/767,733, filed Jan. 24, 2001 entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 2001-0010078 A1 07-26-2001, cited herein as P1.	X
	14	L- 014	N/A	U.S. Appl. No. 11/358,874, filed Feb. 21, 2006, entitled "Multiple Transform Utilization and Application for Secure Digital Watermarking", published as 2006-0140403 A1 06-29-2006, cited herein as P37.	X
	15	L- 015	N/A	U.S. Appl. No. 10/417,231, filed Apr. 17, 2003, entitled "Methods, Systems And Devices For Packet Watermarking And Efficient Provisioning Of Bandwidth", published as 2003-0200439 A1 10-23-2003, cited herein as P13,	X
	16	L- 016	N/A	U.S. Appl. No. 09/789,711, filed Feb. 22, 2001, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2001-0029580 A1 10-11-2001, cited herein as P75.	X
	17	L- 017	N/A	U.S. Appl. No. 11/497,822, filed Aug. 2, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2007-0011458 A1 01-11-2007, cited herein as P39.	X
	18	L- 018	N/A	U.S. Appl. No. 11/599,964, filed Nov. 15, 2006, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 2008-0046742 A1 02-21-2008, cited herein as P58.	X

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	22	L- 022	N/A	U.S. Appl. No. 09/594,719, filed Jun. 16, 2000, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 7,123,718 10-17-2006, cited herein as U255.	X
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	27	L- 027	N/A	PCT Application No. PCT/US00/21189, filed Aug. 4, 2000, entitled, "A Secure Personal Content Server", Pub. No.: WO/2001/018628 ; Publication Date: 15.03.2001, cited herein as F21.	X

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	28	L- 028	N/A	U.S. Appl. No. 09/657,181, filed Sep. 7, 2000, entitled "Method and Device For Monitoring And Analyzing Signals", published as 7,346,472 03-18-2008, cited herein as U271.	X
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	34	L- 034	N/A	U.S. Appl. No. 09/731,039, filed Dec. 7, 2000, entitled "System and Methods for Permitting Open Access to Data Objects and for Securing Data within the Data Objects", published as 2002-0071556 A1 06-13-2002, cited herein as P06.	X
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	39	L- 039	1984	Brealy, et al., Principles of Corporate Finance, "Appendix A--Using Option Valuation Models", 1984, pp. 448-449.	X
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37 CFR 1.98(a)(1)(i) APPLICATION & ATTORNEY DOCKET: 13/556,420 / SCOT0014-6
37 CFR 1.98(a)(1)(iii): THIS IS AN INFORMATION DISCLOSURE STATEMENT

EXAMINER INITIAL	REF. NO. (L series)	REFERENCE NUMBER (L	PUB. DATE	INCLUDE IN SEQUENCE: Name of first author (in CAPITAL LETTERS), Title in quotation marks, name of publication, date or publication, page numbers, publisher, city of publication, and country of publication NOTE - For US patent applications listed herein, if a publication	ENGLISH LANGUAGE TRANSLATION AND/OR OR
	117	L- 0117		DUPLICATE OF L-68, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	118	L- 0118		DUPLICATE OF L-69, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	119	L- 0119		DUPLICATE OF L-70, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	120	L- 0120		DUPLICATE OF L-71, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	121	L- 0121		DUPLICATE OF L-72, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	122	L- 0122		DUPLICATE OF L-73, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	123	L- 0123		DUPLICATE OF L-74, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	124	L- 0124		DUPLICATE OF L-75, DELETED BY RN UPON REVIEW ON 11/16/2010. RAN	X
	125	L- 0125		DUPLICATE OF L-076, REMOVED. RN. 11/16/2010	X
	126	L- 0126		DUPLICATE OF L-77, REMOVED. RN. 11/16/2010	X
	127	L- 0127		DUPLICATE OF L-78, REMOVED. RN. 11/16/2010	X
	128	L- 0128		DUPLICATE OF L-79, REMOVED. RN. 11/16/2010	X
	129	L- 0129		EP0581317A2, MOVED TO FOREIGN PATENT PUBS as F-028	X
	130	L- 0130		DUPLICATE OF L-52, REMOVED. RN. 11/16/2010	X

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	131	L- 0131		DUPLICATE OF L-36, REMOVED. RN. 11/16/2010	X
	132	L- 0132		DUPLICATE OF L-38, REMOVED. RN. 11/16/2010.	X
	133	L- 0133		DUPLICATE OF L-37, REMOVED. RN. 11/16/2010	X
	134	L- 0134		DUPLICATE OF L-36, REMOVED. RN. 11/16/2010	X
	135	L- 0135		DUPLICATE OF L-37, REMOVED. RN. 11/16/2010	X
	136	L- 0136		DUPLICATE OF L-38, REMOVED. RN. 11/16/2010	X
	137	L- 0137		DUPLICATE OF L-39, REMOVED. RN. 11/16/2010	X
	138	L- 0138		DUPLICATE OF L-40, REMOVED. RN. 11/16/2010	X
	139	L- 0139		DUPLICATE OF L-41, REMOVED. RN. 11/16/2010	X
	140	L- 0140		DUPLICATE OF L-42, REMOVED. RN. 11/16/2010	X
	141	L- 0141		DUPLICATE OF L-43, REMOVED. RN. 11/16/2010	X
	142	L- 0142		DUPLICATE OF L-44, REMOVED. RN. 11/16/2010	X
	143	L- 0143		DUPLICATE OF L-45, REMOVED. RN. 11/16/2010.	X
	144	L- 0144		DUPLICATE OF L-46, REMOVED. RN. 11/16/2010.	X

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	145	L- 0145		DUPLICATE OF L-47, REMOVED. RN. 11/16/2010	X
	146	L- 0146		DUPLICATE OF L-48, REMOVED. RN. 11/16/2010	X
	147	L- 0147		DUPLICATE OF L-49, REMOVED. RN. 11/16/2010	X
	148	L- 0148		DUPLICATE OF L-50, REMOVED. RN. 11/16/2010	X
	149	L- 0149		DUPLICATE OF L-51, REMOVED. RN. 11/16/2010	X
	150	L- 0150		DUPLICATE OF L-52, REMOVED. RN. 11/16/2010	X
	151	L- 0151		DUPLICATE OF L-63, REMOVED. RN. 11/16/2010	X
	152	L- 0152		DUPLICATE OF L-54, REMOVED. RN. 11/16/2010	X
	153	L- 0153		DUPLICATE OF L-55, REMOVED. RN. 11/16/2010.	X
	154	L- 0154		DUPLICATE OF L-80, REMOVED. RN. 11/16/2010.	X
	155	L- 0155	N/A	PCT International Search Report in PCT/US95/08159.	X
	156	L- 0156	N/A	PCT International Search Report in PCT/US96/10257.	X
	157	L- 0157	N/A	Supplementary European Search Report in EP 96919405.	X
	158	L- 0158	N/A	PCT International Search Report in PCT/US97/00651.	X
	159	L- 0159	N/A	PCT International Search Report in PCT/US97/00652	X

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	160	L- 0160	N/A	PCT International Search Report in PCT/US97/11455.	X
	161	L- 0161		PCT International Search Report in PCT/US99/07262.	X
	162	L- 0162		PCT International Search Report in PCT/US00/06522	X
	163	L- 0163		Supplementary European Search Report in EP00919398	X
	164	L- 0164		PCT International Search Report in PCT/US00/18411.	X
	165	L- 0165		PCT International Search Report in PCT/US00/18411.	X
	166	L- 0166		PCT International Search Report in PCT/US00/33126	X
	167	L- 0167		PCT International Search Report in PCT/US00/21189	X
	168	L- 0168		Delaigle, J.-F., et al. "Digital Watermarking," Proceedings of the SPIE, vol. 2659, Feb 1, 1996, pp. 99-110.	X
	169	L- 0169	1996	Schneider, M., et al. "A Robust Content Based Digital Signature for Image Authentication," Proceedings of the International Conference on Image Processing (IC. Lausanne) Sep. 16-19, 1996, pp. 227-230, IEEE ISBN.	X
	170	L- 0170	1997	Cox, I. J., et al. "Secure Spread Spectrum Watermarking for Multimedia," IEEE Transactions on Image Processing, vol. 6 No. 12, Dec. 1, 1997, pp. 1673-1686.	X
	171	L- 0171	1998	Wong, Ping Wah. "A Public Key Watermark for Image Verification and Authentication," IEEE International Conference on Image Processing, vol. 1 Oct. 4-7, 1998, pp. 455-459.	X
	172	L- 0172	1998	Fabien A.P. Petitcolas, Ross J. Anderson and Markkus G. Kuhn, "Attacks on Copyright Marking Systems," LNCS, vol. 1525, Apr. 14-17, 1998, pp. 218-238 ISBN: 3-540-65386-4.	X

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	173	L- 0173	1996	Ross Anderson, "Stretching the Limits of Steganography," LNCS, vol. 1174, May/Jun. 1996, 10 pages, ISBN: 3-540-61996-8.	X
	174	L- 0174	1997	Joseph J.K. O'Ruanaidh and Thierry Pun, "Rotation, Scale and Translation Invariant Digital Image Watermarking", pre-publication, Summer 1997 4 pages.	X
	175	L- 0175	1997	Joseph J.K. O'Ruanaidh and Thierry Pun, "Rotation, Scale and Translation Invariant Digital Image Watermarking", Submitted to Signal Processing Aug. 21, 1997, 19 pages.	X
	176	L- 0176	2008	OASIS (Dig Out Your Soul), Big Brother Recordings Ltd, Promotional CD image, 2008, 1 page.	X
	177	L- 0177	1998	Rivest, R. "Chaffing and Winnowing: Confidentiality without Encryption", MIT Lab for Computer Science, http://people.csail.mit.edu/rivest/Chaffing.txt Apr. 24, 1998, 9 pp.	X
	178	L- 0178	2003	PortalPlayer, PP5002 digital media management system-on-chip, May 1, 2003, 4 pp.	X
	179	L- 0179	2001	VeriDisc, "The Search for a Rational Solution to Digital Rights Management (DRM)", http://64.244.235.240/news/whitepaper, /docs/veridisc.sub.--white.sub.--paper.pdf, 2001, 15 pp.	X
	180	L- 0180	2008	Cayre, et al., "Kerckhoff's-Based Embedding Security Classes for WOA Data Hiding", IEEE Transactions on Information Forensics and Security, vol. 3 No. 1, Mar. 2008, 15 pp.	X
	181	L- 0181	1999	Wayback Machine, dated Jan. 17, 1999, http://web.archive.org/web/19990117020420/http://www.netzero.com/, accessed on Feb. 19, 2008.	X
	182	L- 0182	1997	Namgoong, H., "An Integrated Approach to Legacy Data for Multimedia Applications", Proceedings of the 23rd EUROMICRO Conference, vol., Issue 1-4, Sep. 1997, pp. 387-391.	X
	183	L- 0183	2007	Wayback Machine, dated Aug. 26, 2007, http://web.archive,org/web/20070826151732/http://www.screenplaysmag.com/t-abid/96/articleType/ArticleView/articleId/495/Default.aspx/.	X

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	184	L- 0184	2009	"YouTube Copyright Policy: Video Identification tool--YouTube Help", accessed Jun. 4, 2009, http://www.google.com/support/youtube/bin/answer.py?h1=en&answer=83766, 3 pp.	X
	185	L- 0185	N/A	U.S. Appl. No. 12/665,002, filed Dec. 22, 2009, entitled "Method for Combining Transfer Function with Predetermined Key Creation", published as 20100182570 A1 07-22-2010, P76.	X
	186	L- 0186	N/A	U.S. Appl. No. 12/592,331, filed Nov. 23, 2009, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 20100077220 A1 03-25-2010, P77.	X
	187	L- 0187	N/A	U.S. Appl. No. 12/590,553, filed Nov. 10, 2009, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 20100077219 A1 03-25-2010, P78.	X
	188	L- 0188	N/A	U.S. Appl. No. 12/590,681, filed Nov. 12, 2009, entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digital Data", published as 20100064140 A1 03-11-2010, P79.	X
	189	L- 0189	N/A	U.S. Appl. No. 12/655,036, filed Dec. 22, 2009, entitled "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as 20100153734 A1 06-17-2010, P80 .	X
	190	L- 0190	N/A	U.S. Appl. No. 12/655,357, filed Dec. 22, 2009, entitled "Method And Device For Monitoring And Analyzing Signals", published as 20100106736 A1 04-29-2010, P81.	X
	191	L- 0191	N/A	PCT Application No. PCT/US95/08159, filed Jun. 26, 1995, entitled, "Digital Information Commodities Exchange with Virtual Menuing", published as WO/1997/001892; Publication Date: 16.01.1997, F24.	X
	192	L- 0192	N/A	PCT Application No. PCT/US96/10257, filed Jun. 7, 1996, entitled "Steganographic Method and Device"--corresponding to--EPO Application No. 96919405.9, entitled "Steganographic Method and Device", published as WO/1996/042151; Publication Date: 27.12.1996; F19.	X

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	193	L- 0193	N/A	PCT Application No. PCT/US97/00651, filed Jan. 16, 1997, entitled, "Method for Stega-Cipher Protection of Computer Code", published as WO/1997/026732; Publication Date: 24.07.1997.	X
	194	L- 0194	N/A	PCT Application No. PCT/US97/00652, filed Jan. 17, 1997, entitled, "Method for an Encrypted Digital Watermark", published as WO/1997/026733; Publication Date: 24.07.1997	X
	195	L- 0195	N/A	PCT Application No. PCT/US97/11455, filed Jul. 2, 1997, entitled, "Optimization Methods for the Insertion, Protection and Detection of Digital Watermarks in Digitized Data", published as WO/1998/002864; Publication Date: 22.01.1998	X
	196	L- 0196	N/A	PCT Application No. PCT/US99/07262, filed Apr. 2, 1999, entitled, "Multiple Transform Utilization and Applications for Secure Digital Watermarking", published as WO/1999/052271; Publication Date: 14.10.1999.	X
	197	L- 0197	N/A	PCT Application No. PCT/US00/06522, filed Mar. 14, 2000, entitled, "Utilizing Data Reduction in Steganographic and Cryptographic Systems", published as WO/2000/057643; Publication Date: 28.09.2000.	X
	198	L- 0198	N/A	PCT Application No. PCT/US00/18411, filed Jul. 5, 2000, entitled, "Copy Protection of Digital Data Combining Steganographic and Cryptographic Techniques"	X
	199	L- 0199	N/A	PCT Application No. PCT/US00/33126, filed Dec. 7, 2000, entitled "Systems, Methods and Devices for Trusted Transactions", published as WO/2001/043026; Publication Date: 14.06.2001.	X
	200	L- 0200	N/A	EPO Divisional Patent Application No. 07112420.0, entitled "Steganographic Method and Device" corresponding to PCT Application No. PCT/US96/10257, published as WO/1996/042151, 12/27/1996, cited herein above as F019.	X
	201	L- 0201	N/A	US Provisional Application 60/222,023 filed July 31, 2007 entitled “Method and apparatus for recognizing sound and signals in high noise and distortion”	X

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	202	L- 0202	N/A	US Application 11/458,639 filed July 19, 2006 entitled "Methods and Systems for Inserting Watermarks in Digital Signals", published as 20060251291 A1 11-09-2006, P82.	X
	203	L- 0203	1995	"Techniques for Data Hiding in Audio Files," by Morimoto, 1995	X
	204	L- 0204	1998	Howe, Dennis July 13, 1998 http://foldoc..org//steganography	X
	205	L- 0205	N/A	CSG, Computer Support Group and CSGNetwork.com 1973 http://www.csgnetwork.com/glossarys.html	X
	206	L- 0206	2010	QuinStreet Inc. 2010 What is steganography?-A word definition from the Webopedia Computer Dictionary http://www.webopedia.com/terms/steganography.html	X
	207	L- 0207	2000	Graham, Robert August 21, 2000 "Hacking Lexicon" http://robertgraham.com/pubs/hacking-dict.html	X
	208	L- 0208	2010	Farkex, Inc 2010 "Steganography definition of steganography in the Free Online Encyclopedia" http://encyclopedia2.Thefreedictionary.com/steganography	X
	209	L- 0209	1989	Horowitz, et al., The Art of Eletronics. 2nd Ed., 1989, pp7	X
	210	L- 0210	2004	Jimmy eat world ("futures"), Interscope Records, Pre-Release CD image, 2004, 1 page.	X
	211	L- 0211	2001	Aerosmith ("Just Push Play"), Pre-Release CD image, 2001, 1 page.	X
	212	L- 0212	2002	Phil Collins(Testify) Atlantic, Pre-Release CD image, 2002, 1 page.	X
	213	L- 0213	2012	2012 "Address Space Layout Randomization." Wikipedia, The Free Encyclopedia. Wikimedia Foundation, Inc.. 28, August 2012 http://en.wikipedia.org/wiki/ASLR	

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Electronic Acknowledgement Receipt

EFS ID:	13903110
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Richard A. Neifeld/Jamaal Evans
Filer Authorized By:	Richard A. Neifeld
Attorney Docket Number:	SCOT0014-6
Receipt Date:	04-OCT-2012
Filing Date:	24-JUL-2012
Time Stamp:	12:27:03
Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	no

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New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13/556,420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: 2819
ENTITY STATUS: LARGE
Priority claims and PCT Intl data:
This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162, which is a continuation-in-part of U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943 are hereby incorporated by reference in their entireties

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.

2. FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $: 0.00

3. THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:
37 CFR 1.97 Information Disclosure Statement
37 CFR 1.98 Reference Citation List citing references U001-U319, P001-P087, F01-F029, L01-L213.
Copy of NPL Reference L213.

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Service Fees: Amount/CreditAtty/entry date/Services: 600.00/RAN/10-3-2012/IDS Filing

INITIALS OF PERSON WHO *ENTERED* ACCOUNTING DATA: JRE
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)

DATE: 10-4-2012 SIGNATURE: /RichardNeifeld/

PRINTED NAME: RICHARD NEIFELD, REG. NO. 35,299

Printed: October 4, 2012 (11:12am)

Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\IDS_SCOT0014-6_10-3-2012.wpd

PATENT APPLICATION FEE DETERMINATION RECORD

Substitute for Form PTO-875

Application or Docket Number: 13/556,420

APPLICATION AS FILED - PART I

FOR	(Column 1) NUMBER FILED	(Column 2) NUMBER EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY FEE($)
BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A			N/A	380
SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A			N/A	620
EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A			N/A	250
TOTAL CLAIMS (37 CFR 1.16(i))	1 minus 20 =	*			OR	x 60 =	0.00
INDEPENDENT CLAIMS (37 CFR 1.16(h))	1 minus 3 =	*				x 250 =	0.00
APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						0.00
MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))							0.00
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	1250

APPLICATION AS AMENDED - PART II

		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY ADDITIONAL FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY ADDITIONAL FEE($)
AMENDMENT A	Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =	
	Application Size Fee (37 CFR 1.16(s))									
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE($)	ADDITIONAL FEE($)	OR	RATE($)	ADDITIONAL FEE($)
AMENDMENT B	Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =	
	Application Size Fee (37 CFR 1.16(s))									
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".

*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".

The "Highest Number Previously Paid For" (Total or Independent) is the highest found in the appropriate box in column 1.

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
13/556,420	07/24/2012	2819	1250	SCOT0014-6	1	1

CONFIRMATION NO. 5811

UPDATED FILING RECEIPT

31518
NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

OC000000056777043

Date Mailed: 10/01/2012

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

Inventor(s)

Scott A. Moskowitz, Sunny Isles Beach, FL;

Applicant(s)

Scott A. Moskowitz, Sunny Isles Beach, FL;

Power of Attorney: The patent practitioners associated with Customer Number 31518

Domestic Priority data as claimed by applicant

This application is a CON of 11/895,388 08/24/2007
which is a DIV of 10/602,777 06/25/2003 PAT 7664263
which is a CON of 09/046,627 03/24/1998 PAT 6598162

Foreign Applications (You may be eligible to benefit from the **Patent Prosecution Highway** program at the USPTO. Please see http://www.uspto.gov for more information.)

If Required, Foreign Filing License Granted: 08/03/2012

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 13/556,420**

Projected Publication Date: 01/10/2013

Non-Publication Request: No

Early Publication Request: No

Title

Data protection method and device

Preliminary Class

341

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

Applicants also are advised that in the case of inventions made in the United States, the Director of the USPTO must issue a license before applicants can apply for a patent in a foreign country. The filing of a U.S. patent application serves as a request for a foreign filing license. The application's filing receipt contains further information and guidance as to the status of applicant's license for foreign filing.

Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at http://www.uspto.gov/web/offices/pac/doc/general/index.html.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, http://www.stopfakes.gov. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4158).

LICENSE FOR FOREIGN FILING UNDER

Title 35, United States Code, Section 184

Title 37, Code of Federal Regulations, 5.11 & 5.15

GRANTED

The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as

set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Bureau of Industry and Security, Department of Commerce (15 CFR parts 730-774); the Office of Foreign AssetsControl, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

NOT GRANTED

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).

NEIFELD REF: SCOT0014-6

CLIENT REF: SCOT0014-6

Application/Patent No: 13556420

USPTO CONF. NO: 5811

File/Issue Date: 7/24/2012

Inventor: SCOTT MOSKOWITZ

Title: Data protection method and device

Examiner/ArtUnit: Unknown

ENTITY STATUS: LARGE

Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

RESPONSE TO NOTICE OF INCOMPLETE REPLY

In response to the notice of incomplete reply mailed September 17, 2012, please replace the figures filed July 24, 2012 with the 1 page of replacement sheets that is attached herewith.

The attached replacement sheet comprises a clearer version of the figure filed July 24, 2012, specifically, the figure is reasonably free from erasures, alterations, overwriting, interlineations, folds, and copy marks. No new matter has been added to the figure.

Respectfully Submitted,

/BruceMargulies/

DATE	Bruce T. Margulies
9/24/2012	Registration No. 64,175
	Attorney of Record

Date/Time: September 24, 2012 (9:57am)

Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ResponseToNotice_SCOT0014-6_9-24-2012.wpd

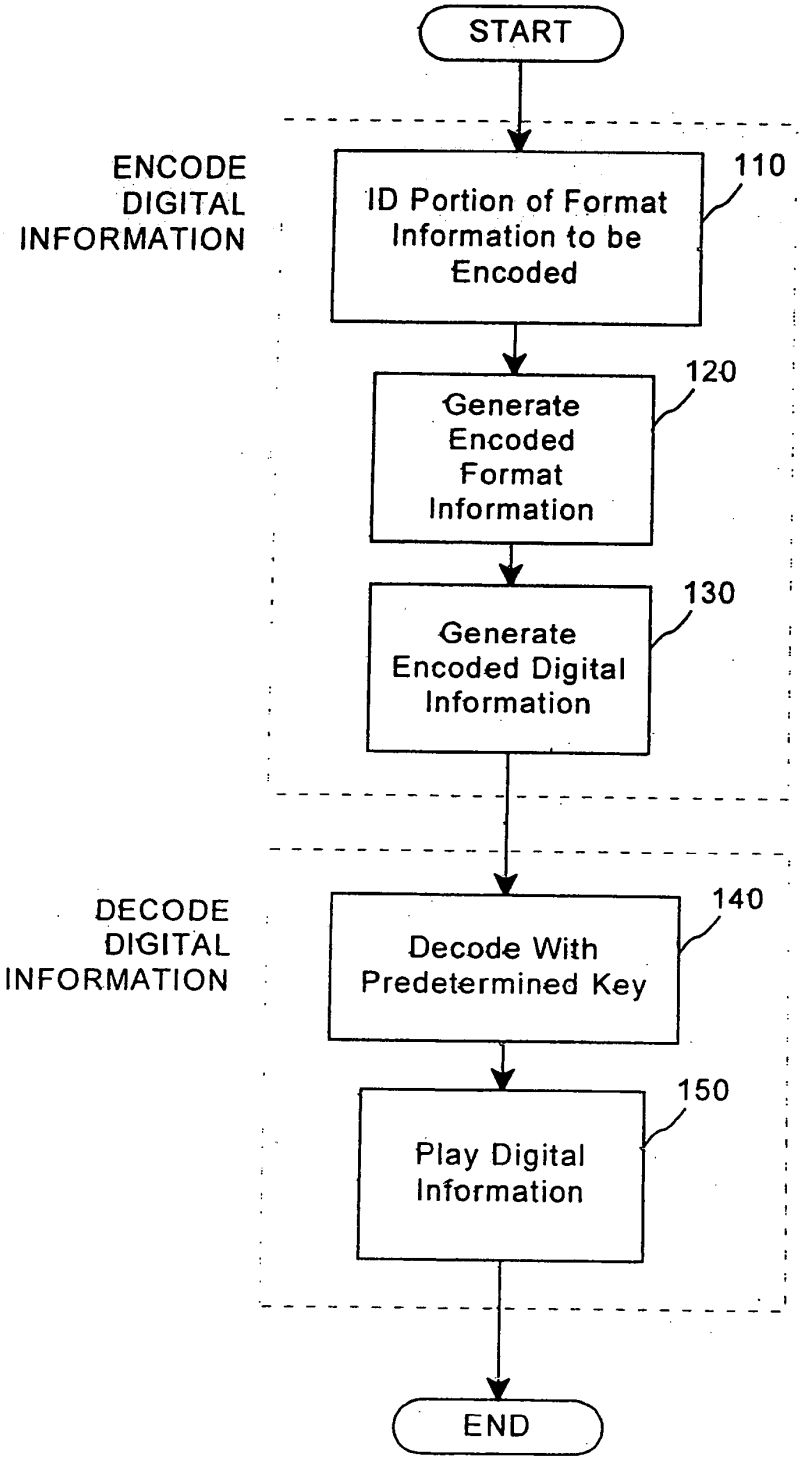

FIG. 1

Electronic Acknowledgement Receipt	
EFS ID:	13815265
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Customer Number:	31518
Filer:	Bruce Talbot Margulies
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	24-SEP-2012
Filing Date:	24-JUL-2012
Time Stamp:	10:04:29
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		ResponseToNotice_SCOT0014-6_9-24-2012c.pdf	100487 ca5dc7a039997a2b43315212446043b8622b0009	yes	3

	Multipart Description/PDF files in .zip description		
	Document Description	**Start**	**End**
	Transmittal Letter	1	1
	Miscellaneous Incoming Letter	2	2
	Drawings-only black and white line drawings	3	3
Warnings:			
Information:			
	Total Files Size (in bytes):	100487	

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: Unknown
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**

3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**

RESPONSE TO NOTICE OF INCOMPLETE REPLY (1 page)
Replacement Figure 1 (1 page)

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: None
Service Fees: None

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: BTM
ATTORNEY SIGNATURE (AUTHORIZING DEPOSIT ACCOUNT)
DATE: 9/24/2012 **SIGNATURE**: /BruceMargulies/
Printed: September 24, 2012 (9:57am) Bruce Margulies, Reg. No. 64,175
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\ResponseToNotice_SCOT0014-6_9-24-2012.wpd

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
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P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NUMBER	FILING OR 371(C) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6

CONFIRMATION NO. 5811

31518
NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

FORMALITIES LETTER

OC000000056531521

Date Mailed: 09/17/2012

NOTICE OF INCOMPLETE REPLY (NONPROVISIONAL)

Filing Date Granted

The U.S. Patent and Trademark Office has received your reply on 09/08/2012 to the Notice to File Missing Parts (Notice) mailed 08/06/2012 and it has been entered into the nonprovisional application. The reply, however, does not include the following items required in the Notice. A complete reply must be timely filed to prevent ABANDONMENT of the above-identified application. Replies should be mailed to: Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.

Applicant is given **TWO MONTHS** from the date of the Notice to File Missing Parts (Notice) mailed 08/06/2012 within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

Items Required to Avoid Abandonment:

The required items noted below SHOULD be filed along with any items required above. The filing date of this nonprovisional application will be the date of receipt of the items required above.

The application is informal since it does not comply with the regulations for the reason(s) indicated below.

The required item(s) identified below must be timely submitted to avoid abandonment:

- Replacement drawings in compliance with 37 CFR 1.84 and 37 CFR 1.121(d) are required. The drawings submitted are not acceptable because:
  - The drawings must be reasonably free from erasures and must be free from alterations, overwriting, interlineations, folds, and copy marks. See Figure(s) 1.

Applicant is cautioned that correction of the above items may cause the specification and drawings page count to exceed 100 pages. If the specification and drawings exceed 100 pages, applicant will need to submit the required application size fee.

Replies should be mailed to:

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

Registered users of EFS-Web may alternatively submit their reply to this notice via EFS-Web.
https://sportal.uspto.gov/authenticate/AuthenticateUserLocalEPF.html

For more information about EFS-Web please call the USPTO Electronic Business Center at **1-866-217-9197** or visit our website at http://www.uspto.gov/ebc.

If you are not using EFS-Web to submit your reply, you must include a copy of this notice.

/zretta/

Office of Data Management, Application Assistance Unit (571) 272-4000, or (571) 272-4200, or 1-888-786-0101

PATENT APPLICATION FEE DETERMINATION RECORD

Substitute for Form PTO-875

Application or Docket Number: 13/556,420

APPLICATION AS FILED - PART I

FOR	(Column 1) NUMBER FILED	(Column 2) NUMBER EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY FEE($)
BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A			N/A	380
SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A			N/A	620
EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A			N/A	250
TOTAL CLAIMS (37 CFR 1.16(i))	1 minus 20 =	*			OR	x 60 =	0.00
INDEPENDENT CLAIMS (37 CFR 1.16(h))	1 minus 3 =	*				x 250 =	0.00
APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						0.00
MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))							0.00
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	1250

APPLICATION AS AMENDED - PART II

		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY ADDITIONAL FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY ADDITIONAL FEE($)
AMENDMENT A	Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =	
	Application Size Fee (37 CFR 1.16(s))									
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE($)	ADDITIONAL FEE($)		RATE($)	ADDITIONAL FEE($)
AMENDMENT B	Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =	
	Application Size Fee (37 CFR 1.16(s))									
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".

*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".

The "Highest Number Previously Paid For" (Total or Independent) is the highest found in the appropriate box in column 1.

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NUMBER	FILING OR 371(C) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6

CONFIRMATION NO. 5811

31518
NEIFELD IP LAW, PC
4813-B EISENHOWER AVENUE
ALEXANDRIA, VA 22304

POA ACCEPTANCE LETTER

OC000000056531276

Date Mailed: 09/17/2012

NOTICE OF ACCEPTANCE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 08/15/2012.

The Power of Attorney in this application is accepted. Correspondence in this application will be mailed to the above address as provided by 37 CFR 1.33.

/sleutchit/

Office of Data Management, Application Assistance Unit (571) 272-4000, or (571) 272-4200, or 1-888-786-0101

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: Unknown
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.115 PRELIMINARY AMENDMENT

Sir:

Prior to examination, please amend this application as follows.

I. IN THE SPECIFICATION

At Page 1, please replace Paragraph [0001] with the following paragraph:

[0001] This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162~~, which is a continuation-in-part of U.S. patent application No. 08/587,943, filed Jan. 17, 1996, issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943~~. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162, and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, issued Apr. 28, 1998, as U.S. Pat. No. ~~5,745,943~~ 5,745,569 are hereby incorporated by reference in their entireties.

II. REMARKS

The specification is amended to remove the priority claim to application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 and to correct a typographical error in the incorporation by reference to application No. 08/587,943, filed Jan. 17, 1996.

Respectfully submitted,

9/2/2012
Date

/BruceMargulies#64175/
Bruce Margulies
Reg. No. 64,175
Attorney of Record

Printed: September 2, 2012 (6:34pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\PreliminaryAmendment_SCOT0014-6_9-2-2012.wpd

Electronic Acknowledgement Receipt	
EFS ID:	13692802
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Correspondence Address:	SCOTT MOSKOWITZ - 16711 COLLINS AVENUE NO. 2505 - SUNNY ISLES BEACH FL 33160 US 3059569041 -
Filer:	Bruce Talbot Margulies
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	08-SEP-2012
Filing Date:	24-JUL-2012
Time Stamp:	21:09:39
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		PreliminaryAmendment_SCOT 0014-6_9-2-2012c.pdf	33850 c6537da311c5c1d0d7ff8b0a878600c325aa833b	yes	4

Multipart Description/PDF files in .zip description		
Document Description	**Start**	**End**
Transmittal Letter	1	1
Preliminary Amendment	2	4

Warnings:	
Information:	
Total Files Size (in bytes):	33850

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13556420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: Unknown
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,569 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**

3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**

37 CFR 1.115 PRELIMINARY AMENDMENT (3 pages)

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: None
Service Fees: None

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: BTM
AUTHORIZING SIGNER ON DEPOSIT ACCOUNT:
DATE: 9/2/2012 **SIGNATURE:** /BruceMargulies#64175/
PRINTED NAME: Bruce T. Margulies
Printed: September 2, 2012 (6:34pm)
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\PreliminaryAmendment_SCOT0014-6_9-2-2012.wpd

PTO/SB/06 (07-06)
Approved for use through 1/31/2007. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 13/556,420	Filing Date 07/24/2012	☐ To be Mailed

APPLICATION AS FILED – PART I

	(Column 1)	(Column 2)	SMALL ENTITY ☐		OR	OTHER THAN SMALL ENTITY	
FOR	NUMBER FILED	NUMBER EXTRA	RATE ($)	FEE ($)		RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A			N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A			N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A			N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =		OR	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =			X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $250 ($125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))							
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	

APPLICATION AS AMENDED – PART II

AMENDMENT 07/24/2012

	(Column 1)		(Column 2)	(Column 3)	SMALL ENTITY		OR	OTHER THAN SMALL ENTITY	
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)		RATE ($)	ADDITIONAL FEE ($)
Total (37 CFR 1.16(i))	* 1	Minus	** 20	= 0	X $ =		OR	X $60=	0
Independent (37 CFR 1.16(h))	* 1	Minus	***3	= 0	X $ =		OR	X $250=	0
☐ Application Size Fee (37 CFR 1.16(s))									
☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
					TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	0

AMENDMENT 09/08/2012

	(Column 1)		(Column 2)	(Column 3)	RATE ($)	ADDITIONAL FEE ($)		RATE ($)	ADDITIONAL FEE ($)
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA					
Total (37 CFR 1.16(i))	* 1	Minus	** 20	= 0	X $ =		OR	X $60 =	0
Independent (37 CFR 1.16(h))	* 1	Minus	*** 3	= 0	X $ =		OR	X $250 =	0
☐ Application Size Fee (37 CFR 1.16(s))									
☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
					TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	0

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

Legal Instrument Examiner:
/LINDA HUMES/

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NUMBER	FILING OR 371(C) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
13/556,420	07/24/2012	Scott A. Moskowitz	SCOT0014-6

CONFIRMATION NO. 5811

SCOTT MOSKOWITZ
16711 COLLINS AVENUE NO. 2505
SUNNY ISLES BEACH, FL 33160

FORMALITIES LETTER

OC000000055767940

Date Mailed: 08/06/2012

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

An application number and filing date have been accorded to this application. The application is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given TWO MONTHS from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

The required item(s) identified below must be timely submitted to avoid abandonment:

- Replacement drawings in compliance with 37 CFR 1.84 and 37 CFR 1.121(d) are required. The drawings submitted are not acceptable because:
  - The drawings must be reasonably free from erasures and must be free from alterations, overwriting, interlineations, folds, and copy marks. See Figure(s) 1.

Applicant is cautioned that correction of the above items may cause the specification and drawings page count to exceed 100 pages. If the specification and drawings exceed 100 pages, applicant will need to submit the required application size fee.

Replies should be mailed to:

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

Registered users of EFS-Web may alternatively submit their reply to this notice via EFS-Web.
https://sportal.uspto.gov/authenticate/AuthenticateUserLocalEPF.html

For more information about EFS-Web please call the USPTO Electronic Business Center at **1-866-217-9197** or visit our website at http://www.uspto.gov/ebc.

If you are not using EFS-Web to submit your reply, you must include a copy of this notice.

/tmelesse/

Office of Data Management, Application Assistance Unit (571) 272-4000, or (571) 272-4200, or 1-888-786-0101

PATENT APPLICATION FEE DETERMINATION RECORD

Substitute for Form PTO-875

Application or Docket Number: 13/556,420

APPLICATION AS FILED - PART I

FOR	(Column 1) NUMBER FILED	(Column 2) NUMBER EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY FEE($)
BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A			N/A	380
SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A			N/A	620
EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A			N/A	250
TOTAL CLAIMS (37 CFR 1.16(i))	1 minus 20 =	*			OR	x 60 =	0.00
INDEPENDENT CLAIMS (37 CFR 1.16(h))	1 minus 3 =	*				x 250 =	0.00
APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $310 ($155 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						0.00
MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))							0.00
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	1250

APPLICATION AS AMENDED - PART II

		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	SMALL ENTITY RATE($)	SMALL ENTITY ADDITIONAL FEE($)	OR	OTHER THAN SMALL ENTITY RATE($)	OTHER THAN SMALL ENTITY ADDITIONAL FEE($)
AMENDMENT A	Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =	
	Application Size Fee (37 CFR 1.16(s))									
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE($)	ADDITIONAL FEE($)	OR	RATE($)	ADDITIONAL FEE($)
AMENDMENT B	Total (37 CFR 1.16(i))	*	Minus	**	=	x =		OR	x =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	x =		OR	x =	
	Application Size Fee (37 CFR 1.16(s))									
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest found in the appropriate box in column 1.

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
13/556,420	07/24/2012	2819	1250	SCOT0014-6	1	1

CONFIRMATION NO. 5811

SCOTT MOSKOWITZ
16711 COLLINS AVENUE NO. 2505
SUNNY ISLES BEACH, FL 33160

FILING RECEIPT

OC000000055767939

Date Mailed: 08/06/2012

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

Applicant(s)

Scott A. Moskowitz, Sunny Isles Beach, FL;

Power of Attorney: None

Domestic Priority data as claimed by applicant

This application is a CON of 11/895,388 08/24/2007
which is a DIV of 10/602,777 06/25/2003 PAT 7664263
which is a CON of 09/046,627 03/24/1998 PAT 6598162
which is a CIP of 08/587,943 01/17/1996 PAT 5745569

Foreign Applications (You may be eligible to benefit from the **Patent Prosecution Highway** program at the USPTO. Please see http://www.uspto.gov for more information.)

If Required, Foreign Filing License Granted: 08/03/2012

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 13/556,420**

Projected Publication Date: To Be Determined - pending completion of Corrected Papers

Non-Publication Request: No

Early Publication Request: No

Title

Data protection method and device

Preliminary Class

341

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

Applicants also are advised that in the case of inventions made in the United States, the Director of the USPTO must issue a license before applicants can apply for a patent in a foreign country. The filing of a U.S. patent application serves as a request for a foreign filing license. The application's filing receipt contains further information and guidance as to the status of applicant's license for foreign filing.

Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at http://www.uspto.gov/web/offices/pac/doc/general/index.html.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, http://www.stopfakes.gov. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4158).

LICENSE FOR FOREIGN FILING UNDER

Title 35, United States Code, Section 184

Title 37, Code of Federal Regulations, 5.11 & 5.15

GRANTED

The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as

set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Bureau of Industry and Security, Department of Commerce (15 CFR parts 730-774); the Office of Foreign AssetsControl, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

NOT GRANTED

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13/556,151
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
FIRST NAMED INVENTOR: Moskowitz
Title: Data protection method and device
Examiner/ArtUnit:
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162, which is a continuation-in-part of U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943 are hereby incorporated by reference in their entireties.

INVENTOR POWER OF ATTORNEY

I. POWER OF ATTORNEY

I hereby appoint the practitioners associated with the following customer number (for attorneys associated with Neifeld IP Law, PC) as attorneys of record to prosecute the application/patent identified above.

CUSTOMER NUMBER: **31518**

II. CORRESPONDENCE ADDRESS

Recognize or change the correspondence address for the above-identified application to the following customer number (for correspondence with Neifeld IP Law, PC)

CUSTOMER NUMBER: **31518**

III. AUTHORITY

I am a named inventor of the application identified above.

SIGNATURE: _____

PRINTED NAME: Scott Moskowitz

DATE SIGNED: July 31, 2012

Printed: July 30, 2012 (1:10PM)
Y:\FirmForms\Forms_Patent\USPowerofAttorneyFromAnInventor.doc

Electronic Acknowledgement Receipt	
EFS ID:	13420686
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott A. Moskowitz
Correspondence Address:	SCOTT MOSKOWITZ - 16711 COLLINS AVENUE NO. 2505 - SUNNY ISLES BEACH FL 33160 US 3059569041 -
Filer:	Bruce Talbot Margulies
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	05-AUG-2012
Filing Date:	24-JUL-2012
Time Stamp:	22:43:25
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		POAFiling_SCOT0014-6_8-5-2012c.pdf	119024 7325a558432877c45bf9511e0033ea1af09cf7ea	yes	3

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Transmittal Letter	1	1
	Power of Attorney	2	3

Warnings:	
Information:	
Total Files Size (in bytes):	119024

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: 13,556,420
USPTO CONF. NO: 5811
File/Issue Date: 7/24/2012
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: Unknown
ENTITY STATUS: LARGE
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162, which is a continuation-in-part of U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **FOR 35 USC 371 NATIONAL STAGE FILINGS, ONLY, THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**
NEW APPLICATION FILING FEES

3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
NEW APPLICATION DOCUMENTS
Power of Attorney (1 page)

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: None

Service Fees: None

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA: BTM
AUTHORIZING SIGNER ON DEPOSIT ACCOUNT:
DATE: 8/5/2012 **SIGNATURE**: /BruceMargulies/
Printed: August 5, 2012 (10:36pm) Bruce Margulies, Reg. No. 64,175
Y:\Clients\SCOT Scott A Moskowitz and Wistaria Trading, Inc\SCOT0014-6\Drafts\POAFiling_SCOT0014-6_8-5-2012.wpd

NEIFELD REF: SCOT0014-6
CLIENT REF: SCOT0014-6
Application/Patent No: Unknown
USPTO CONF. NO: Unknown
File/Issue Date: Filed Herewith
Inventor: SCOTT MOSKOWITZ
Title: Data protection method and device
Examiner/ArtUnit: Unknown
ENTITY STATUS: Unknown
Priority claims and PCT Intl data: This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division of U.S. patent application No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation of U.S. patent application No. 09/046,627, filed March 24, 1998, issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162, which is a continuation-in-part of U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application No. 09/046,627 which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162 and U.S. patent application No. 08/587,943, filed Jan. 17, 1996, which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943 are hereby incorporated by reference in their entireties.

37 CFR 1.7(c) FILING RECEIPT AND TRANSMITTAL LETTER WITH AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

1. **FOR 35 USC 371 NATIONAL STAGE FILINGS, ONLY, THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY FEES WHICH MAY BE REQUIRED, OR CREDIT ANY OVERPAYMENT, TO DEPOSIT ACCOUNT NUMBER 50-2106.**

2. **FEES (PAID HEREWITH BY EFS CREDIT CARD SUBMISSION) $:**1,250
NEW APPLICATION FILING FEES
1011/2011 1.16(a)(1) Basic filing fee - Utility $380
1111/2111 1.16(k) Utility Search Fee $620
1311/2311 1.16(o) Utility Examination Fee $250

3. **THE FOLLOWING DOCUMENTS ARE SUBMITTED HEREWITH:**
NEW APPLICATION DOCUMENTS
37 CFR 1.115 PRELIMINARY AMENDMENT (4 pages)
SPECIFICATION (22 pages)
CLAIMS (8 pages)
ABSTRACT (1 page)
FIGURES (1 page)
DECLARATION filed in parent application No. 11/895,388, filed August 24, 2007 (3 pages)

4. **FOR INTERNAL NEIFELD IP LAW, PC USE ONLY**
Disbursements: PClaw BankAcct, G/L: **6, 5010**
PCLAW BILLING REFERENCE:SCOT0001
Check#, Entry date, Amount: 1496, 7/22/2012, 1,250

Service Fees: Amount/CreditAtty/Entry date/Services: 400/BTM/7-22-2012/firm charge for paying a gov. fee for application filing

INITIALS OF PERSON WHO ***ENTERED*** ACCOUNTING DATA:
AUTHORIZING SIGNER ON DEPOSIT ACCOUNT:
DATE: 7/24/2012 **SIGNATURE**: /BruceMargulies/
Printed: July 24, 2012 (10:51am) Bruce Margulies, Reg. No. 64,175
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37 CFR 1.115 PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
ALEXANDRIA, VA 22313

Sir:

Prior to examination on the merits, please amend this application as follows.

I. IN THE SPECIFICATION

At Page 1, please replace Paragraph [0001] with the following paragraph:

[0001] This application is a continuation of U.S. Application No. 11/895,388, filed August 24, 2007, which is a division[[al]] of U.S. patent application [[Ser.]] No. 10/602,777, filed June 25, 2003, issued February 16, 2010 as U.S. Patent No. 7,664,263, which is a continuation ~~application~~ of U.S. patent application [[Ser. No.]] 09/046,627, filed March 24, 1998, ~~(which~~ issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162[[)]], which is a continuation-in-part of U.S. patent application [[Ser.]] No. 08/587,943, filed Jan. 17, 1996, ~~(which~~ issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943[[)]]. The entire disclosure of U.S. Application No. 11/895,388, filed August 24, 2007, U.S. patent application [[Ser.]] No. 09/046,627 ~~(which~~ issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162[[)]] and U.S. patent application [[Ser.]] No. 08/587,943, filed Jan. 17, 1996, ~~(which~~ issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943[[)]] are hereby incorporated by reference in their entireties.

II. IN THE CLAIMS

1. (Original) A method for copy protection of digital information, the digital information including a digital sample and format information, comprising the steps of: identifying a portion of the format information to be encoded; generating encoded format information from the identified portion of the format information; and generating encoded digital information, including the digital sample and the encoded format information.

2-57 (Canceled).

III. REMARKS

This preliminary amendment updates the priority claim and cancels claims 2-57. Claim 1 is pending. The applicant intends to add claims for examination prior to examination.

Respectfully Submitted,

7/24/2012 /BruceMargulies/

Date Bruce Margulies

Registration No. 64,175

BTM

Printed: July 24, 2012 (10:51am)

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DATA PROTECTION METHOD AND DEVICE

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application is a divisional of U.S. patent application Ser. No. 10/602,777, which is a continuation application of U.S. patent application Ser. No. 09/046,627 (which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162), which is a continuation-in-part of U.S. patent application Ser. No. 08/587,943, filed Jan. 17, 1996, (which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943). The entire disclosure of U.S. patent application Ser. No. 09/046,627 (which issued Jul. 22, 2003, as U.S. Pat. No. 6,598,162) and U.S. patent application Ser. No. 08/587,943, filed Jan. 17, 1996, (which issued Apr. 28, 1998, as U.S. Pat. No. 5,745,943) are hereby incorporated by reference in their entireties.

FIELD OF THE INVENTION

[0002] The invention relates to the protection of digital information. More particularly, the invention relates to a method and device for data protection.

[0003] With the advent of computer networks and digital multimedia, protection of intellectual property has become a prime concern for creators and publishers of digitized copies of copyrightable works, such as musical recordings, movies, video games, and computer software. One method of protecting copyrights in the digital domain is to use "digital watermarks."

[0004] The prior art includes copy protection systems attempted at many stages in the development of the software industry. These may be various methods by which a software engineer can write the software in a clever manner to determine if it has been copied, and if so to deactivate itself. Also included are undocumented changes to the storage format of the content. Copy protection was generally abandoned by the software industry, since pirates were generally just as clever as the software engineers and figured out ways to modify the software and deactivate the protection. The cost of developing such protection was not justified considering the level of piracy which occurred despite the copy protection.

[0005] Other methods for protection of computer software include the requirement of entering certain numbers or facts that may be included in a packaged software's manual, when prompted at start-up. These may be overcome if copies of the manual are distributed to unintended users,

or by patching the code to bypass these measures. Other methods include requiring a user to contact the software vendor and to receive "keys" for unlocking software after registration attached to some payment scheme, such as credit card authorization. Further methods include network-based searches of a user's hard drive and comparisons between what is registered to that user and what is actually installed on the user's general computing device. Other proposals, by such parties as AT&T's Bell Laboratories, use "kerning" or actual distance in pixels, in the rendering of text documents, rather than a varied number of ASCII characters. However, this approach can often be defeated by graphics processing analogous to sound processing, which randomizes that information. All of these methods require outside determination and verification of the validity of the software license.

[0006] Digital watermarks can be used to mark each individual copy of a digitized work with information identifying the title, copyright holder, and even the licensed owner of a particular copy. When marked with licensing and ownership information, responsibility is created for individual copies where before there was none. Computer application programs can be watermarked by watermarking digital content resources used in conjunction with images or audio data Digital watermarks can be encoded with random or pseudo random keys, which act as secret maps for locating the watermarks. These keys make it impossible for a party to find the watermark without having the key. In addition, the encoding method can be enhanced to force a party to cause damage to a watermarked data stream when trying to erase a random-key watermark. Other information is disclosed in "Technology: Digital Commerce", Denise Caruso, New York Times, Aug. 7, 1995; and "Copyrighting in the Information Age", Harley Ungar, ONLINE MARKETPLACE, September 1995, Jupiter Communications.

[0007] Additionally, other methods for hiding information signals in content signals, are disclosed in U.S. Pat. No. 5,319,735--Preuss et al. and U.S. Pat. No. 5,379,345--Greenberg.

[0008] It is desirable to use a "stega-cipher" or watermarking process to hide the necessary parts or resources of the executable object code in the digitized sample resources. It is also desirable to further modify the underlying structure of an executable computer application such that it is more resistant to attempts at patching and analysis by memory capture. A computer application seeks to provide a user with certain utilities or tools, that is, users interact with a computer or similar device to accomplish various tasks and applications provide the relevant interface. Thus,

a level of authentication can also be introduced into software, or "digital products," that include digital content, such as audio, video, pictures or multimedia, with digital watermarks. Security is maximized because erasing this code watermark without a key results in the destruction of one or more essential parts of the underlying application, rendering the "program" useless to the unintended user who lacks the appropriate key. Further, if the key is linked to a license code by means of a mathematical function, a mechanism for identifying the licensed owner of an application is created.

[0009] It is also desirable to randomly reorganize program memory structure intermittently during program run time, to prevent attempts at memory capture or object code analysis aimed at eliminating licensing or ownership information, or otherwise modifying, in an unintended manner, the functioning of the application.

[0010] In this way, attempts to capture memory to determine underlying functionality or provide a "patch" to facilitate unauthorized use of the "application," or computer program, without destroying the functionality and thus usefulness of a copyrightable computer program can be made difficult or impossible.

[0011] It is thus the goal of the present invention to provide a higher level of copyright security to object code on par with methods described in digital watermarking systems for digitized media content such as pictures, audio, video and multimedia content in its multifarious forms, as described in previous disclosures, "Steganographic Method and Device" Ser. No. 08/489,172, filed Jun. 7, 1995, now U.S. Pat. No. 5,613,004, and "Human Assisted Random Key Generation and Application for Digital Watermark System", Ser. No. 08/587,944, filed on Jan. 17, 1996, the disclosure of which is hereby incorporated by reference.

[0012] It is a further goal of the present invention to establish methods of copyright protection that can be combined with such schemes as software metering, network distribution of code and specialized protection of software that is designed to work over a network, such as that proposed by Sun Microsystems in their HotJava browser and Java programming language, and manipulation of application code in proposed distribution of documents that can be exchanged with resources or the look and feel of the document being preserved over a network. Such systems are currently being offered by companies including Adobe, with their Acrobat software.

This latter goal is accomplished primarily by means of the watermarking of font, or typeface, resources included in applications or documents, which determine how a bitmap representation of the document is ultimately drawn on a presentation device.

[0013] The present invention includes an application of the technology of "digital watermarks." As described in previous disclosures, "Steganographic Method and Device" and "Human Assisted Random Key Generation and Application for Digital Watermark System," watermarks are particularly suitable to the identification, metering, distributing and authenticating digitized content such as pictures, audio, video and derivatives thereof under the description of "multimedia content." Methods have been described for combining both cryptographic methods, and steganography, or hiding something in plain view. Discussions of these technologies can be found in Applied Cryptography by Bruce Schneier and The Code Breakers by David Kahn. For more information on prior art public-key cryptosystems see U.S. Pat. No. 4,200,770 Diffie-Hellman, U.S. Pat. No. 4,218,582 Hellman, U.S. Pat. No. 4,405,829 RSA, U.S. Pat. No. 4,424,414 Hellman Pohlig. Computer code, or machine language instructions, which are not digitized and have zero tolerance for error, must be protected by derivative or alternative methods, such as those disclosed in this invention, which focuses on watermarking with "keys" derived from license codes or other ownership identification information, and using the watermarks encoded with such keys to hide an essential subset of the application code resources.

BACKGROUND OF THE INVENTION

[0014] Increasingly, commercially valuable information is being created and stored in "digital" form. For example, music, photographs and video can all be stored and transmitted as a series of numbers, such as 1's and 0's. Digital techniques let the original information be recreated in a very accurate manner. Unfortunately, digital techniques also let the information be easily copied without the information owner's permission.

[0015] Because unauthorized copying is clearly a disincentive to the digital distribution of valuable information, it is important to establish responsibility for copies and derivative copies of such works. For example, if each authorized digital copy of a popular song is identified with a unique number, any unauthorized copy of the song would also contain the number. This would allow the owner of the information, such as a song publisher, to investigate who made the

unauthorized copy. Unfortunately, it is possible that the unique number could be erased or altered if it is simply tacked on at the beginning or end of the digital information.

[0016] As will be described, known digital "watermark" techniques give creators and publishers of digitized multimedia content localized, secured identification and authentication of that content. In considering the various forms of multimedia content, such as "master," stereo, National Television Standards Committee (NTSC) video, audio tape or compact disc, tolerance of quality will vary with individuals and affect the underlying commercial and aesthetic value of the content. For example, if a digital version of a popular song sounds distorted, it will be less valuable to users. It is therefore desirable to embed copyright, ownership or purchaser information, or some combination of these and related data, into the content in a way that will damage the content if the watermark is removed without authorization.

[0017] To achieve these goals, digital watermark systems insert ownership information in a way that causes little or no noticeable effects, or "artifacts," in the underlying content signal. For example, if a digital watermark is inserted into a digital version of a song, it is important that a listener not be bothered by the slight changes introduced by the watermark. It is also important for the watermark technique to maximize the encoding level and "location sensitivity" in the signal to force damage to the content signal when removal is attempted. Digital watermarks address many of these concerns, and research in the field has provided extremely robust and secure implementations.

[0018] What has been overlooked in many applications described in the art, however, are systems which closely mimic distribution of content as it occurs in the real world. For instance, many watermarking systems require the original un-watermarked content signal to enable detection or decode operations. These include highly publicized efforts by NEC, Digimarc and others. Such techniques are problematic because, in the real world, original master copies reside in a rights holders vaults and are not readily available to the public.

[0019] With much activity overly focused on watermark survivability, the security of a digital watermark is suspect. Any simple linear operation for encoding information into a signal may be used to erase the embedded signal by inverting the process. This is not a difficult task, especially when detection software is a plug-in freely available to the public, such as with Digimarc. In

general, these systems seek to embed cryptographic information, not cryptographically embed information into target media content.

[0020] Other methods embed ownership information that is plainly visible in the media signal, such as the method described in U.S. Pat. No. 5,530,739 to Braudaway et al. The system described in Braudaway protects a digitized image by encoding a visible watermark to deter piracy. Such an implementation creates an immediate weakness in securing the embedded information because the watermark is plainly visible. Thus, no search for the embedded signal is necessary and the watermark can be more easily removed or altered. For example, while certainly useful to some rights owners, simply placing the symbol "©" in the digital information would only provide limited protection. Removal by adjusting the brightness of the pixels forming the "©" would not be difficult with respect to the computational resources required.

[0021] Other relevant prior art includes U.S. Pat. Nos. 4,979,210 and 5,073,925 to Nagata et al., which encodes information by modulating an audio signal in the amplitude/time domain. The modulations introduced in the Nagata process carry a "copy/don't copy" message, which is easily found and circumvented by one skilled in the art. The granularity of encoding is fixed by the amplitude and frequency modulation limits required to maintain inaudibility. These limits are relatively low, making it impractical to encode more information using the Nagata process.

[0022] Although U.S. Pat. No. 5,661,018 to Leighton describes a means to prevent collusion attacks in digital watermarks, the disclosed method may not actually provide the security described. For-example, in cases where the watermarking technique is linear, the "insertion envelope" or "watermarking space" is well-defined and thus susceptible to attacks less sophisticated than collusion by unauthorized parties. Over-encoding at the watermarking encoding level is but one simple attack in such linear implementations. Another consideration not made by Leighton is that commercially-valuable content may already exist in a un-watermarked form somewhere, easily accessible to potential pirates, gutting the need for any type of collusive activity. Digitally signing the embedded signal with preprocessing of watermark data is more likely to prevent successful collusion. Furthermore, a "baseline" watermark as disclosed is quite subjective. It is simply described elsewhere in the art as the "perceptually significant" regions of a signal. Making a watermarking function less linear or inverting the insertion of watermarks would seem to provide the same benefit without the

additional work required to create a "baseline" watermark. Indeed, watermarking algorithms should already be capable of defining a target insertion envelope or region without additional steps. What is evident is the Leighton patent does not allow for initial prevention of attacks on an embedded watermark as the content is visibly or audibly unchanged.

[0023] It is also important that any method for providing security also function with broadcasting media over networks such as the Internet, which is also referred to as "streaming." Commercial "plug-in" products such as RealAudio and RealVideo, as well as applications by vendors VDONet and Xtreme, are common in such network environments. Most digital watermark implementations focus on common file base signals and fail to anticipate the security of streamed signals. It is desirable that any protection scheme be able to function with a plug-in player without advanced knowledge of the encoded media stream.

[0024] Other technologies focus solely on file-based security. These technologies illustrate the varying applications for security that must be evaluated for different media and distribution environments. Use of cryptolopes or cryptographic containers, as proposed by IBM in its Cryptolope product, and InterTrust, as described in U.S. Pat. Nos. 4,827,508, 4,977,594, 5,050,213 and 5,410,598, may discourage certain forms of piracy. Cryptographic containers, however, require a user to subscribe to particular decryption software to decrypt data. IBM's InfoMarket and InterTrust's DigiBox, among other implementations, provide a generalized model and need proprietary architecture to function. Every user must have a subscription or registration with the party which encrypts the data. Again, as a form of general encryption, the data is scrambled or encrypted without regard to the media and its formatting. Finally, control over copyrights or other neighboring rights is left with the implementing party, in this case, IBM, InterTrust or a similar provider.

[0025] Methods similar to these "trusted systems" exist, and Cerberus Central Limited and Liquid Audio, among a number of companies, offer systems which may functionally be thought of as subsets of IBM and InterTrust's more generalized security offerings. Both Cerberus and Liquid Audio propose proprietary player software which is registered to the user and "locked" in a manner parallel to the locking of content that is distributed via a cryptographic container. The economic trade-off in this model is that users are required to use each respective companies' proprietary player to play or otherwise manipulate content that is downloaded. If, as is the case

presently, most music or other media is not available via these proprietary players and more companies propose non-compatible player formats, the proliferation of players will continue. Cerberus and Liquid Audio also by way of extension of their architectures provide for "near-CD quality" but proprietary compression. This requirement stems from the necessity not to allow content that has near-identical data make-up to an existing consumer electronic standard, in Cerberus and Liquid Audio's case the so-called Red Book audio CD standard of 16 bit 44.1 kHz, so that comparisons with the proprietary file may not yield how the player is secured. Knowledge of the player's file format renders its security ineffective as a file may be replicated and played on any common player, not the intended proprietary player of the provider of previously secured and uniquely formatted content. This is the parallel weakness to public key crypto-systems which have gutted security if enough plain text and cipher text comparisons enable a pirate to determine the user's private key.

[0026] Many approaches to digital watermarking leave detection and decoding control with the implementing party of the digital watermark, not the creator of the work to be protected. A set of secure digital watermark implementations address this fundamental control issue forming the basis of key-based approaches. These are covered by the following patents and pending applications, the entire disclosures of which are hereby incorporated by reference: U.S. Pat. No. 5,613,004 entitled "Steganographic Method and Device" and its derivative U.S. patent application Ser. No. 08/775,216 (which issued Nov. 11, 1997, as U.S. Pat. No. 5,687,236), U.S. patent application Ser. No. 08/587,944 entitled "Human Assisted Random Key Generation and Application for Digital Watermark System" (which issued Oct. 13, 1998, as U.S. Pat. No. 5,822,432), U.S. patent application Ser. No. 08/587,943 entitled "Method for Stega-Cipher Protection of Computer Code" (which issued Apr. 28, 1998, as U.S. Pat. No. 5,748,569), U.S. patent application Ser. No. 08/677,435 entitled "Optimization Methods for the Insertion, Protection, and Detection of Digital Watermarks in Digitized Data" (which issued Mar. 30, 1999, as U.S. Pat. No. 5,889,868) and U.S. patent application Ser. No. 08/772,222 entitled "Z-Transform Implementation of Digital Watermarks" (which issued Jun. 20, 2000, as U.S. Pat. No. 6,078,664). Public key crypto-systems are described in U.S. Pat. Nos. 4,200,770, 4,218,582, 4,405,829 and 4,424,414, the entire disclosures of which are also hereby incorporated by reference.

[0027] In particular, an improved protection scheme is described in "Method for Stega-Cipher

Protection of Computer Code," U.S. patent application Ser. No. 08/587,943 (which issued Apr. 28, 1998, as U.S. Pat. No. 5,748,569). This technique uses the key-based insertion of binary executable computer code within a content signal that is subsequently, and necessarily, used to play or otherwise manipulate the signal in which it is encoded. With this system, however, certain computational requirements, such as one digital player per digital copy of content, may be necessitated. For instance, a consumer may download many copies of watermarked content. With this technique, the user would also be downloading as many copies of the digital player program. While this form of security may be desirable for some applications, it is not appropriate in many circumstances.

[0028] Finally, even when digital information is distributed in encoded form, it may be desirable to allow unauthorized users to play the information with a digital player, perhaps with a reduced level of quality. For example, a popular song may be encoded and freely distributed in encoded form to the public. The public, perhaps using commonly available plug-in digital players, could play the encoded content and hear the music in some degraded form. The music may-sound choppy, or fuzzy or be degraded in some other way. This lets the public decide, based on the available lower quality version of the song, if they want to purchase a key from the publisher to decode, or "clean-up," the content. Similar approaches could be used to distribute blurry pictures or low quality video. Or even "degraded" text, in the sense that only authenticated portions of the text can be determined with the predetermined key or a validated digital signature for the intended message.

[0029] In view of the foregoing, it can be appreciated that a substantial need exists for a method allowing encoded content to be played, with degraded quality, by a plug-in digital player, and solving the other problems discussed above.

SUMMARY OF THE INVENTION

[0030] The disadvantages of the art are alleviated to a great extent by a method for combining transfer functions with predetermined key creation. In one embodiment, digital information, including a digital sample and format information, is protected by identifying and encoding a portion of the format information. Encoded digital information, including the digital sample and the encoded format information, is generated to protect the original digital information.

[0031] In another embodiment, a digital signal, including digital samples in a file format having an inherent granularity, is protected by creating a predetermined key. The predetermined key is comprised of a transfer function-based mask set to manipulate data at the inherent granularity of the file format of the underlying digitized samples.

[0032] It is thus a goal of the present invention, to provide a level of security for executable code on similar grounds as that which can be provided for digitized samples. Furthermore, the present invention differs from the prior art in that it does not attempt to stop copying, but rather, determines responsibility for a copy by ensuring that licensing information must be preserved in descendant copies from an original. Without the correct license information, the copy cannot function.

[0033] An improvement over the art is disclosed in the present invention, in that the software itself is a set of commands, compiled by software engineer, which can be configured in such a manner as to tie underlying functionality to the license or authorization of the copy in possession by the user. Without such verification, the functions sought out by the user in the form of software cease to properly work. Attempts to tamper or "patch" substitute code resources can be made highly difficult by randomizing the location of said resources in memory on an intermittent basis to resist most attacks at disabling the system.

[0034] With these and other advantages and features of the invention that will become hereinafter apparent, the nature of the invention may be more clearly understood by reference to the following detailed description of the invention, the appended claims and to the several drawings attached herein.

BRIEF DESCRIPTION OF THE DRAWINGS

[0035] FIG. 1 is a block flow diagram of a method for copy protection or authentication of digital information according to an embodiment of the present invention.

DETAILED DESCRIPTION

[0036] In accordance with an embodiment of the present invention, a method combines transfer

functions with predetermined key creation. Increased security is achieved in the method by combining elements of "public-key steganography" with cryptographic protocols, which keep in-transit data secure by scrambling the data with "keys" in a manner that is not apparent to those with access to the content to be distributed. Because different forms of randomness are combined to offer robust, distributed security, the present invention addresses an architectural "gray space" between two important areas of security: digital watermarks, a subset of the more general art of steganography, and cryptography. One form of randomness exists in the mask sets that are randomly created to map watermark data into an otherwise unrelated digital signal. The second form of randomness is the random permutations of data formats used with digital players to manipulate the content with the predetermined keys. These forms can be thought of as the transfer function versus the mapping function inherent to digital watermarking processes.

[0037] According to an embodiment of the present invention, a predetermined, or randomly generated, key is used to scramble digital information in a way that is unlike known "digital watermark" techniques and public key crypto-systems. As used herein, a key is also referred to as a "mask set" which includes one or more random or pseudo-random series of bits. Prior to encoding, a mask can be generated by any cryptographically secure random generation process. A block cipher, such as a Data Encryption Standard (DES) algorithm, in combination with a sufficiently random seed value, such as one created using a Message Digest 5 (MD5) algorithm, emulates a cryptographically secure random bit generator. The keys are saved in a database, along with information matching them to the digital signal, for use in descrambling and subsequent viewing or playback. Additional file format or transfer property information is prepared and made available to the encoder, in a bit addressable manner. As well, any authenticating function can be combined, such as Digital Signature Standard (DSS) or Secure Hash Algorithm (SHA).

[0038] Using the predetermined key comprised of a transfer function-based mask set, the data representing the original content is manipulated at the inherent granularity of the file format of the underlying digitized samples. Instead of providing, or otherwise distributing, watermarked content that is not noticeably altered, a partially "scrambled" copy of the content is distributed. The key is necessary both to register the sought-after content and to descramble the content into its original form.

[0039] The present invention uses methods disclosed in "Method for Stega-Cipher Protection of Computer Code," U.S. patent application Ser. No. 08/587,943 (which issued Apr. 28, 1998, as U.S. Pat. No. 5,748,569), with respect to transfer functions related to the common file formats, such as PICT, TIFF, AIFF, WAV, etc. Additionally, in cases where the content has not been altered beyond being encoded with such functional data, it is possible for a digital player to still play the content because the file format has not been altered. Thus, the encoded content could still be played by a plug-in digital player as discrete, digitally sampled signals, watermarked or not. That is, the structure of the file can remain basically unchanged by the watermarking process, letting common file format based players work with the "scrambled" content.

[0040] For example, the Compact Disc-Digital Audio (CD-DA) format stores audio information as a series of frames. Each frame contains a number of digital samples representing, for example, music, and a header that contains file format information. As shown in FIG. 1, according to an embodiment of the present invention some of the header information can be identified and "scrambled" using the predetermined key at steps 110 to 130. The music samples can remain unchanged. Using this technique, a traditional CD-DA player will be able to play a distorted version of the music in the sample. The amount of distortion will depend on the way, and extent, that the header, or file format, information has been scrambled. It would also be possible to instead scramble some of the digital samples while leaving the header information alone. In general, the digital signal would be protected by manipulating data at the inherent granularity, or "frames," of the CD-DA file format. To decode the information, a predetermined key is used before playing the digital information at steps 140 and 150.

[0041] A key-based decoder can act as a "plug-in" digital player of broadcast signal streams without foreknowledge of the encoded media stream. Moreover, the data format orientation is used to partially scramble data in transit to prevent unauthorized descrambled access by decoders that lack authorized keys. A distributed key can be used to unscramble the scrambled content because a decoder would understand how to process the key. Similar to on-the-fly decryption operations, the benefits inherent in this embodiment include the fact that the combination of watermarked content security, which is key-based, and the descrambling of the data, can be performed by the same key which can be a plurality of mask sets. The mask sets may include primary, convolution and message delimiter masks with file format data included.

[0042] The creation of an optimized "envelope" for insertion of watermarks provides the basis of much watermark security, but is also a complementary goal of the present invention. The predetermined or random key that is generated is not only an essential map to access the hidden information signal, but is also the descrambler of the previously scrambled signal's format for playback or viewing.

[0043] In a system requiring keys for watermarking content and validating the distribution of the content, different keys may be used to encode different information while secure one way hash functions or one-time pads may be incorporated to secure the embedded signal. The same keys can be used to later validate the embedded digital signature, or even fully decode the digital watermark if desired. Publishers can easily stipulate that content not only be digitally watermarked but that distributors must check the validity of the watermarks by performing digital signature-checks with keys that lack any other functionality. The system can extend to simple authentication of text in other embodiments.

[0044] Before such a market is economically feasible, there are other methods for deploying key-based watermarking coupled with transfer functions to partially scramble the content to be distributed without performing full public key encryption, i.e., a key pair is not necessarily generated, simply, a predetermined key's function is created to re-map the data of the content file in a lossless process. Moreover, the scrambling performed by the present invention may be more dependent on the file in question. Dissimilarly, encryption is not specific to any particular media but is performed on data. The file format remains unchanged, rendering the file useable by any conventional viewer/player, but the signal quality can be intentionally degraded in the absence of the proper player and key. Public-key encryption seeks to completely obscure the sensitive "plaintext" to prevent comparisons with the "ciphertext" to determine a user's private keys. Centralized encryption only differs in the utilization of a single key for both encryption and decryption making the key even more highly vulnerable to attacks to defeat the encryption process. With the present invention, a highly sought after photograph may be hazy to the viewer using any number of commonly available, nonproprietary software or hardware, without the authorized key. Similarly, a commercially valuable song may sound poor.

[0045] The benefit of some form of cryptography is not lost in the present invention. In fact, some piracy can be deterred when the target signal may be known but is clearly being protected

through scrambling. What is not anticipated by known techniques, is an ala carte method to change various aspects of file formatting to enable various "scrambled states" for content to be subsequently distributed. An image may lack all red pixels or may not have any of the most significant bits activated. An audio sample can similarly be scrambled to render it less-than-commercially viable.

[0046] The present invention also provides improvements over known network-based methods, such as those used for the streaming of media data over the Internet. By manipulating file formats, the broadcast media, which has been altered to "fit" within electronic distribution parameters, such as bandwidth availability and error correction considerations, can be more effectively utilized to restrict the subsequent use of the content while in transit as well as real-time viewing or playing.

[0047] The mask set providing the transfer function can be read on a per-use basis by issuing an authorized or authenticating "key" for descrambling the signal that is apparent to a viewer or a player or possessor of the authenticating key. The mask set can be read on a per-computer basis by issuing the authorized key that is more generalized for the computer that receives the broadcast signals. Metering and subscription models become viable advantages over known digital watermark systems which assist in designating the ownership of a copy of digitized media content, but do not prevent or restrict the copying or manipulation of the sampled signal in question. For broadcast or streamed media, this is especially the case. Message authentication is also possible, though not guaranteeing the same security as an encrypted file as with general crypto systems.

[0048] The present invention thus benefits from the proprietary player model without relying on proprietary players. No new players will be necessary and existing multimedia file formats can be altered to exact a measure of security which is further increased when coupled with digital watermarks. As with most consumer markets for media content, predominant file formats exist, de facto, and corresponding formats for computers likewise exist. For a commercial compact disc quality audio recording, or 16 bit 44.1 kHz, corresponding file formats include: Audio Interchange File Format (AIFF), Microsoft WAV, Sound Designer II, Sun's .au, Apple's Quicktime, etc. For still image media, formats are similarly abundant: TIFF, PICT, JPEG, GIF, etc. Requiring the use of additional proprietary players, and their complementary file formats, for

limited benefits in security is wasteful. Moreover, almost all computers today are multimedia-capable, and this is increasingly so with the popularity of Intel's MMX chip architecture and the PowerPC line of microchips. Because file formatting is fundamental in the playback of the underlying data, the predetermined key can act both as a map, for information to be encoded as watermark data regarding ownership, and a descrambler of the file that has been distributed. Limitations will only exist in how large the key must be retrofitted for a given application, but any manipulation of file format information is not likely to exceed the size of data required versus that for an entire proprietary player.

[0049] As with previous disclosures by the inventor on digital watermarking techniques, the present invention may be implemented with a variety of cryptographic protocols to increase both confidence and security in the underlying system. A predetermined key is described as a set of masks. These masks may include primary, convolution and message delimiter mask. In previous disclosures, the functionality of these masks is defined solely for mapping. The present invention includes a mask set which is also controlled by the distributing party of a copy of a given media signal. This mask set is a transfer function which is limited only by the parameters of the file format in question. To increase the uniqueness or security of each key used to scramble a given media file copy, a secure one way hash function can be used subsequent to transfer properties that are initiated to prevent the forging of a particular key. Public and private keys may be used as key pairs to further increase the unlikeliness that a key may be compromised.

[0050] These same cryptographic protocols can be combined with the embodiments of the present invention in administering streamed content that requires authorized keys to correctly display or play the streamed content in an unscrambled manner. As with digital watermarking, symmetric or asymmetric public key pairs may be used in a variety of implementations. Additionally, the need for certification authorities to maintain authentic key-pairs becomes a consideration for greater security beyond symmetric key implementations. The cryptographic protocols makes possible, as well, a message of text to be authenticated by a message authenticating function in a general computing device that is able to ensure secure message exchanges between authorizing parties.

[0051] An executable computer program is variously referred to as an application, from the point of view of a user, or executable object code from the point of view of the engineer. A collection

of smaller, atomic (or indivisible) chunks of object code typically comprise the complete executable object code or application which may also require the presence of certain data resources. These indivisible portions of object code correspond with the programmers' function or procedure implementations in higher level languages, such as C or Pascal. In creating an application, a programmer writes "code" in a higher level language, which is then compiled down into "machine language," or, the executable object code, which can actually be run by a computer, general purpose or otherwise. Each function, or procedure, written in the programming language, represents a self-contained portion of the larger program, and implements, typically, a very small piece of its functionality. The order in which the programmer types the code for the various functions or procedures, and the distribution of and arrangement of these implementations in various files which hold them is unimportant. Within a function or procedure, however, the order of individual language constructs, which correspond to particular machine instructions is important, and so functions or procedures are considered indivisible for purposes of this discussion. That is, once a function or procedure is compiled, the order of the machine instructions which comprise the executable object code of the function is important and their order in the computer memory is of vital importance. Note that many "compilers" perform "optimizations" within functions or procedures, which determine, on a limited scale, if there is a better arrangement for executable instructions which is more efficient than that constructed by the programmer, but does not change the result of the function or procedure. Once these optimizations are performed, however, making random changes to the order of instructions is very likely to "break" the function. When a program is compiled, then, it consists of a collection of these sub-objects, whose exact order or arrangement in memory is not important, so long as any sub-object which uses another sub-object knows where in memory it can be found.

[0052] The memory address of the first instruction in one of these sub-objects is called the "entry point" of the function or procedure. The rest of the instructions comprising that sub-object immediately follow from the entry point. Some systems may prefix information to the entry point which describes calling and return conventions for the code which follows, an example is the Apple Macintosh Operating System (MacOS). These sub-objects can be packaged into what are referred to in certain systems as "code resources," which may be stored separately from the application, or shared with other applications, although not necessarily. Within an application there are also data objects, which consist of some data to be operated on by the executable code. These data objects are not executable. That is, they do not consist of executable instructions. The

data objects can be referred to in certain systems as "resources."

[0053] When a user purchases or acquires a computer program, she seeks a computer program that "functions" in a desired manner. Simply, computer software is overwhelmingly purchased for its underlying functionality. In contrast, persons who copy multimedia content, such as pictures, audio and video, do so for the entertainment or commercial value of the content. The difference between the two types of products is that multimedia content is not generally interactive, but is instead passive, and its commercial value relates more on passive not interactive or utility features, such as those required in packaged software, set-top boxes, cellular phones, VCRs, PDAs, and the like. Interactive digital products which include computer code may be mostly interactive but can also contain content to add to the interactive experience of the user or make the underlying utility of the software more aesthetically pleasing. It is a common concern of both of these creators, both of interactive and passive multimedia products, that "digital products" can be easily and perfectly copied and made into unpaid or unauthorized copies. This concern is especially heightened when the underlying product is copyright protected and intended for commercial use.

[0054] The first method of the present invention described involves hiding necessary "parts" or code "resources" in digitized sample resources using a "digital watermarking" process, such as that described in the "Steganographic Method and Device" patent application. The basic premise for this scheme is that there are a certain sub-set of executable code resources, that comprise an application and that are "essential" to the proper function of the application. In general, any code resource can be considered "essential" in that if the program proceeds to a point where it must "call" the code resource and the code resource is not present in memory, or cannot be loaded, then the program fails. However, the present invention uses a definition of "essential" which is more narrow. This is because, those skilled in the art or those with programming experience, may create a derivative program, not unlike the utility provided by the original program, by writing additional or substituted code to work around unavailable resources. This is particularly true with programs that incorporate an optional "plug-in architecture," where several code resources may be made optionally available at run-time. The present invention is also concerned with concentrated efforts by technically skilled people who can analyze executable object code and "patch" it to ignore or bypass certain code resources. Thus, for the present embodiment's purposes, "essential" means that the function which distinguishes this application from any other

application depends upon the presence and use of the code resource in question. The best candidates for this type of code resources are NOT optional, or plug-in types, unless special care is taken to prevent work-arounds.

[0055] Given that there are one or more of these essential resources, what is needed to realize the present invention is the presence of certain data resources of a type which are amenable to the "stega-cipher" process described in the "Steganographic Method and Device" patent U.S. Pat. No. 5,613,004. Data which consists of image or audio samples is particularly useful. Because this data consists of digital samples, digital watermarks can be introduced into the samples. What is further meant is that certain applications include image and audio samples which are important to the look and feel of the program or are essential to the processing of the application's functionality when used by the user. These computer programs are familiar to users of computers but also less obvious to users of other devices that run applications that are equivalent in some measure of functionality to general purpose computers including, but not limited to, set-top boxes, cellular phones, "smart televisions," PDAs and the like. However, programs still comprise the underlying "operating systems" of these devices and are becoming more complex with increases in functionality.

[0056] One method of the present invention is now discussed. When code and data resources are compiled and assembled into a precursor of an executable program the next step is to use a utility application for final assembly of the executable application. The programmer marks several essential code resources in a list displayed by the utility. The utility will choose one or several essential code resources, and encode them into one or several data resources using the stegacipher process. The end result will be that these essential code resources are not stored in their own partition, but rather stored as encoded information in data resources. They are not accessible at run-time without the key. Basically, the essential code resources that provide functionality in the final end-product, an executable application or computer program, are no longer easily and recognizably available for manipulation by those seeking to remove the underlying copyright or license, or its equivalent information, or those with skill to substitute alternative code resources to "force" the application program to run as an unauthorized copy. For the encoding of the essential code resources, a "key" is needed. Such a key is similar to those described in U.S. Pat. No. 5,613,004, the "Steganographic Method and Device" patent. The purpose of this scheme is to make a particular licensed copy of an application distinguishable

from any other. It is not necessary to distinguish every instance of an application, merely every instance of a license. A licensed user may then wish to install multiple copies of an application, legally or with authorization. This method, then, is to choose the key so that it corresponds, is equal to, or is a function of, a license code or license descriptive information, not just a text file, audio clip or identifying piece of information as desired in digital watermarking schemes extant and typically useful to stand-alone, digitally sampled content. The key is necessary to access the underlying code, i.e., what the user understands to be the application program.

[0057] The assembly utility can be supplied with a key generated from a license code generated for the license in question. Alternatively, the key, possibly random, can be stored as a data resource and encrypted with a derivative of the license code. Given the key, it encodes one or several essential resources into one or several data resources. Exactly which code resources are encoded into which data resources may be determined in a random or pseudo random manner. Note further that the application contains a code resource which performs the function of decoding an encoded code resource from a data resource. The application must also contain a data resource which specifies in which data resource a particular code resource is encoded. This data resource is created and added at assembly time by the assembly utility. The application can then operate as follows:

[0058] 1) when it is run for the first time, after installation, it asks the user for personalization information, which includes the license code. This can include a particular computer configuration;

[0059] 2) it stores this information in a personalization data resource;

[0060] 3) Once it has the license code, it can then generate the proper decoding key to access the essential code resources.

[0061] Note that the application can be copied in an uninhibited manner, but must contain the license code issued to the licensed owner, to access its essential code resources. The goal of the invention, copyright protection of computer code and establishment of responsibility for copies, is thus accomplished.

[0062] This invention represents a significant improvement over prior art because of the inherent difference in use of purely informational watermarks versus watermarks which contain executable object code. If the executable object code in a watermark is essential to an application which accesses the data which contains the watermark, this creates an all-or-none situation. Either the user must have the extracted watermark, or the application cannot be used, and hence the user cannot gain full access to the presentation of the information in the watermark bearing data. In order to extract a digital watermark, the user must have a key. The key, in turn, is a function of the license information for the copy of the software in question. The key is fixed prior to final assembly of the application files, and so cannot be changed at the option of the user. That, in turn, means the license information in the software copy must remain fixed, so that the correct key is available to the software. The key and the license information are, in fact, interchangeable. One is merely more readable than the other. In U.S. Pat. No. 5,613,004, the "Steganographic Method and Device, patent", the possibility of randomization erasure attacks on digital watermarks was discussed. Simply, it is always possible to erase a digital watermark, depending on how much damage you are willing to do to the watermark-bearing content stream. The present invention has the significant advantage that you must have the watermark to be able to use the code it contains. If you erase the watermark you have lost a key piece of the functionality of the application, or even the means to access the data which bear the watermark.

[0063] A preferred embodiment would be implemented in an embedded system, with a minimal operating system and memory. No media playing "applets," or smaller sized applications as proposed in new operating environments envisioned by Sun Microsystems and the advent of Sun's Java operating system, would be permanently stored in the system, only the bare necessities to operate the device, download information, decode watermarks and execute the applets contained in them. When an applet is finished executing, it is erased from memory. Such a system would guarantee that content which did not contain readable watermarks could not be used. This is a powerful control mechanism for ensuring that content to be distributed through such a system contains valid watermarks. Thus, in such networks as the Internet or set-top box controlled cable systems, distribution and exchange of content would be made more secure from unauthorized copying to the benefit of copyright holders and other related parties. The system would be enabled to invalidate, by default, any content which has had its watermark(s) erased, since the watermark conveys, in addition to copyright information, the means to fully access, play, record or otherwise manipulate, the content.

[0064] A second method according to the present invention is to randomly re-organize program memory structure to prevent attempts at memory capture or object code analysis. The object of this method is to make it extremely difficult to perform memory capture-based analysis of an executable computer program. This analysis is the basis for a method of attack to defeat the system envisioned by the present invention.

[0065] Once the code resources of a program are loaded into memory, they typically remain in a fixed position, unless the computer operating system finds it necessary to rearrange certain portions of memory during "system time," when the operating system code, not application code, is running. Typically, this is done in low memory systems, to maintain optimal memory utilization. The MacOS for example, uses Handles, which are double-indirect pointers to memory locations, in order to allow the operating system to rearrange memory transparently, underneath a running program. If a computer program contains countermeasures against unlicensed copying, a skilled technician can often take a snapshot of the code in memory, analyze it, determine which instructions comprise the countermeasures, and disable them in the stored application file, by means of a "patch." Other applications for designing code that moves to prevent scanning-tunnelling microscopes, and similar high sensitive hardware for analysis of electronic structure of microchips running code, have been proposed by such parties as Wave Systems. Designs of Wave Systems' microchip are intended for preventing attempts by hackers to "photograph" or otherwise determine "burn in" to microchips for attempts at reverse engineering. The present invention seeks to prevent attempts at understanding the code and its organization for the purpose of patching it. Unlike systems such as Wave Systems', the present invention seeks to move code around in such a manner as to complicate attempts by software engineers to reengineer a means to disable the methods for creating licensed copies on any device that lacks "trusted hardware." Moreover, the present invention concerns itself with any application software that may be used in general computing devices, not chipsets that are used in addition to an underlying computer to perform encryption. Wave Systems' approach to security of software, if interpreted similarly to the present invention, would dictate separate microchip sets for each piece of application software that would be tamperproof. This is not consistent with the economics of software and its distribution.

[0066] Under the present invention, the application contains a special code resource which knows about all the other code resources in memory. During execution time, this special code

resource, called a "memory scheduler," can be called periodically, or at random or pseudo random intervals, at which time it intentionally shuffles the other code resources randomly in memory, so that someone trying to analyze snapshots of memory at various intervals cannot be sure if they are looking at the same code or organization from one "break" to the next. This adds significant complexity to their job. The scheduler also randomly relocates itself when it is finished. In order to do this, the scheduler would have to first copy itself to a new location, and then specifically modify the program counter and stack frame, so that it could then jump into the new copy of the scheduler, but return to the correct calling frame. Finally, the scheduler would need to maintain a list of all memory addresses which contain the address of the scheduler, and change them to reflect its new location.

[0067] The methods described above accomplish the purposes of the invention--to make it hard to analyze captured memory containing application executable code in order to create an identifiable computer program or application that is different from other copies and is less susceptible to unauthorized use by those attempting to disable the underlying copyright protection system. Simply, each copy has particular identifying information making that copy different from all other copies.

[0068] Although various embodiments are specifically illustrated and described herein, it will be appreciated that modifications and variations of the present invention are covered by the above teachings and within the purview of the appended claims without departing from the spirit and intended scope of the invention.

WHAT IS CLAIMED IS:

1. A method for copy protection of digital information, the digital information including a digital sample and format information, comprising the steps of: identifying a portion of the format information to be encoded; generating encoded format information from the identified portion of the format information; and generating encoded digital information, including the digital sample and the encoded format information.

2. The method of claim 1, further comprising the step of requiring a predetermined key to decode the encoded format information.

3. The method of claim 2, wherein the digital sample and format information are configured to be used with a digital player, and wherein information output from the digital player will have a degraded quality unless the encoded format information is decoded with the predetermined key.

4. The method of claim 3, wherein the information output from the digital player represents a still image, audio or video.

5. The method of claim 3, wherein the information output represents text data to be authenticated.

6. A method for protecting a digital signal, the digital signal including digital samples in a file format having an inherent granularity, comprising the step of:

creating a predetermined key comprised of a transfer function-based mask set to manipulate data at the inherent granularity of the file format of the underlying digitized samples.

7. The method of claim 6, wherein the digital signal represents a continuous analog waveform.

8. The method of claim 6, wherein the predetermined key comprises a plurality of mask sets.

9. The method of claim 6, wherein the digital signal is a message to be authenticated.

10. The method of claim 6, wherein the mask set is ciphered by a key pair comprising a public key and a private key.

11. The method of claim 6, further comprising the step of:

using a digital watermarking technique to encode information that identifies ownership, use, or other information about the digital signal, into the digital signal.

12. The method of claim 6, wherein the digital signal represents a still image, audio or video.

13. The method of claim 6, further comprising the steps of:

selecting the mask set, including one or more masks having random or pseudo-random series of bits; and

validating the mask set at the start of the transfer function-based mask set.

14. The method of claim 13, wherein said step of validating comprises the step of: comparing a hash value computed at the start of the transfer function-based mask set with a determined transfer function of the hash value.

15. The method of claim 6, further comprising the steps of:

selecting the mask set, including one or more masks having random or pseudo-random series of bits; and

authenticating the mask set by comparing a hash value computed at the start of the transfer function-based mask set with a determined transfer function of the hash value.

16. The method of claim 13, wherein said step of validating comprises the step of: comparing a digital signature at the start of the transfer function-based mask set with a determined transfer function of the digital signature.

17. The method of claim 6, further comprising the steps of:

selecting the mask set, including one or more masks having random or pseudo-random

series of bits; and

authenticating the mask set by comparing a digital signature at the start of the transfer function-based mask set with a determined transfer function of the digital signature.

18. The method of claim 13, further comprising the step of:

using a digital watermarking technique to embed information that identifies ownership, use, or other infonnation about the digital signal, into the digital signal; and

wherein said step of validating is dependent on validation of the embedded information.

19. The method of claim 6, further comprising the step of:

computing a secure one way hash function of carrier signal data in the digital signal, wherein the hash function is insensitive to changes introduced into the carrier signal for the purpose of carrying the transfer function-based mask set.

20. A method for protecting a digital signal, the digital signal including digital samples in a file format having an inherent granularity, comprising the steps of:

creating a predetermined key comprised of a transfer function-based mask set that can manipulate data at the inherent granularity of the file format of the underlying digitized samples;

authenticating the predetermined key containing the correct transfer function-based mask set during playback of the data; and

metering the playback of the data to monitor content.

21. The method of claim 20, wherein the predetermined key is authenticated to authenticate message information.

22. A method to prepare for the scrambling of a sample stream of data, comprising the steps of:

generating a plurality of mask sets to be used for encoding, including a random primary mask, a random convolution mask and a random start of message delimiter;

obtaining a transfer function to be implemented;

generating a message bit stream to be encoded;

loading the message bit stream; a stega-cipher map truth table, the primary mask, the convolution mask and the start of message delimiter into memory;

initializing the state of a primary mask index, a convolution mask index, and a message

bit index; and

setting a message size equal to the total number of bits in the message bit stream.

23. A method to prepare for the encoding of stega-cipher information into a sample stream of data, comprising the steps of:

generating a mask set to be used for encoding, the set including a random primary mask, a random convolution mask, and a random start of message -delimiter;

obtaining a message to be encoded;

compressing and encrypting the message if desired;

generating a message bit stream to be encoded;

loading the message bit stream, a stega-cipher map truth table, the primary mask, the convolution mask and the start of message delimiter into memory;

initializing the state of a primary mask index, a convolution mask index, and a message bit index; and

setting the message size equal to the total number of bits in the message bit stream.

24. The method of claim 23 wherein the sample stream of data has a plurality of windows, further comprising the steps of:

calculating over which windows in the sample stream the message will be encoded;

computing a secure one way hash function of the information in the calculated windows, the hash function generating hash values insensitive to changes in the samples induced by a stega-cipher; and

encoding the computed hash values in an encoded stream of data.

25. The method of claim 13, wherein said step of selecting comprises the steps of:

collecting a series of random bits derived from keyboard latency intervals in random typing;

processing the initial series of random bits through an MD5 algorithm;

using the results of the MD5 processing to seed a triple-DES encryption loop;

cycling through the triple-DES encryption loop, extracting the least significant bit of each result after each cycle; and

concatenating the triple-DES output bits into the random series of bits.

26. A method for copy protection of digital information, the digital information including a digital sample and format information, comprising the steps of:

identifying a portion of the digital sample to be encoded;

generating an encoded digital sample from the identified portion of the digital sample; and

generating encoded digital information, including the encoded digital sample and the format information.

27. The method of claim 26, further comprising the step of requiring a predetermined key to decode the encoded digital sample.

28. The method of claim 27, wherein the digital sample and format information are configured to be used with a digital player. and wherein information output from the digital player will have a degraded quality unless the encoded digital sample is decoded with the predetermined key.

29. The method of claim 27, wherein information output will have non authentic message data unless the encode digital sample is decoded with the predetermined key.

30. A method for protecting digital data, where the digital data signal is organized into a plurality of frames, each frame having i) a header comprising file format information and ii) at least a portion of the digital data, said method comprising the steps of:

creating a predetermined key to manipulate the file format information in one or more of the plurality of frames; and

manipulating the file format information using the predetermined key in at least two of the plurality of frames, such that the digital data will be perceived by a human as noticeably altered if it is played without using a decode key to restore the file format information to a prior state.

31. The method of claim 30, wherein the predetermined key comprises a private key that is associated with a key pair.

32. A method for copy protection of software comprising: embedding the software with a

watermark wherein the embedded software operates in a manner substantially the same as the software prior to the embedding step.

33. The process of claim 32, wherein the step of embedding the software with a watermark increases the complexity of code analysis and/or tampering with the software.

34. The process of claim 32, wherein the watermarked software queries a user for personalization information during installation of the software.

35. The process of claim 32, wherein the watermark is accessible with a key.

36. The process of claim 35, wherein the key enables authorized use of the watermarked software.

37. The process according to claim 35, wherein the key and license information are interchangeable.

38. The process according to claim 32, wherein the step of embedding the software with a watermark is performed during execution of the software.

39. The process according to claim 32, wherein the step of embedding the software with a watermark modifies the structure of the software being embedded.

40. An article of manufacture comprising a machine readable medium, having thereon stored instructions adapted to be executed by a processor, which instructions when executed result in a process comprising: receiving potentially watermarked software; and identifying the software by extracting the watermark.

41. The article of manufacture of claim 40, wherein the watermark is associated with information fixed prior to distribution of the watermarked software.

42. The article of manufacture of claim 40, wherein the watermark affects functionality of the watermarked software.

43. The article of manufacture of claim 40, wherein the extracted watermark enables generation of a key.

44. The article of manufacture of claim 43, wherein the generated key and licensing information are associated.

45. The article of manufacture of claim 40, further comprising limiting functionality of the software if the watermark cannot be extracted.

46. A method for watermarking software comprising: determining the structure a plurality of code contained in the software; and configuring at least a portion of the plurality of code according to a watermarking process.

47. The process of claim 46, wherein the watermarking process further comprises inserting information into the software after installation.

48. The process of claim 46, wherein the watermarking process configures the at least a portion of the plurality of code according to a key.

49. The process of claim 46, wherein the watermarking process increases the complexity of code analysis and/or tampering with the software.

50. The process of claim 46, wherein the watermarking process is selected from the group comprising: data hiding, steganography or steganographic ciphering.

51. The process of claim 46, wherein the watermarking process is applied during execution of the software.

52. A system for copy protection of software comprising the steps of: associating license information with a copy of a software application; encoding the associated license information into the copy of the software application using a watermarking process; providing the copy of the software application having license information encoded therein to a user; and, comparing information received by a user with the encoded license information.

53. The system of claim 52, wherein the encoding is controlled by a key.

54. The system of claim 52, wherein the step of comparing the user supplied information with the encoded license information enables authorization of the software.

55. The system of claim 53, wherein the key is fixed prior to distribution of the software.

56. The system of claim 52, wherein the license information comprises code which affects functionality of the watermarked software.

57. The system of claim 52, wherein the watermarked software is resistant to code analysis and/or tampering.

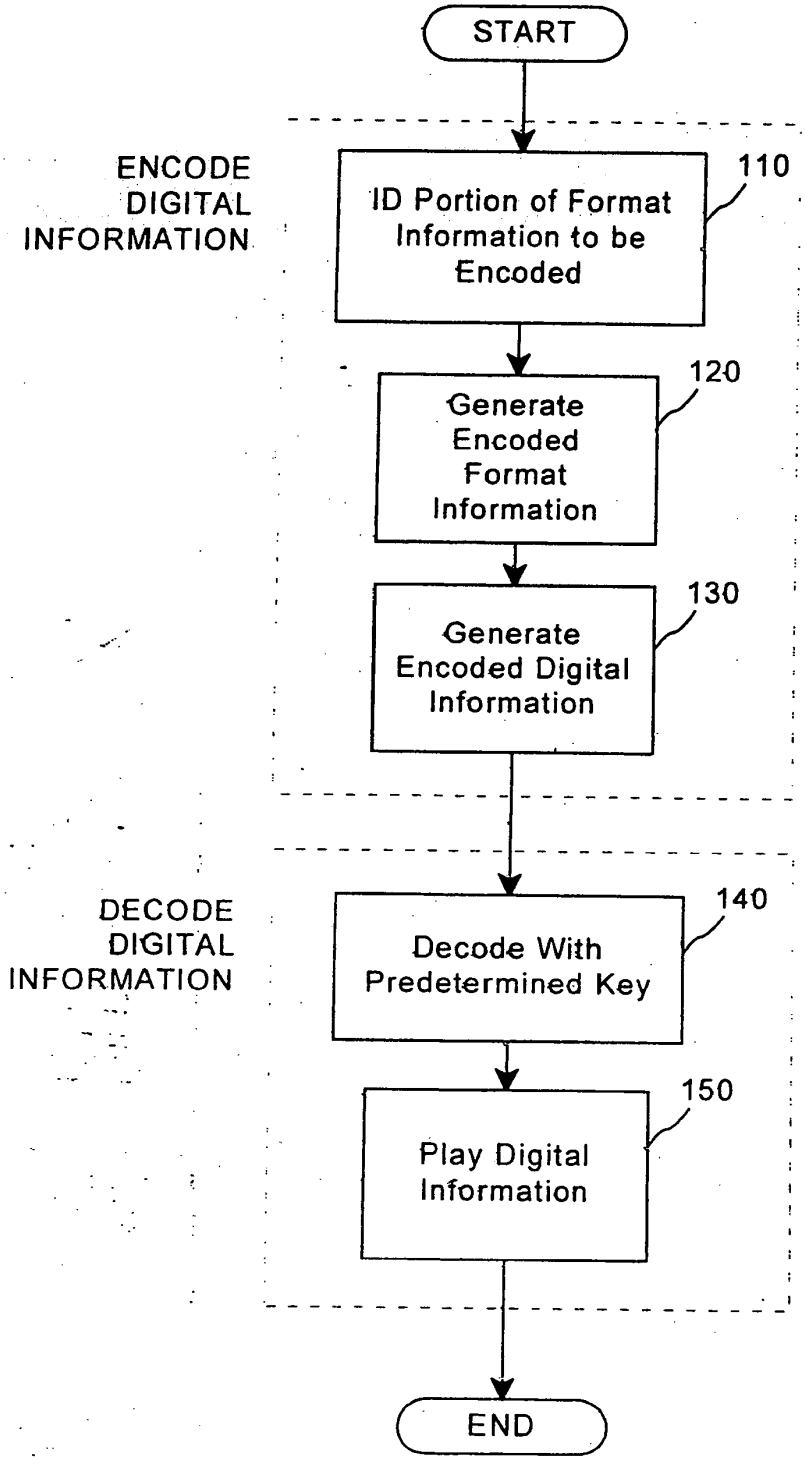

FIG. 1

DECLARATION FOR PATENT APPLICATION

As one of the below named inventors, I hereby declare that:

My residence, post office address and citizenship is as stated below next to my name;

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

DATA PROTECTION METHOD AND DEVICE

the specification of which: ☒ is attached hereto.
☐ was filed on:
as Application No.:
and was amended on: _____.

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to patentability as defined in 37 C.F.R. § 1.56.

Prior Foreign Application(s)

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below any foreign application(s) for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Country	Application Number	Date of Filing (day, month, year)	Date of Issue (day, month, year)	Priority Claimed	
				Yes ☐	No ☐
				Yes ☐	No ☐

Prior Provisional Application(s)

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

Application Number	Date of Filing (day, month, year)

Prior United States Application(s)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s), or § 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application Number	Date of Filing (day, month, year)	Status – Patented, Pending, Abandoned
10/602,777	June 25, 2003	Pending
09/046,627	March 24, 1998	Patent No. 6,598,162 July 22, 2003
08/587,943	January 17, 1996	Patent No. 5,745,569 April 28, 1998

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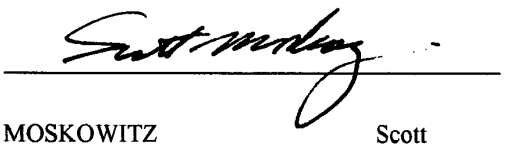
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. § 1001, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature		Date	AUGUST 1, 2007
Full Name of First Inventor:	MOSKOWITZ (Family Name)	Scott (First Given Name)	A. (Second Given Name)
Citizenship:	United States of America		
Residence:	16711 Collins Avenue, # 2505, Sunny Isles Beach, FL 33160		
Post Office Address:	16711 Collins Avenue, # 2505, Sunny Isles Beach, FL 33160		

ABSTRACT OF THE DISCLOSURE

An apparatus and method for encoding and decoding additional information into a digital information in an integral manner. More particularly, the invention relates to a method and device for data protection.

Electronic Patent Application Fee Transmittal	
Application Number:	
Filing Date:	
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott Moskowitz
Filer:	Bruce Talbot Margulies
Attorney Docket Number:	SCOT0014-6
Filed as Large Entity	

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Utility application filing	1011	1	380	380
Utility Search Fee	1111	1	620	620
Utility Examination Fee	1311	1	250	250
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Miscellaneous:				
		Total in USD ($)		**1250**

Electronic Acknowledgement Receipt

EFS ID:	13323484
Application Number:	13556420
International Application Number:	
Confirmation Number:	5811
Title of Invention:	Data protection method and device
First Named Inventor/Applicant Name:	Scott Moskowitz
Customer Number:	31518
Filer:	Bruce Talbot Margulies
Filer Authorized By:	
Attorney Docket Number:	SCOT0014-6
Receipt Date:	24-JUL-2012
Filing Date:	
Time Stamp:	13:02:05
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	$1250
RAM confirmation Number	12992
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)

1		ApplicationFiling_SCOT0014-6_7-22-2012c.pdf	563323 2dd9b4c59c36d490ac9fca9506475bc10a288645	yes	41

	Multipart Description/PDF files in .zip description		
	Document Description	**Start**	**End**
	Transmittal of New Application	1	2
	Preliminary Amendment	3	6
	Specification	7	28
	Claims	29	36
	Abstract	37	37
	Drawings-only black and white line drawings	38	38
	Oath or Declaration filed	39	41

Warnings:

Information:

2	Fee Worksheet (SB06)	fee-info.pdf	32740 ada978274a62d17c1d68397b0d4f26991a003e78	no	2

Warnings:

Information:

Total Files Size (in bytes):	596063

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.