

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

GOOGLE, INC.
Petitioner,

v.

UNILOC USA, INC.
Patent Owner

Case IPR2017-01665
U.S. Patent 8,566,960

DECLARATION OF DR. VAL DI EULIIS

OCTOBER 18, 2017

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I, Dr. Val DiEuliis, hereby declare and state as follows:

1. Introduction

1. My name is Val DiEuliis, and I have been retained by Uniloc USA, Inc. (“Uniloc” or the “Patent Owner”). My client Uniloc and its associated counsel, Etheridge Law Group, have asked me to study U.S. Patent No. 8,566,960 (“the ’960 patent”), the Petition, the proffered prior art in this case, and other relevant documents. I document my findings in this declaration.

2. I have concluded that U.S. Patent No. 7,047,411 (“DeMello”) [EX1004], alone or in view of Irish Patent Application IE 02/0429 (“Staruiala”) [EX1005] and U.S. Patent No. 7,962,424 (“Colosso”) [EX1006], does not render obvious any challenged claim of the patent at issue, the ’960 patent, at least for the following reasons:

- The proposed combinations of prior art do not disclose “verify that a license data associated with the digital product is valid based at least in part on a device identity generated by sampling physical parameters of the given device”
- A POSITA on the priority date would not have been motivated to combine Colosso with DeMello and/or Staruiala because Colosso and DeMello disclose significantly different, and mutually exclusive, approaches to the licensing problem.

- Even if combined, the proposed combinations do not disclose “in response to the device identity not being on the record, set the allowed copy count to a first upper limit for a first time period, the allowed copy count corresponding to a maximum number of devices authorized to use the digital product”

3. The limited scope of my opinions and analysis in this declaration do not imply that I may not later express other opinions or report other results from other investigations concerning other issues raised by the Petitioner or its experts in this IPR.

2. Qualifications

4. I am an electrical engineer with over 45 years of experience developing, programming, and analyzing computer algorithms and software. I am experienced with and able to create, read, and interpret firmware and software in C, C++, Java, assembly language, HTML, and other computer programming languages. I have served as an expert witness in multiple cases for which I analyzed computer source code in various languages and testified at ITC hearings and two jury trials concerning my results.

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