

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

VIZIO, INC.,
Petitioner,

v.

BROADCOM CORPORATION,
Patent Owner.

Case IPR2018-00786
Patent 7,590,059 B2

Before JEAN R. HOMERE, RAMA S. ELLURU, and
DANIEL J. GALLIGAN, *Administrative Patent Judges*.

HOMERE, *Administrative Patent Judge*.

DECISION
Granting Institution of *Inter Partes* Review
35 U.S.C. § 314

I. INTRODUCTION

Vizio Inc. (“Petitioner”) filed a Petition requesting *inter partes* review of claims 11–20 (“the challenged claims”) of U.S. Patent No. 7,590,059 B2 (Ex. 1001, “the ’059 patent”). Paper 1 (“Pet.”). Broadcom Corporation (“Patent Owner”) filed a Preliminary Response. Paper 6 (“Prelim. Resp.”). Pursuant to 37 C.F.R. § 42.4(a), we have the authority to determine whether to institute review.

Under 35 U.S.C. § 314(a), an *inter partes* review may not be instituted unless the information presented in the petition and the preliminary response “shows that there is a reasonable likelihood that the petitioner would prevail with respect to at least 1 of the claims challenged in the petition.” For the reasons that follow, we determine that, on this record, Petitioner has established a reasonable likelihood that it would prevail with respect to the challenged claims. Accordingly, we institute an *inter partes* review as to the challenged claims.

A. *Related Matters*

Petitioner indicates that the ’059 patent is involved in *Broadcom Corp v. VIZIO, Inc.*, Civil Action No. 8:17-cv-00408 (C.D. Cal.) and *In re Certain Semiconductor Devices and Consumer Audiovisual Products Containing the Same*, Investigation No. 337-TA-1047 (USITC). Pet. 1. Petitioner further indicates that the ’059 patent is also involved in certain other matters. *Id.* at 1–2.

B. The '059 Patent

The '059 patent relates to multi-standard video decoder chip 300 for selecting a suitable decoding process based on an identified one of a plurality of encoding types associated with packets separated by delimiters within a received encoded video stream. Ex. 1001, 2:38–51, Fig. 3A. Specifically, as depicted in Figure 3A below, the '059 patent discloses decoder chip 300 includes memory block 301 for storing elementary video stream 309 from which code-in-port (CIP- 305) acquires a start code, which stream parser 307 processes and forwards to processing block 303 to generate information necessary for subsequent decoding of the video stream. *Id.* at col. 6:9–52.

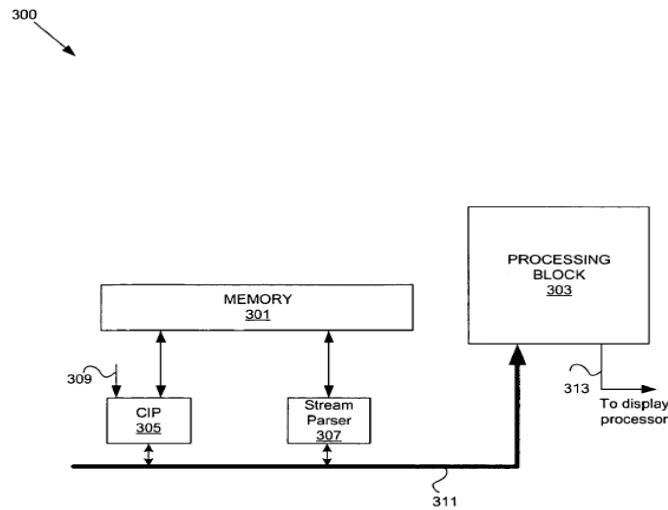


FIG. 3A

Figure 3A depicts a block diagram of the multi-standard video decoder.

More particularly, as illustrated in Figure 1 below, the '059 patent discloses that the encapsulated payload within each received packet 100 includes elementary video stream data 105, and delimiter 104. *Id.* at 5:4–33. Delimiter 104 contains start code 101 indicating the beginning of the packet, and start code suffix 103, which may indicate the particular encoding standard (e.g., MPEG-1, MPEG-2, MPEG-4H.264, VC-1) associated with the packet. *Id.*

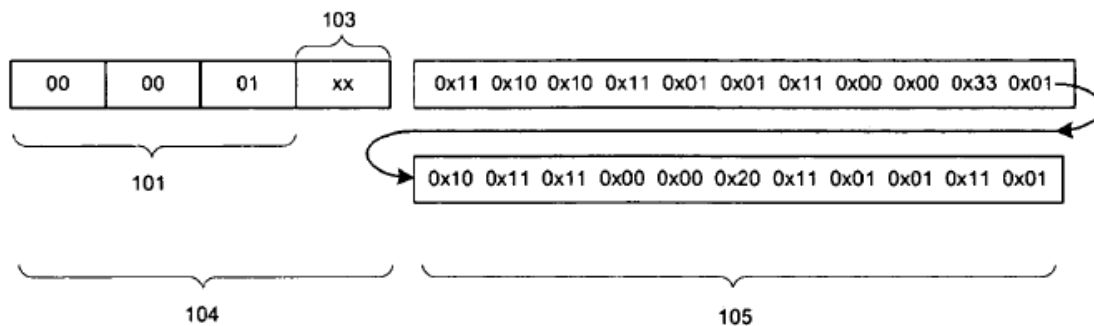


Figure 1 depicts the block diagram of encapsulated video payload 100 with a delimiter.

According to an embodiment of the '059 patent all the steps in the decoding process shown in Figure 9 below are performed by a processor on the chip. *Id.* at 3:13–25. In particular, upon receiving the packetized data on the chip, the processor examines delimiter 104 to identify start code 101 as well as identifier 103 so as to identify one or more encoding types associated with packets in the video stream. *Id.* at 18:29–44, Fig. 9. The processor

subsequently selects a suitable one of a plurality of decoding processes based on the identified encoding process to decode the packetized data. *Id.*

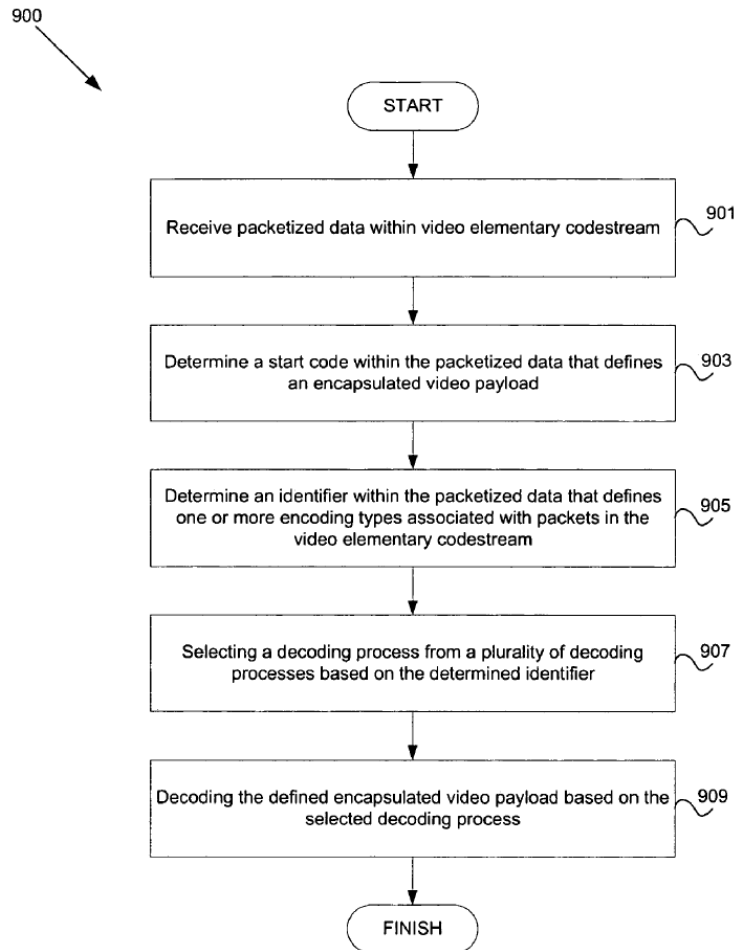


FIG. 9

Figure 9 depicts a method for decoding an encoded video stream.

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