

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SAMSUNG ELECTRONICS CO., LTD. AND SAMSUNG ELECTRONICS
AMERICA, INC.
Petitioner

v.

INVENSAS CORPORATION
Patent Owner

U.S. Patent No. 6,825,554

**PETITION FOR *INTER PARTES* REVIEW
OF U.S. PATENT NO. 6,825,554**

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1001	U.S. Patent No. 6,825,554 (“the ’554 Patent”)
1002	Declaration of Peter Elenius
1003	Curriculum Vitae for Peter Elenius
1004	Prosecution File History of U.S. Patent No. 6,825,554
1005	Schaper et al., “Electrical Characterization of the Interconnected Mesh Power System IMPS MCM Topology,” IEEE Transactions on Components, Packaging, and Manufacturing Technology, Vol. 18, No. 1, February 1995 at pp. 99-105 (“Schaper”)
1006	U.S. Patent Application Publication No. 2002/0118528 (“Su”)
1007	U.S. Patent No. 6,125,042 (“Verdi”)
1008	U.S. Patent No. 6,359,341 (“Huang”)
1009	Simon S. Ang et al., “A low-cost, flexible ball-grid-array multichip module technology,” Proc. SPIE 3184, August 1997 at pp. 13-21 (“Schaper II”)
1010	Davidson and Katopis, Microelectronics Packaging Handbook, Chapter 3, 1989.
1011	Microelectronics Packaging Handbook, 696 (Rao Tummala and Eugene Rymaszewski eds., 1989)
1012	Semiconductor Packaging A Multidisciplinary Approach, 29-30 (Robert Hannemann, et al., eds., 1994)
1013	Ball Grid Array Technology, 370-377 (John Lau, ed., 1995)
1014	Declaration of Sylvia D. Hall-Ellis
1015	Curriculum Vitae for Sylvia D. Hall-Ellis
1016	IEEE Xplore Bibliographic Record For Schaper
1017	SPIE Bibliographic Record For Schaper II
1018	Ball Grid Array Technology, 279-280 (John Lau, ed., 1995)
1019	Huang, et al., “CBGA Package Design for C4 PowerPC Microprocessor Chips: Trade-off between Substrate Routability and Performance” (IEEE 1994)
1020	Electronic Packaging and Interconnection Handbook, 7.52 (Charles Harper, ed., 2000)
1021	Tsai, “Inductance and SSN Performance Comparison of a 225 Plastic BGA and a 208 Plastic QFP” (1996)
1022	T. Chang, et al., “Parasitic characteristics of BGA packages,” 124-129 (IEEE 1998)
1023	Citation to T. Chang, et al., “Parasitic characteristics of BGA

packages,” 124-129 (IEEE 1998)

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