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# (54) DYNAMIC MEMORY SIZING FOR POWER

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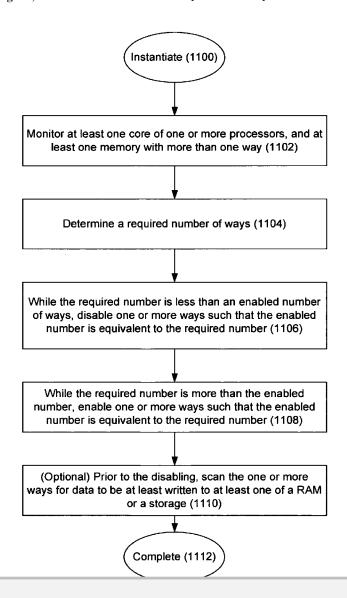
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**ABSTRACT** (57)

Systems and methods of dynamic memory for power reduction are described with respect to a memory with a coupled sleep device. In one embodiment, the operating requirements can reflect amount of memory required to perform commensurate operations. Memory power management logic is used to coordinate memory requirements with operating requirements. The sleep device is able to enable or disable the memory based on the requirements to reduce power consumption.





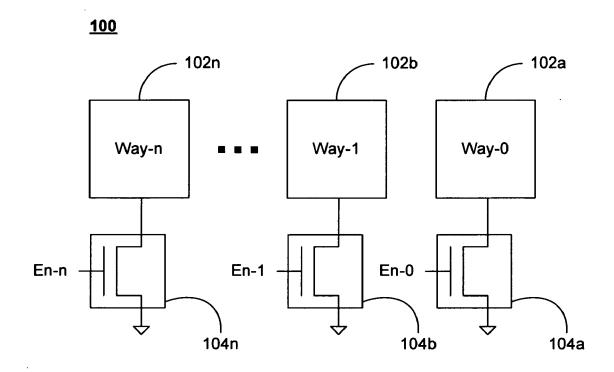


FIG. 1

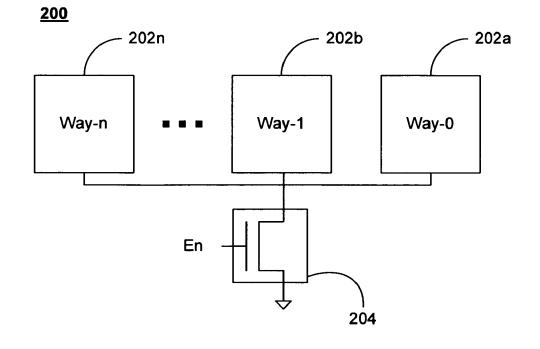
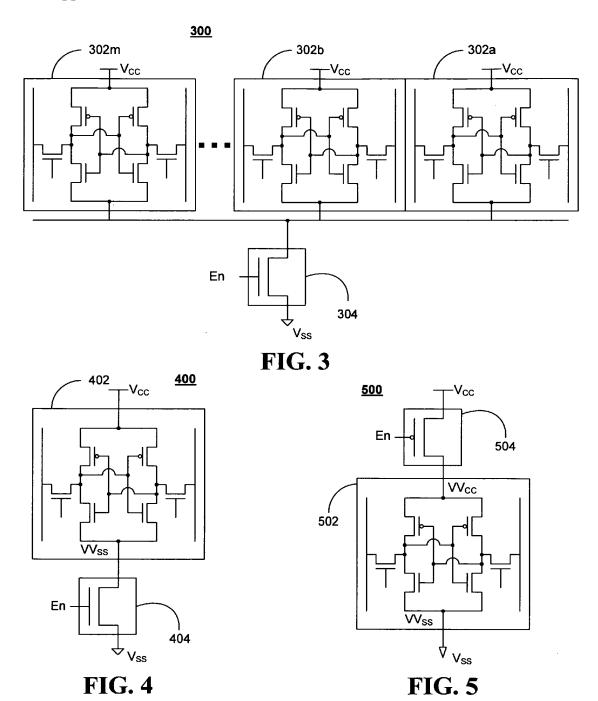
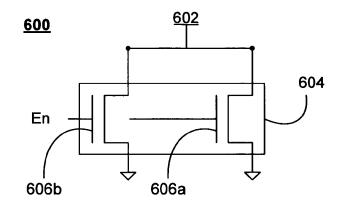
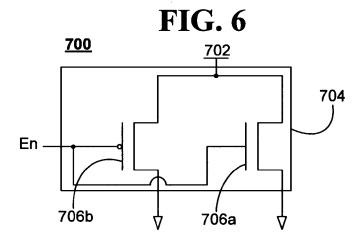


FIG. 2







**FIG.** 7

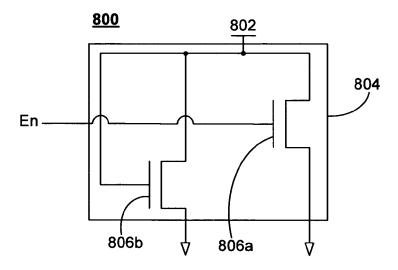


FIG. 8





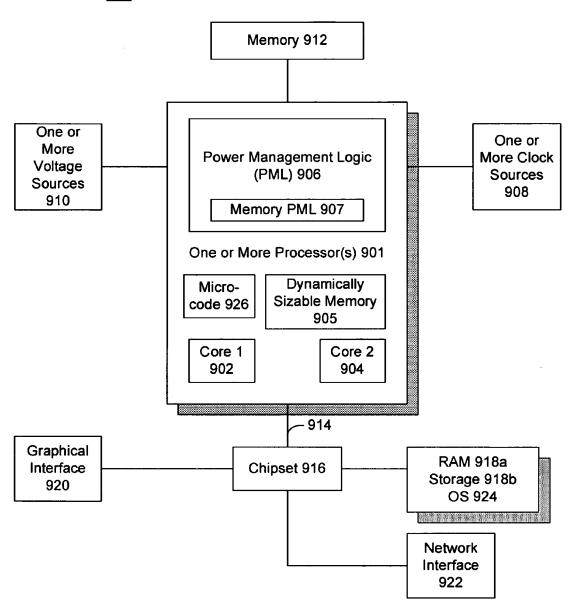


FIG. 9

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