

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

INTEL CORPORATION,
Petitioner,

v.

VLSI TECHNOLOGY LLC,
Patent Owner.

Case IPR2019-01196
U.S. Patent No. 7,246,027

**PATENT OWNER'S MANDATORY NOTICE
PURSUANT TO 37 C.F.R. § 42.8**

Mail Stop: PATENT BOARD
Patent Trial and Appeal Board
U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

Pursuant to 37 C.F.R. § 42.8, Patent Owner VLSI Technology LLC (“Patent Owner”) hereby submits the following Notice.

I. Real Party-In-Interest

The real parties-in-interest are VLSI Technology LLC and CF VLSI Holding LLC.

II. Related Matters Patent Owner identifies the following matters that may be deemed related pursuant to 37 C.F.R. § 42.8(b)(2):

- VLSI Technology LLC (“VLSI”) has asserted U.S. Patent No. 7,246,027 in *VLSI Technology LLC v. Intel Corporation*, C.A. No. 18-966-CFC (D. Del.)

III. Lead and Back-Up Counsel

LEAD COUNSEL	BACK-UP COUNSEL
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IPR2019-01196
Patent No. 7,246,027

IV. Service Information

Please address all correspondence to the lead and back-up counsel as shown above. The Patent Owner consents to electronic service to VLSIprs@irell.com.

Dated: July 18, 2019

Respectfully submitted,

*/Hong Zhong/*_____

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CERTIFICATE OF SERVICE

Pursuant to 37 C.F.R. § 42.6, the undersigned certifies that on July 18, 2019, a copy of the foregoing document **PATENT OWNER'S MANDATORY NOTICE** was served by electronic mail upon the following:

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