

TCL'S INVALIDITY CONTENTIONS FOR U.S. 8,713,206
Exhibit O5: Prior Art References Under 35 U.S.C. § 103

To the extent that Plaintiff asserts that any reference charted in these Invalidity Contentions does not disclose a given claim element, it would have been obvious to combine such reference with the knowledge of a person of ordinary skill in the art. The references identified herein as disclosing the element, Applicant's Admitted Prior Art, and/or the additional prior art references in Exhibits E1-E14, the contents of which are hereby incorporated by reference into this chart. One of ordinary skill in the art would have been motivated to, and would have understood how to, combine any of these disclosures based on the background knowledge of a person of ordinary skill in the art and the teachings from these references, as well as the exemplary motivations identified in the Invalidity Contentions.

To the extent 35 U.S.C. § 112, ¶6 applies to any of the claim limitations of the Asserted Claims, the art cited herein discloses the corresponding structure(s) and function(s) claimed or their equivalents, as shown below, or renders them obvious to a person of ordinary knowledge of one skilled in the art.

'206 Claim	Claim Element	Obviousness Combinations ¹²
1.pre	A display control apparatus comprising:	To the extent that the preamble is construed as a limitation, this element was well-known as of the priority date of the '206 patent. <i>See</i> , element 1.pre in Exhibits E1-E14 <i>See, e.g.</i> , elements 1.a – 1.c.
1.a	a communication unit configured to communicate with an external device; and	This element was well-known as of the priority date of the '986 patent. <i>See</i> , element 1.a in Exhibits E1-E14. <u>U.S. Patent No. 7,577,766 (“Ono”):</u> 3:33-37 (“FIG. 1 shows a configuration of an essential part of the inkjet recording apparatus to the embodiment of the present invention. In FIG. 1, the inkjet recording apparatus includes a recording body 101 and a CPU (central processing unit) 102 controlling the inkjet recording”).

¹ Defendants incorporate herein any related work performed by or on behalf of an author or an inventor in the United States under 35 U.S.C. § 103. This invalidity chart is not to be considered final. Defendants reserve the right to supplement and amend this chart on further investigation and ongoing discovery.

² Except where specifically noted otherwise, this chart may apply the apparent interpretations of claim language as used by Plaintiff in its invalidity contentions. Such use, however, does not imply that Defendants adopt or agree with Plaintiff's interpretations in any way. Additionally, in the invalidity contentions for claim preamble elements, Defendants do not take a position on whether the preamble is a claim limitation.

'206 Claim	Claim Element	Obviousness Combinations ¹²
		<p>3:58-67 (“The USB host controller 105 operates as a USB host and communication device connected to a USB connector (type-A) 108 so as to transmit/receive data.”)</p> <p>In FIG. 1, the digital camera 301 and the USB memory 401 are shown as example devices connected to the USB connector 108.</p> <p>Typically, in a printer capable of performing direct printing from this type of digital image driver of only a still image class may be provided for a path between the USB controller and the USB host controller 105.”)</p> <p style="text-align: center;">FIG. 1</p> <p>JP 2007206408A (“Ariga”):</p> <p>¶[0021] (“The projector 10 receives an image signal or the like from a notebook computer or the like via a cable 30 and projects an image based on the image signal.”)</p>

'206 Claim	Claim Element	Obviousness Combinations ¹²
		<p>3:10-14 (“Both the computing and auxiliary screen unit 10, 20 include input/output enable operable interconnection between. This interconnection may be by means of USB, by wireless connection such as “Bluetooth”, or IEEE802.11 Wireless E</p> <p>U.S. Patent Pub. No. 2006/0242362 (“Hanes”):</p> <p>¶[0017] (“The chipset 14 is coupled to the I/O bus 24. The I/O bus 24 serves as a pathway for signals from the chipset 14 to I/O devices 26, 27, 28, 30, 32, and 26, 27, 28, 30, 32, and 34 include external data storage devices, such as an external drive 26, an external data storage device 27, or a flash memory drive 28, as well as I/O devices, such as a mouse 30, a video display 32, or a keyboard 34. The external data storage device 27 may comprise any suitable type of external data storage device, such as a camera, a memory card reader, that can be coupled to the computer system 10.”); <i>see also</i> F</p> <p>¶[0018] (“In one embodiment, the I/O bus 24 employs any one of a number of protocols to communicate with the I/O devices 26, 27, 28, 30, 32, and 34. External data storage devices communicate with the computer using an interface protocol. These interface protocols permit the computer to exchange information with the external data storage devices. The communication protocols include, but are not limited to, universal serial bus (USB), serial advanced technology attachment (“S-ATA”), IEEE-1394, small computer system interface (“SCSI”), integrated drive electronics (“IDE”), fiber channel, gigabit Ethernet, and other protocols. Various types of communication protocols can be implemented in the computer system 10. In an alternate embodiment, the I/O bus 24 is integrated into the chipset 14.”); <i>see also</i> ¶[0023].</p> <p>High-Definition Multimedia Interface Specification Version 1.3a (“HDMI Specification”):</p> <p>1 (“This document constitutes the specification for the High-Definition Multimedia Interface version 1.3a. The High-Definition Multimedia Interface is provided for transmitting and receiving television audiovisual signals from DVD players, set-top boxes and other audio and video equipment to television sets, projectors and other video displays. HDMI can carry high quality digital audio data and can carry all standard and highdefinition consumer electronics video data. HDCP (High Definition Content Protection) technology is available. HDMI can also carry control and status information in both directions.”)</p>

'206 Claim	Claim Element	Obviousness Combinations ¹²
		<p>8 (“HDMI carries a VESA DDC channel. The DDC is used for configuration and control between a single Source and a single Sink. The optional CEC protocol provides high level control functions between all of the various audiovisual products in a user’s environment.”)</p> <p>10 (“A device’s external HDMI connection shall be presented via one of the three connector types, Type A, Type B or Type C. This connector can be attached directly to the device or attached via a cable adapter that is shipped with the device.”)</p> <p><u>U.S. Patent no. 9,110,624 (“Herz ’624”):</u></p> <p>2:40-48 (“In one embodiment, the present invention is implemented as a method of controlling output to a display device (e.g., LCD display, projection TV display, plasma display, etc.) <u>The method includes detecting a display device connection change (e.g., connection disconnection) on an output connector (e.g., HDMI, DVI, and the like) and in response, the settings (e.g., resolution and/or refresh rate) are changed to a compatible output standard (e.g., NTSC standard 480i, etc.) on the connected output connector.</u>”); <i>see also</i> 7:4-7:10.</p> <p><u>U.S. Patent No. 5,027,269 (“Grant”):</u></p> <p>4:35-42 (“In the communication services component 30, a session control block (32) and memory 34 for recording the session state information are maintained. Address space 10 for recovery (alternate) instance for Lux. The connection labeled 22 in FIG. 1A depicts the connection appearance in address space 10 when processing is proceeding normally.”); <i>see also</i> 1B.</p> <p>4:52-59 (“Depending on the type of recovery, address space 20 can be viewed as a local instance of Lux in address space 10, as another address space that contains a local alternate, or as another host system. In the latter case, the connection between the communication services component and address space that contained the active logical unit requires a communication access method, such as a bus, or high speed link.”).</p> <p><u>U.S. Patent No. 6,442,573 (“Schiller”):</u></p> <p>8:45-50 (“For example, each frame device is programmed to automatically configure itself upon obtaining input form the user. So long as the frame device is connected to a power source and a communication source it may remain operational without obtaining input from the user.”); 9:13-29.</p>

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