United States Patent [19]

Crump et al.

[54] COMPUTER SYSTEM HAVING MULTI-LEVEL SUSPEND TIMERS TO SUSPEND FROM OPERATION IN ATTENDED AND UNATTENDED MODES

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- [21] Appl. No.: 477,857
- [22] Filed: Jun. 7, 1995
- [51] Int. Cl.⁶ G06F 1/32

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[11] Patent Number: 5,752,044

[45] Date of Patent: May 12, 1998

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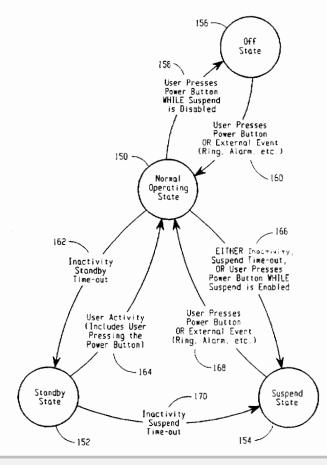
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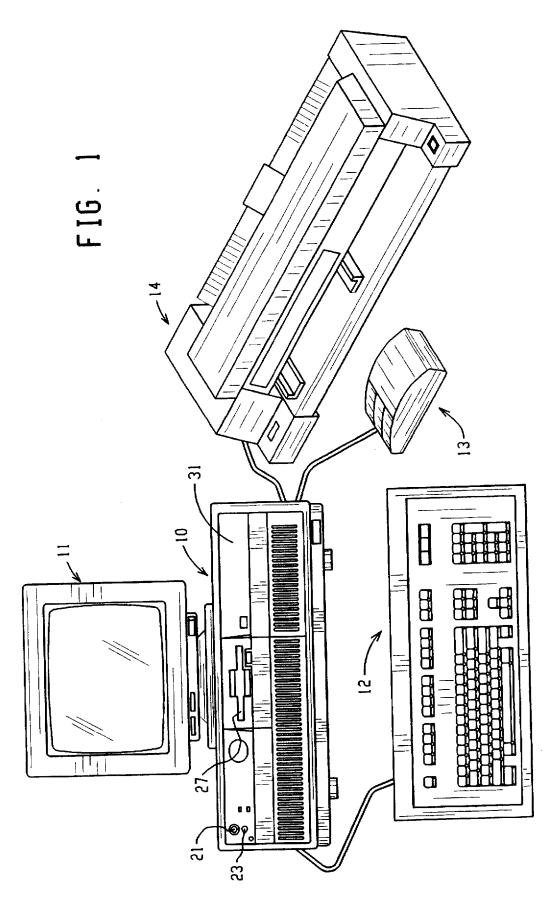
[57] ABSTRACT

A computer system having suspend and resume capabilities using multiple suspend timers. The multiple suspend timers execute such that the computer system suspends after a longer period of time of user inactivity when the system is powered up in an attended mode and suspends after a shorter period of time of inactivity when the system is powered up in an unattended mode. If the system awakens in an unattended mode and user activity is subsequently detected, the system changes to suspend after the longer period of time of user inactivity. The multiple timers are implemented either by a single timer restarted to two different values or by two independent timers having different expiration values.

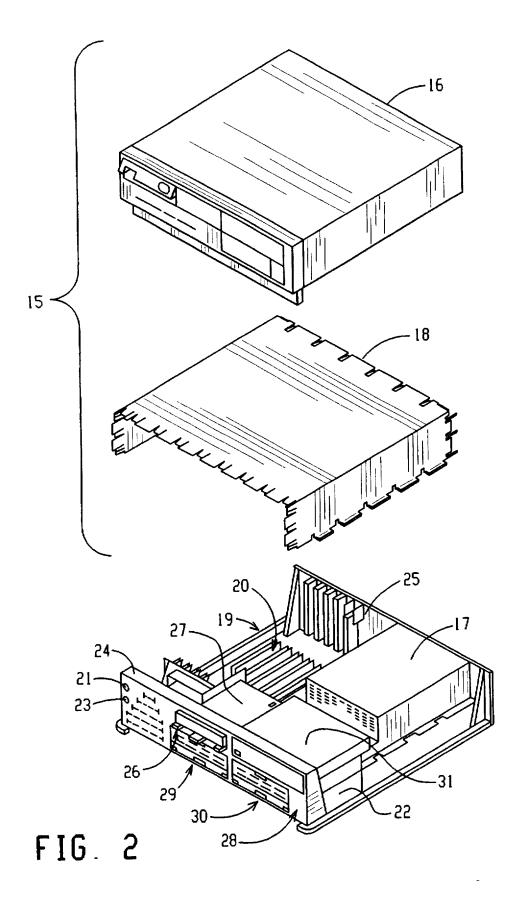
27 Claims, 57 Drawing Sheets

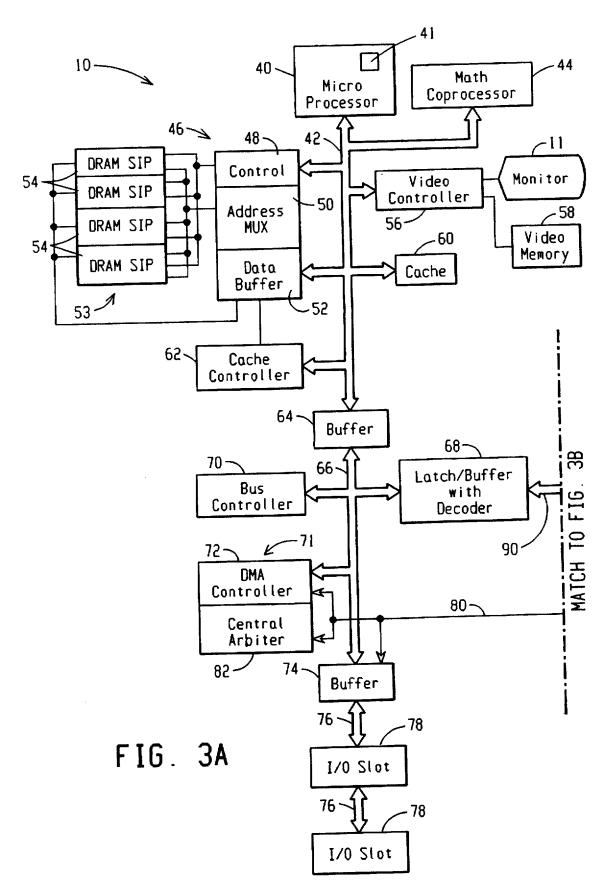


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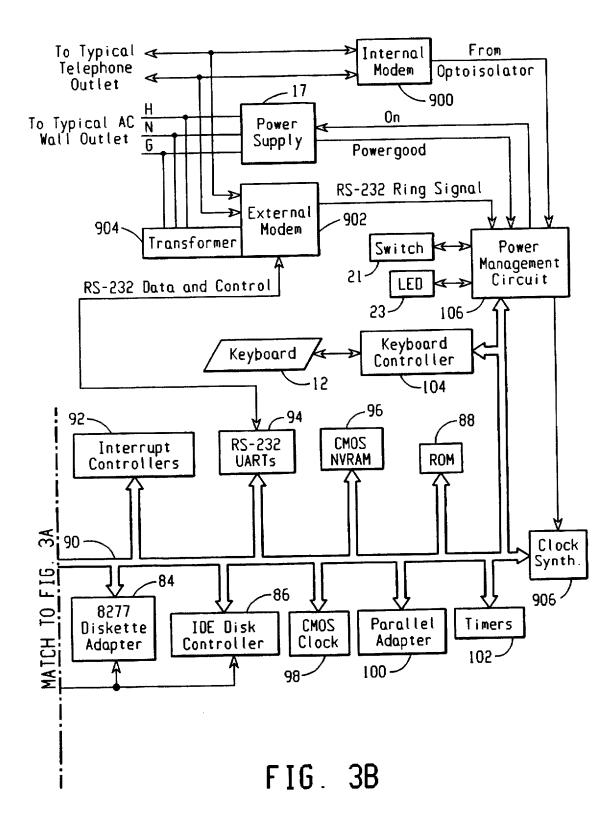


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