



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO., EXAMINER, ART UNIT, PAPER NUMBER, NOTIFICATION DATE, DELIVERY MODE. Includes application details for Jun Haishima and examiner information.

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

patentdocket@oblon.com
oblonpat@oblon.com
jgardner@oblon.com

DETAILED ACTION

1. Claims 1-3 are pending.
2. The examiner has acknowledged the applicant's priority to Japanese Patent Application No. 2004-245377 filed on August, 25, 2004.

Specification

3. The applicant's specification appears to be in proper order. However, it has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of that the applicant may become aware of in the specification.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 and 2 are rejected under 35 U.S.C. 102(b) as being anticipated by Owada et al (Odwada) (United States Patent NO.5860122).

As Per claim 1 Odwada teaches:

- ***An information process device comprising:
a first memory device for storing a boot program executed when the
information process device is started to operate; a mother board on which
the first memory device is provided;***(Odwada, Col 2 lines 35-42, "According to a first aspect of the present invention, a backup unit comprises a first storage means containing at least one program and a setup file in which the operating

environment of this program is written) Wherein the first storage means is the first memory device as claimed and the setup file is the program which is executed. Furthermore a motherboard is an inherent feature of all computers;

- ***a second memory device for storing an application program, the second memory device being connected to the mother board;*** (Odwada, Col 1 lines 5-7, The present invention relates to a backup unit having a countermeasure against a fault in an external storage unit, such as a hard disk drive ...) Wherein the harddrive is the second memory device as claimed;
- ***a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device; wherein the fault inspection program is stored in the first memory device;*** (Odwada, Col 3 lines 5-8, According to a fourth aspect of the present invention, the above-mentioned backup unit further comprises a fault detecting means for detecting a fault in the first storage means.);
- ***and wherein the control device executes the fault inspection program when the information process device is started to operate.*** (Odwada, Col 3 lines 9-11, When a fault is detected in the first storage means by the fault detecting means, the switch means selects the second storage means as a storage means for booting) Wherein the fact that it detected the problem during booting must mean that it is running when the device is started.

As Per claim 2 claim 1 is incorporated and further Odwada teaches:

- ***wherein the first memory device is a ROM provided on the mother board, wherein the second memory device is a hard disk which is independent from the mother board, and wherein the control device executes the fault***

inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk. (Odwada, Col 8 lines 29-41 In the first and second embodiments of the invention, the duplication and conversion unit 31, the system loading switch unit 32, the setup file correction detector 33, and the HDD fault detector 34 are implemented by software, i.e., programs loaded to the central control unit 30. However, these constituents may be implemented by hardware. Further, when these constituents are implemented by software, their programs may be stored in the HDD 10. In addition, a program for generating the system loading switch unit 32 may be stored in a ROM in the information processor.)

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Owada et al (Odwada) (United States Patent NO.5860122) in view of Pascal et al (Pascal) (United States Patent NO. 5971851).

As Per claim 3 claim 1 is incorporated and Odwada does not specifically teach that the information process device is utilized as a gaming machine for business use.

However the analogous art of Pascal teaches the above limitation. (Pascal Col 1 lines 11-15, Game machines ... However, these machines occasionally malfunction and enter into a lock up state, ceasing all functions and leaving the player stranded.).

Therefore it would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teaching of Pascal into the invention of

Owada to create a fault tolerant gaming machine, because one of ordinary skill would have recognized the benefit of preventing players from having to " either abandon the machine after a few attempts at "fixing" the machine, or wait for an attendant's assistance." (Pascal Col 1 lines 15-18).

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Bradford A. Rodgers-Farmer whose telephone number is (571) 270-3888. The examiner can normally be reached on Monday to Thursday 7:30am to 5:00 pm EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Cameli Das can be reached on 571-272-3696. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Application/Control Number: 11/205,121
Art Unit: 4141

Page 6

/Bradford A Rodgers-Farmer/
Examiner, Art Unit 4141
March 25, 2008

/CHAMELI C. DAS/
Supervisory Patent Examiner, Art Unit 4141

Notice of References Cited	Application/Control No. 11/205,121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN	
	Examiner Bradford A. Rodgers-Farmer	Art Unit 4141	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,860,122 A	01-1999	Owada et al.	711/162
*	B	US-5,971,851 A	10-1999	Pascal et al.	463/24
*	C	US-6,449,735 B1	09-2002	Edwards et al.	714/25
*	D	US-2004/0078697 A1	04-2004	Duncan, William L.	714/042
*	E	US-2007/0168738 A1	07-2007	Wang, Chien-Chih	714/036
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			


FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	


*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

<i>Index of Claims</i> 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner Bradford A Rodgers-Farmer	Art Unit 4141

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

Claims renumbered in the same order as presented by applicant
 CPA
 T.D.
 R.1.47

CLAIM		DATE									
Final	Original	03/25/2008									
	1	✓									
	2	✓									
	3	✓									

Search Notes 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner Bradford A Rodgers-Farmer	Art Unit 4141

SEARCHED			
Class	Subclass	Date	Examiner
714	36	3/25/2008	BARF
710	104	3/25/2008	BARF

SEARCH NOTES		
Search Notes	Date	Examiner
Classes 711/170 and 713/1-100 searched using text	3/25/2008	BARF
Plus searche requested and concidered	3/25/2008	BARF
Google scholar searched	3/25/2008	BARF
IEEE searched	3/25/2008	BARF
East searched using EPO: JPO: Dewert; USPAT; USPgpub	3/25/2008	BARF

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner


UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIB DATA SHEET
CONFIRMATION NO. 2910

SERIAL NUMBER	FILING or 371(c) DATE RULE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.	
11/205,121	08/17/2005	714	4141	276900US90	
APPLICANTS Jun Haishima, Tokyo, JAPAN; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN 2004-245337 08/25/2004 ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/02/2005					
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and /BRADFORD A Acknowledged RODGERS FARMER/ Examiner's Signature	<input type="checkbox"/> Met after Allowance _____ Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 3	TOTAL CLAIMS 3	INDEPENDENT CLAIMS 1
ADDRESS OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314 UNITED STATES					
TITLE Information process device					
FILING FEE RECEIVED 1000	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		



Web Images Video News Maps more »

game machine fault detection

Search

Advanced Scholar Search
Scholar Preferences
Scholar Help

Scholar All articles - Recent articles Results 1 - 10 of about 13,300 for game machine fault detection. (0.18 seconds)

All Results

M Yannakakis

D PELED

R Finkel

R Alur

U Manber

Progressive gaming control and communication system - all 3 versions »

DN Hilgendorf, PE Pajak - US Patent 5,249,800, 1993 - Google Patents
... said **fault detection**. 30. ... unit indicating that said current unit over said connect
line indicating that the **game ... machine** is connected to said communication unit ...
[Cited by 21](#) - [Related Articles](#) - [Web Search](#)

Distinguishing tests for nondeterministic and probabilistic machines - all 5 versions »

R Alur, C Courcoubetis, M Yannakakis - Proceedings of the twenty-seventh annual ACM symposium on
..., 1995 - portal.acm.org
... two-player **games** with incomplete information. A two-player Yv-**game** G consists of
a nondeterministic **machine** with **game**-positions S, inputs I, out- ...
[Cited by 39](#) - [Related Articles](#) - [Web Search](#)

Testing, Optimizaton, and Games - all 10 versions »

M Yannakakis - Automata, Languages And Programming: 31st International ..., 2004 - books.google.com
... A deterministic FSM (or Mealy **machine**) M consists of ... Each state diagram defines an
instance of the **game**. ... for the extent of the **fault detection** capabilities of ...
[Cited by 9](#) - [Related Articles](#) - [Web Search](#)

Shooting game machine - all 3 versions »

K Suzuki - US Patent 5,366,229, 1994 - Google Patents
... output an on-target **detection** signal to the **game** arith- ... meanS; it is possible to
make the whole **game machine** ... would occur no **fault** resu lting from any mis ...
[Cited by 32](#) - [Related Articles](#) - [Web Search](#)

The Silicon Gaming Odyssey slot machine - all 4 versions »

A Levinthal, M Barnett - Compeon'97. Proceedings, IEEE, 1997 - ieeeexplore.ieee.org
... Page 3 Navigator, **Games**, Stoppage, MMS Application ... requirements of casino gaming
machines, including high reliability and security, **fault detection** and recovery ...
[Cited by 7](#) - [Related Articles](#) - [Web Search](#)

BLACK BOX CHECKING - all 10 versions »

D PELED, M YANNAKAKIS - Formal Methods for Protocol Engineering and Distributed ..., 1999 - books.
google.com
... He also posed the **fault detection** or conformance testing ... we want to check whether
this **machine** deadlocks, namely ... modeling the problem as a **game** with incomplete ...
[Cited by 57](#) - [Related Articles](#) - [Web Search](#)

An expert system approach to fault diagnosis in hydraulic systems

C Angeli, A Chatzirikolaou - Expert System, 1995 - Blackwell Synergy
... produce a useful tool for **fault diag-** nosis ... based systems, International Journal
ofMan-**Machine** Studies, 26,3 ... BWSE, JH and BR **GAMES** (1988) Knowledge Acquisition ...
[Cited by 16](#) - [Related Articles](#) - [Web Search](#)

On the application of a machine learning technique to faultdiagnosis of power distribution lines -

[all 4 versions »](#)

M Togami, N Abe, T Kitahashi, H Ogawa, N Mfg - Power Delivery, IEEE Transactions on, 1995 - ieeexplore.ieee.org

... An area of particular interest is **fault detection** and diagnosis. ... One aim of **machine learning** is to make an algorithm which automatically and efficiently shows ...

[Cited by 12](#) - [Related Articles](#) - [Web Search](#)

[Can machine learning be secure? - all 8 versions »](#)

M Barreno, B Nelson, R Sears, AD Joseph, JD Tygar - Proceedings of the 2006 ACM Symposium on Information, ..., 2006 - portal.acm.org

... within the context of deception **games** [14, 32 ... 5.2 Scale of Training Some **machine learning** systems are ... a widely used paradigm in **fault detection** [40], intrusion ...

[Cited by 10](#) - [Related Articles](#) - [Web Search](#)

[Traction Monitoring for Collision Detection with Legged Robots - all 7 versions »](#)

MJ Quinlan, CL Murch, RH Middleton, SK Chalup - Robocup 2003: Robot Soccer World Cup VII, 2004 - books.google.com

... the legs slip on ice the **machine** would not ... soccer, but the objective of the **game** is identical ... Note however that the **fault detection** methods presented below are ...

[Cited by 14](#) - [Related Articles](#) - [Web Search](#)



Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2008 Google

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 276900US8		SERIAL NO. 11/205,121	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Jun HAISHIMA			
				FILING DATE August 17, 2005		GROUP 2113	
				U.S. PATENT DOCUMENTS			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
B.A.R.F./	AA	US 6,449,735 B1	9/10/2002	David A. EDWARDS, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
					YES	NO	
B.A.R.F./	AO	JP 9-319445	12/12/1997	Japan (with English Abstract)			x
B.A.R.F./	AP	JP 2000-35888	2/2/2000	Japan (with English Abstract)			x
B.A.R.F./	AQ	EP 0 774 716 A1	5/21/1997	Europe			
B.A.R.F./	AR	EP 0 801 387 A2	10/15/1997	Europe			
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW						
	AX						
	AY						
	AZ						<input type="checkbox"/> Additional References sheet(s) attached
Examiner /Bradford Rodgers-Farmer/					Date Considered 03/25/2008		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Docket No. 276900US90

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Jun HAISHIMA

SERIAL NO: New Application

GAU:

FILED: Herewith

EXAMINER:

FOR: INFORMATION PROCESS DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s), published application(s) or issued patent(s) which may be related to the present application. In accordance with the waiver of 37 CFR 1.98 dated September 21, 2004, copies of the cited pending applications are not provided. Cited published and/or issued patents, if any, are listed on the attached PTO form 1449.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Masayasu Mori

Registration No. 47,301

C. Irvin McClelland
Registration Number 21,124

Customer Number

22850Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 05/03)

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 276900US90		SERIAL NO. New Application		
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Jun HAISHIMA				
				FILING DATE Herewith			GROUP	
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE		
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							
	AM							
	AN							
FOREIGN PATENT DOCUMENTS								
DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION					
			YES	NO				
/B.A.R.F./ AO	2003-330793	11/21/2003	Japan (with English Abstract)			X		
/B.A.R.F./ AP	2003-331236	11/21/2003	Japan (with English Abstract)			X		
	AQ							
	AR							
	AS							
	AT							
	AU							
	AV							
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)								
	AW							
	AX							
	AY							
	AZ						<input type="checkbox"/> Additional References sheet(s) attached	
Examiner /Bradford Rodgers-Farmer/						Date Considered 03/25/2008		
<small>*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>								

Results for "((gaming machine)<in>metadata) <and> ((fault)<in>metadata) <and> ((det..."
 Your search matched **1** of **1764710** documents.
 A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

e-mail printer friendly



Search Options

[View Session History](#)

[New Search](#)

Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

Check to search only within this results set

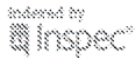
Display Format: Citation Citation & Abstract

IEEE/IET Books Educational Courses **Application Notes [Beta]**

Practical applied content provided by GlobalSpec to explain, illustrate and promote technologies. Not reviewed or endorsed by the IEEE.

 [Select All](#) [Deselect All](#)

- 1. The Silicon Gaming Odyssey slot machine**
 Levinthal, A.; Barnett, M.;
[Compcon '97 Proceedings, IEEE](#)
 23-26 Feb. 1997 Page(s):296 - 301
 Digital Object Identifier 10.1109/CMPCON.1997.584736
[AbstractPlus](#) | [Full Text: PDF\(436 KB\)](#) IEEE CNF
[Rights and Permissions](#)



EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	339	(714/36).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/03/25 14:24
L2	1144	(710/104).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/03/25 15:02
L3	1	("6,449,735").PN.	US-PGPUB; USPAT	OR	OFF	2008/03/25 15:06
L4	1	("5860122").PN.	US-PGPUB; USPAT	OR	OFF	2008/03/25 15:08
L5	7927	711/170.ccls. 713/1-100.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; IBM_TDB	OR	ON	2008/03/25 16:05
L6	5056597	memory storage area ROM	US-PGPUB; USPAT; FPRS; EPO; JPO; IBM_TDB	OR	ON	2008/03/25 16:05
L7	176959	(fault error problem crash) near (program detect\$4 inspect\$6 software device locat\$4)	US-PGPUB; USPAT	OR	ON	2008/03/25 16:08
L8	820841	boot start\$1up "start-up" POST	US-PGPUB; USPAT	OR	ON	2008/03/25 16:09
L9	38264	(fault problem crash error) near I6	US-PGPUB; USPAT	OR	ON	2008/03/25 16:09
L10	3576	I8 and I7 and I9 and I6	US-PGPUB; USPAT	OR	ON	2008/03/25 16:10
L11	88	I8 and I7 and I9 and I6 and I5	US-PGPUB; USPAT	OR	ON	2008/03/25 16:10
L12	903	I8 and I7 and I9 and I6 and "714".clas.	US-PGPUB; USPAT	OR	ON	2008/03/25 16:10
L13	2612	I7 same ROM	US-PGPUB; USPAT	OR	ON	2008/03/25 16:12
L14	921	(fault problem crash error) near ("Hard disk" "hard drive" HDD HD)	US-PGPUB; USPAT	OR	ON	2008/03/25 16:13
L15	67	I13 and I12	US-PGPUB; USPAT	OR	ON	2008/03/25 16:13
L16	1	I4 and I15	US-PGPUB; USPAT	OR	ON	2008/03/25 16:13

L17	26147	((arcade gam\$4) near (machine portable)) psp gameboy playstation nintendo xbox	US-PGPUB; USPAT	OR	ON	2008/03/25 16:40
L18	223	l17 and "714".clas.	US-PGPUB; USPAT	OR	ON	2008/03/25 16:40
L19	55	l18 l8	US-PGPUB; USPAT	AND	ON	2008/03/25 16:41
L20	3575356	memory storage area ROM	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:40
L21	84621	(fault error problem crash) near (program detect\$4 inspect\$6 software device locat\$4)	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:40
L22	259085	boot start\$1up "start-up" POST	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:40
L23	3	(fault problem crash error) l20	FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/03/25 17:41
L24	0	l23 and l22 and l21	FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/03/25 17:41
L25	0	l23 and l22	FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/03/25 17:41
L26	0	l22 and l23	FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/03/25 17:42
L27	679	l22 and l21	FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/03/25 17:42
L28	267	l22 and l21 and l20	FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/03/25 17:42
L29	166	(fault problem crash error) near ("Hard disk" "hard drive" HDD HD)	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:42

L30	117891	((arcade gam\$4) near (machine portable)) psp gameboy playstation nintendo xbox	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:42
L31	0	I29 and I28	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:42
L32	0	I30 and I29	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:43
L33	1587	I30 and I22	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:43
L34	242	I30 and I21	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:43
L35	3	I33 and I21	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/25 17:43

3/ 25/ 2008 6:07:22 PM

DOCKET NO: 276900US8

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
JUN HAISHIMA : EXAMINER: RODGERS FARMER, B.A.
SERIAL NO: 11/205,121 :
FILED: AUGUST 17, 2005 : GROUP ART UNIT: 4141
FOR: INFORMATION PROCESS DEVICE :

AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated April 3, 2008, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of this paper.

Remarks begin on page 6 of this paper.

Application No. 11/205,121
Reply to Office Action of April 3, 2008

IN THE SPECIFICATION

Please replace the Abstract on page 8, lines 1-6 in its entirety, as follows:

ABSTRACT

An information processing device and associated methodology are provided for inspecting a program fault. A first memory stores a boot program executed when the information processing device is initiated for operation. A second memory device is provided for storing an application program. A control device executes a fault inspection program to inspect whether or not a fault has occurred in the second memory device. The fault inspection program is stored in the first memory device and is initiated upon initialization of the information processing device.

IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Original): An information process device comprising:

a first memory device for storing a boot program executed when the information process device is started to operate;

a mother board on which the first memory device is provided;

a second memory device for storing an application program, the second memory device being connected to the mother board;

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device;

wherein the fault inspection program is stored in the first memory device; and

wherein the control device executes the fault inspection program when the information process device is started to operate.

Claim 2 (Original): The information process device according to claim 1, wherein the first memory device is a ROM provided on the mother board,

wherein the second memory device is a hard disk which is independent from the mother board, and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk.

Claim 3 (Original): The information process device according to claim 1, wherein the information process device is utilized as a gaming machine for business use.

Claim 4 (New): A gaming machine utilizing an information process device, the

information process device comprising:

a first memory device configured to store a boot program executed when the information process device is started to operate;

a mother board on which the first memory device is provided;

a second memory device configured to store a game application program, the second memory device being connected to the mother board and electrically rewritable;

a control device configured to execute a fault inspection program to inspect whether or not a fault occurs in the second memory device;

wherein the fault inspection program is stored in the first memory device;

wherein the control device executes the fault inspection program every time the information process device is started to operate, and

wherein when the fault does not occur in the second memory device the game application program is started to execute and when the fault occurs in the second memory device an error display is conducted on a display device.

Claim 5 (New): The gaming machine according to Claim 4,

wherein the first memory device is a ROM provided on the mother board,

wherein the second memory device is a hard disk which is independent from the mother board, and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk.

Claim 6 (New): The gaming machine according to Claim 5, further comprising:

a RAM provided on the mother board;

wherein the game application program is loaded from the hard disk to the RAM and executed by the control device when the fault does not occur in the hard disk.

REMARKS

Favorable reconsideration of this application as presently amended, and in light of the following discussion, is respectfully requested.

After entry of the foregoing amendment, Claims 1-6 remain pending in the present application. New Claims 4-6 are added and find support in the original specification and claims. A new Abstract has been submitted which is in a better format for U.S. practice. No new matter has been added.

By way of summary, the Official Action presents the following issues: Claims 1-2 stand rejected under 35 U.S.C. § 102 as being unpatentable over Owada et al. (U.S. Patent No. 5,860,122, hereinafter "Owada"); and Claim 3 stands rejected under 35 U.S.C. § 103 as being unpatentable over Owada in view of Pascal et al. (U.S. Patent No. 5,971,851, hereinafter "Pascal").

REJECTION UNDER 35 U.S.C. § 102

The Official Action has rejected Claims 1 and 2 under 35 U.S.C. § 102 as being unpatentable over Owada. The Official Action contends that Owada describes all of the Applicant's claimed features. Applicant respectfully traverses the rejection.

Applicant's Claim 1 recites, *inter alia*, an information processing device, including:

a first memory device for storing a boot program executed when the information process device is started to operate; ...

a second memory device for storing an application program, the second memory device being connected to the mother board;

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device;

wherein the fault inspection program is stored in the first memory device. ...

As can be appreciated from the above claim language, the first memory device as claimed must store a boot program and a fault inspection program. The fault inspection program of the first memory device is executed to inspect whether or not a fault occurs in the second memory device.

At pages 2-3 of the Official Action, the first memory device is identified as corresponding to column 2, lines 35-42 of Owada. Additionally, the control device is identified as corresponding to column 3, lines 5-8 of Owada. In other words, the Official Action is taking the position that the central control unit (30) and HDT fault detector (34) as shown in Figure 5 of Owada, corresponds to the claimed fault inspection program. However, the Official Action has also taken the position that the first memory device is a first storage means which contains at least one program and a set up file.

As the Official Action has not identified a first memory in accordance with Applicant's claims, namely, a memory that includes both **A Boot Program and a Fault Inspection Program**, the rejection is clearly improper under 35 U.S.C. § 102.

Accordingly, Applicant respectfully request that the rejection of Claims 1 and 2 under 35 U.S.C. § 102 be withdrawn.

REJECTION UNDER 35 U.S.C. § 103

The Official Action has rejected Claim 3 under 35 U.S.C. § 103 as being unpatentable over Owada in view of Pascal. The Official Action contends that the combination of these references describe all of the Applicant's claimed features. Applicant respectfully traverses the rejection.

As noted above, Owada does not provide all of the features of the Applicant's claims for which it has been asserted. Likewise, as Pascal does not remedy the deficiencies

discussed above, either alone or in combination with Owada, Applicant respectfully submit that a *prima facie* case of obviousness has not been presented.

Accordingly, Applicant respectfully request that the rejection of Claim 3 under 35 U.S.C. § 103 be withdrawn.

NEW CLAIMS

New Claims 4-6 present more detailed aspects of the Applicants specification. For example, as recited in Claim 4, before execution of an application program a fault inspection program inspects a fault in electrically-rewritable second memory device. If there is no fault detected, a game application program is executed. New Claim 6 recites that a RAM is provided on a mother board and loaded with an application program of a hard disk when a fault is not detected. Applicants respectfully submit that these more detailed aspects of the Applicants claimed advancements are neither disclosed nor suggested by the art of record.

CONCLUSION

Consequently, in view of the foregoing amendment and remarks, it is respectfully submitted that the present application, including Claims 1-6, is patentably distinguished over the prior art, in condition for allowance, and such action is respectfully requested at an early date.

Respectfully submitted,
OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



Bradley D. Lytle
Attorney of Record
Registration No. 40,073

Scott A. McKeown
Registration No. 42,866

Customer Number
22850

Tel: (703) 413-3000
Fax: (703) 413 -2220
(OSMMN 08/07)

I:\ATTY\SAM\PROSECUTION WORK\276900\276900US-AM.DOC

Electronic Acknowledgement Receipt

EFS ID:	3551268
Application Number:	11205121
International Application Number:	
Confirmation Number:	2910
Title of Invention:	Information process device
First Named Inventor/Applicant Name:	Jun Haishima
Customer Number:	22850
Filer:	Marvin Jay Spivak/Corrine Cunningham
Filer Authorized By:	Marvin Jay Spivak
Attorney Docket Number:	276900US90
Receipt Date:	01-JUL-2008
Filing Date:	17-AUG-2005
Time Stamp:	16:59:26
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
------------------------	----

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		276900USamendment.pdf	263094 <small>151efe5b823f3631d1e6a86f16740c0d59ac3d04</small>	yes	10

Multipart Description/PDF files in .zip description		
Document Description	Start	End
Miscellaneous Incoming Letter	1	1
Amendment - After Non-Final Rejection	2	2
Specification	3	3
Abstract	4	4
Claims	5	7
Applicant Arguments/Remarks Made in an Amendment	8	10
Warnings:		
Information:		
Total Files Size (in bytes):		263094
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>		

Docket No. 276900US8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Jun HAISHIMA

SERIAL NO: 11/205,121

ART UNIT: 4141

FILING DATE: August 17, 2005

EXAMINER: RODGERS, FARMER, B.A.

FOR: INFORMATION PROCESS DEVICE

FEE TRANSMITTAL


- No additional fee is required
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.

The Fee has been calculated as shown below:

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	6 - 20 =	0	x \$50 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	x \$210 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$370 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
BASIC FEES				\$0.00
TOTAL OF ABOVE CALCULATIONS				\$0.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
TOTAL				\$0.00

- Please charge Deposit Account No. 15-0030 in the amount of **\$0.00**
- A check in the amount of **\$0.00** to cover the filing fee is enclosed.
- Online credit card payment is being made to cover the filing fee in the amount of **\$0.00**
- The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030.

Submitted by:


 Bradley D. Lytle
 Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 07/07)

Scott A. McKeown
 Registration No. 42,866

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875				Application or Docket Number 11/205,121		Filing Date 08/17/2005		<input type="checkbox"/> To be Mailed					
APPLICATION AS FILED – PART I													
(Column 1)			(Column 2)			SMALL ENTITY <input type="checkbox"/>		OR		OTHER THAN SMALL ENTITY			
FOR		NUMBER FILED	NUMBER EXTRA		RATE (\$)	FEE (\$)	OR		RATE (\$)	FEE (\$)			
<input type="checkbox"/> BASIC FEE <small>(37 CFR 1.16(a), (b), or (c))</small>		N/A	N/A		N/A				N/A				
<input type="checkbox"/> SEARCH FEE <small>(37 CFR 1.16(k), (l), or (m))</small>		N/A	N/A		N/A		N/A						
<input type="checkbox"/> EXAMINATION FEE <small>(37 CFR 1.16(o), (p), or (q))</small>		N/A	N/A		N/A		N/A						
TOTAL CLAIMS <small>(37 CFR 1.16(i))</small>		minus 20 =	*		X \$ =		OR		X \$ =				
INDEPENDENT CLAIMS <small>(37 CFR 1.16(h))</small>		minus 3 =	*		X \$ =		OR		X \$ =				
<input type="checkbox"/> APPLICATION SIZE FEE <small>(37 CFR 1.16(s))</small>		If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).											
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENT <small>(37 CFR 1.16(j))</small>													
* If the difference in column 1 is less than zero, enter "0" in column 2.													
APPLICATION AS AMENDED – PART II													
(Column 1)			(Column 2)		(Column 3)			SMALL ENTITY		OR		OTHER THAN SMALL ENTITY	
AMENDMENT	07/01/2008	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE (\$)	ADDITIONAL FEE (\$)	OR		RATE (\$)	ADDITIONAL FEE (\$)		
	Total <small>(37 CFR 1.16(i))</small>	* 6	Minus	** 20	= 0	X \$ =				X \$50=	0		
	Independent <small>(37 CFR 1.16(h))</small>	* 2	Minus	***3	= 0	X \$ =		X \$210=	0				
	<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>												
	<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>												
						TOTAL ADD'L FEE		OR		TOTAL ADD'L FEE	0		
AMENDMENT		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE (\$)	ADDITIONAL FEE (\$)	OR		RATE (\$)	ADDITIONAL FEE (\$)		
	Total <small>(37 CFR 1.16(i))</small>	*	Minus	**	=	X \$ =				X \$ =			
	Independent <small>(37 CFR 1.16(h))</small>	*	Minus	***	=	X \$ =		X \$ =					
	<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>												
	<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>												
						TOTAL ADD'L FEE		OR		TOTAL ADD'L FEE			
* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.													
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".													
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".													
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.													

Legal Instrument Examiner:
/ADRIENE D. SELLMAN/

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO., EXAMINER, ART UNIT, PAPER NUMBER, NOTIFICATION DATE, DELIVERY MODE. Includes details for application 11/205,121 filed 08/17/2005 by Jun Haishima, attorney 276900US90, examiner CONTINO, PAUL F, art unit 2114, notified 11/04/2008 electronically.

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

- patentdocket@oblon.com
oblonpat@oblon.com
jgardner@oblon.com

Office Action Summary	Application No. 11/205,121	Applicant(s) HAISHIMA, JUN	
	Examiner PAUL F. CONTINO	Art Unit 2114	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 01 July 2008.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-6 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-6 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 17 August 2005 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 - 1. Certified copies of the priority documents have been received.
 - 2. Certified copies of the priority documents have been received in Application No. _____.
 - 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO/SB/08)
 Paper No(s)/Mail Date _____.
- 4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
- 5) Notice of Informal Patent Application
- 6) Other: _____.

DETAILED ACTION: Non-final Rejection

Response to Arguments

1. Applicant's arguments, filed July 1, 2008, with respect to the rejection of claims 1-3 have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground of rejection is made as follows.

Specification

2. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chang (U.S. PGPub 2005/0246586) in view of Official Notice.

As in claim 1, Chang teaches an information process device comprising:

a first memory device for storing a boot program executed when the information process device is started to operate (*Fig. 3 #10; paragraph [0002], BIOS*);

a mother board on which the first memory device is provided (*Fig. 3; paragraph [0005], [0007], and [0037], ROM containing BIOS and a motherboard*);

a second memory device for storing an application program, the second memory device being connected to the mother board (*paragraph [0002], hard disk*);

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device (*paragraph [0002], BIOS contains a testing program*);

wherein the fault inspection program is stored in the first memory device (*paragraph [0002]*); and

wherein the control device executes the fault inspection program when the information process device is started to operate (*paragraph [0002]*).

However, Chang fails to expressly teach of a mother board or a second memory device attached to a mother board. The Examiner takes Official Notice that it would have been obvious to one skilled in the art at the time the invention was made to have included these elements in the invention of Chang. This would have been obvious because it is well known in the art to include a memory device (ROM) on a mother board and to connect a second memory device (hard disk) to the motherboard, resulting in a compact, efficient, and properly working computer system.

As in claim 2, Chang teaches the first memory device is a ROM provided on the mother board (*paragraph [0005]*),

wherein the second memory device is a hard disk which is independent from the mother board (*paragraph [0002]*), and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk (*paragraph [0002]*).

As in claim 3, Chang teaches of the information processing device as a machine for general use. However, Chang fails to teach of using the device as a gaming machine for business use. The Examiner takes Official Notice that it would have been obvious to a person skilled in the art at the time the invention was made to utilize the invention of Chang as a gaming machine for business use. This would have been obvious because it is well known in the art to use a computer as a business tool with software included which runs gaming applications as a secondary utilization.

* * *

4. Claims 4-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chang in view of Yamato (U.S. Patent No. 6,115,036), further in view of Official Notice.

As in claim 4, Chang teaches of a machine utilizing an information processing device, the information process device comprising:

a first memory device for storing a boot program executed when the information process device is started to operate (*Fig. 3 #10; paragraph [0002], BIOS*);

a mother board on which the first memory device is provided (*Fig. 3; paragraph [0005], [0007], and [0037], ROM containing BIOS and a motherboard*);

a second memory device configured to store an application program, the second memory device being connected to the mother board and electrically rewritable (*paragraph [0002], hard disk*);

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device (*paragraph [0002], BIOS contains a testing program*);

wherein the fault inspection program is stored in the first memory device (*paragraph [0002]*); and

wherein the control device executes the fault inspection program every time the information process device is started to operate (*paragraph [0002]*); and

wherein when the fault does not occur in the second memory device the application program is started to execute and when the fault occurs in the second memory device an error display is conducted on a display device (*paragraph [0002]*).

However, Chang fails to expressly teach of a gaming machine, a game application program, a mother board or a second memory device attached to a mother board. Yamato teaches of a gaming machine and a game application program (*Fig. 1A; column 26 lines 1-14 [game application program] and 54-57*). It would have been obvious to a person skilled in the art at the time the invention was made to have included the gaming components as taught by

Yamato in the invention of Chang because Chang offers a further level of fault tolerance while booting a computer system using a BIOS in the invention of Yamato.

The Examiner takes Official Notice that it would have been obvious to one skilled in the art at the time the invention was made to have included a mother board and a second memory device in the combined invention of Chang and Yamato. This would have been obvious because it is well known in the art to include a memory device (ROM) on a mother board and to connect a second memory device (hard disk) to the motherboard, resulting in a compact, efficient, and properly working computer system.

As in claim 5, Chang teaches the first memory device is a ROM provided on the mother board (*paragraph [0005]*),

wherein the second memory device is a hard disk which is independent from the mother board (*paragraph [0002]*), and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk (*paragraph [0002]*).

As in claim 6, Yamato teaches of a RAM provided on the mother board (*Fig. 2A*);

wherein the game application program is loaded from the hard disk to the RAM and executed by the control device when the fault does not occur in the hard disk (*Chang: paragraph [0002] hard disk; Yamato: column 5 lines 36-37, program from disk 8 to RAM 4*).

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to PAUL F. CONTINO whose telephone number is (571)272-3657. The examiner can normally be reached on Monday-Friday 9:00 am - 6:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Scott Baderman can be reached on (571) 272-3644. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Paul F. Contino/
28 October 2008
Patent Examiner
AU 2114

Notice of References Cited	Application/Control No. 11/205,121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN	
	Examiner PAUL F. CONTINO	Art Unit 2114	Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A US-2005/0246586	11-2005	Chang, Yen Sheng	714/036
*	B US-6,115,036	09-2000	Yamato et al.	715/723
	C US-			
	D US-			
	E US-			
	F US-			
	G US-			
	H US-			
	I US-			
	J US-			
	K US-			
	L US-			
	M US-			


FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N				
	O				
	P				
	Q				
	R				
	S				
	T				

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	
V	
W	
X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

<i>Index of Claims</i> 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner PAUL F. CONTINO	Art Unit 2114

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

Claims renumbered in the same order as presented by applicant
 CPA
 T.D.
 R.1.47

CLAIM		DATE							
Final	Original	03/25/2008	10/28/2008						
	1	✓	✓						
	2	✓	✓						
	3	✓	✓						
	4		✓						
	5		✓						
	6		✓						

Search Notes 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner PAUL F. CONTINO	Art Unit 2114

SEARCHED			
Class	Subclass	Date	Examiner
714	36	10/28/2008	PFC
710	104	10/28/2008	PFC

SEARCH NOTES		
Search Notes	Date	Examiner
Previous Examiner's prosecution history	10/28/2008	PFC
714/2,3,5 - text search - see included search history	10/28/2008	PFC
Inventor search	10/28/2008	PFC

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner

--	--

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1592	714/36.ccls. 710/104.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 17:25
S1	2	"5860122".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 10:32
S2	2	"5971851".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 10:34
S4	1662	714/3,5.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:38
S5	2664	713/2.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:38
S6	4296	S4 S5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:38
S7	309	S6 and (boot\$3 with (rom eprom eeprom)) and ((fault\$3 corrupt \$3 error erroneous fail \$3) with (memory hdd harddrive hard adj (drive disc disk)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:39

S8	68	S6 and (boot\$3 with (rom eprom eeprom)) same ((fault\$3 corrupt \$3 error erroneus fail \$3) with (memory hdd harddrive hard adj (drive disc disk)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:40
S9	18	S6 and (boot\$3 with (rom eprom eeprom)) same ((fault\$3 corrupt \$3 error erroneus fail \$3) with (hdd harddrive hard adj (drive disc disk)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:41
S10	2	(boot\$3 with post with (rom eprom eeprom)) same ((fault\$3 corrupt \$3 error erroneus fail \$3) with (hdd harddrive hard adj (drive disc disk)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:45
S11	1	(boot\$3 with (test\$3 diagnos \$5) with (rom eprom eeprom)) same ((fault\$3 corrupt \$3 error erroneus fail \$3) with (hdd harddrive hard adj (drive disc disk)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:45

S12	43	(boot\$3 with (post test\$3 diagnos\$5)) same ((fault\$3 corrupt\$3 error erroneous fail \$3) with (hdd harddrive hard adj (drive disc disk)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 14:46
S13	1991	boot\$3 with (gam\$3 videogam\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 15:40
S14	39	S13 with bios	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/28 15:40

10/28/08 5:32:38 PM

C:\Documents and Settings\pcontino\My Documents\EAST\Workspaces
\11_205121.wsp



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIB DATA SHEET

CONFIRMATION NO. 2910

SERIAL NUMBER 11/205,121	FILING or 371(c) DATE 08/17/2005 RULE	CLASS 714	GROUP ART UNIT 2114	ATTORNEY DOCKET NO. 276900US90	
APPLICANTS Jun Haishima, Tokyo, JAPAN; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN 2004-245337 08/25/2004 ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/02/2005					
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/PAUL F CONTINO/</u> Examiner's Signature	<input type="checkbox"/> Met after Allowance Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 3	TOTAL CLAIMS 3	INDEPENDENT CLAIMS 1
ADDRESS OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314 UNITED STATES					
TITLE Information process device					
FILING FEE RECEIVED 1000	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		

DOCKET NO: 276900US8

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
JUN HAISHIMA : EXAMINER: P. F. CONTINO
SERIAL NO: 11/205,121 :
FILED: AUGUST 17, 2005 : GROUP ART UNIT: 2114
FOR: GAMING APPARATUS HAVING :
MEMORY FAULT DETECTION :

AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated November 4, 2008, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 6 of this paper.

Application No. 11/205,121
Reply to Office Action of November 4, 2008

IN THE SPECIFICATION

Please delete the title on page 1 and insert therefor the following replacement title as follows:

GAMING APPARATUS HAVING MEMORY FAULT DETECTION

IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Currently Amended): An information process device comprising:

a first memory device for storing a boot program executed when the information process device is started to operate;

a mother board on which the first memory device is provided;

a second memory device for storing an application program, the second memory device being connected to the mother board;

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device and the application program stored therein;

wherein the fault inspection program is stored in the first memory device; and

wherein the control device executes the fault inspection program when the information process device is started to operate.

Claim 2 (Original): The information process device according to claim 1, wherein the first memory device is a ROM provided on the mother board,

wherein the second memory device is a hard disk which is independent from the mother board, and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk.

Claim 3 (Original): The information process device according to claim 1, wherein the information process device is utilized as a gaming machine for business use.

Claim 4 (Currently Amended): A gaming machine utilizing an information process device, the information process device comprising:

a first memory device configured to store a boot program executed when the information process device is started to operate;

a mother board on which the first memory device is provided;

a second memory device configured to store a game application program, the second memory device being connected to the mother board and electrically rewritable;

a control device configured to execute a fault inspection program to inspect whether or not a fault occurs in the second memory device and the application program stored therein;

wherein the fault inspection program is stored in the first memory device;

wherein the control device executes the fault inspection program every time the information process device is started to operate, and

wherein when the fault does not occur in the second memory device the game application program is started to execute and when the fault occurs in the second memory device an error display is conducted on a display device.

Claim 5 (Previously Presented): The gaming machine according to Claim 4,

wherein the first memory device is a ROM provided on the mother board,

wherein the second memory device is a hard disk which is independent from the mother board, and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk.

Claim 6 (Previously Presented): The gaming machine according to Claim 5, further comprising:

a RAM provided on the mother board;
wherein the game application program is loaded from the hard disk to the RAM and executed by the control device when the fault does not occur in the hard disk.

Claim 7 (New): An information process device comprising:
a first memory device for storing a boot program executed when the information process device is started to operate;
a mother board on which the first memory device is provided;
a second memory device for storing an application program and a BIOS, the second memory device being connected to the mother board;
a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device and the application program stored therein;
wherein the fault inspection program is stored in the first memory device; and
wherein the control device executes the boot program to initialize the BIOS stored in the second memory device before executing the fault inspection program when the information process device is started to operate.

REMARKS

Favorable reconsideration of this application as presently amended, and in light of the following discussion, is respectfully requested.

After entry of the foregoing amendment, Claims 1-7 remain pending in the present application. Claims 1 and 4 have been amended to include features described at least at page 5 of the specification. New Claim 7 has been added. Support for which is found at least at page 4 of the specification. A new title has been submitted. No new matter has been added.

By way of summary, the Official Action presents the following issues: the title of the invention is objected to as to matters of form; Claims 1-3 stand rejected under 35 U.S.C. § 103 as being unpatentable over Chang (U.S. Patent Publication 2005/0246586) in view of Official Notice; and Claims 4-6 stand rejected under 35 U.S.C. § 103 as being unpatentable over Chang in view of Yamato (U.S. Patent 6,115,036) in further view of Official Notice.

Objection to the Title

With regard to the objection to the title outlined at paragraph 2 of the Official Action, Applicant submits herewith a new title.

Accordingly, Applicant respectfully requests that the objection to the title be withdrawn.

Rejection under 35 U.S.C. § 103

The Official Action has rejected Claims 1-3 under 35 U.S.C. § 103 as being unpatentable over Chang. The Official Action contends that Chang describes or suggests all of the Applicant's claimed features, in combination with Official Notice. Applicant respectfully traverses the rejection.

Applicant's amended Claim 1 recites, *inter alia*, an information processing device, including:

a first memory device for storing a boot program executed when the information process device is started to operate;

a mother board on which the first memory device is provided;

a second memory device for storing an application program, the second memory device being connected to the mother board;

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device and the application program stored therein; ...

Applicant's amended claims require that the control device be configured to execute a fault inspection program which inspects whether or not a fault occurs in a second memory device and an application program stored therein.

As the Official Action has cited the BIOS of Chang as corresponding to the fault detection program, Applicant points out that the BIOS does not perform any analysis of application programs.

Accordingly, Applicant respectfully requests that the rejection of Claims 1-3 under 35 U.S.C. § 103 be withdrawn.

The Official Action has rejected Claims 4-6 under 35 U.S.C. § 103 as being unpatentable over Chang and Yamato, in view of Official Notice. Applicant respectfully traverses the rejection.

As noted above, Chang does not provide all of the features of the Applicant's amended claims for which it has been cited. Likewise, as Yamato nor Official Notice is relied upon for such features, nor do they provide such features, Applicant respectfully submits that a *prima facie* case of obviousness has not been presented.

Application No. 11/205,121
Reply to Office Action of November 4, 2008

Accordingly, Applicant respectfully requests that the rejection of Claims 4-6 under 35 U.S.C. § 103 be withdrawn.

NEW CLAIM

New Claim 7 has been added to clarify that a BIOS is included in the claim device and provides functionality which is separate with respect to the fault inspection program. Claim 7 is allowable over the art of record at least for the reasons discussed above.

CONCLUSION

Consequently, in view of the foregoing amendment and remarks, it is respectfully submitted that the present application, including Claims 1-6, is patentably distinguished over the prior art, in condition for allowance, and such action is respectfully requested at an early date.

Respectfully submitted,
OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



Bradley D. Lytle
Attorney of Record
Registration No. 40,073

Customer Number
22850

Tel: (703) 413-3000
Fax: (703) 413 -2220
(OSMMN 08/07)

Scott A. McKeown
Registration No. 42,866

I:\ATTY\ISAM\PROSECUTION WORK\276900\276900US-AM DUE 02-04-09.DOC

Electronic Acknowledgement Receipt

EFS ID:	4711208
Application Number:	11205121
International Application Number:	
Confirmation Number:	2910
Title of Invention:	Information process device
First Named Inventor/Applicant Name:	Jun Haishima
Customer Number:	22850
Filer:	Marvin Jay Spivak/Corrine Cunningham
Filer Authorized By:	Marvin Jay Spivak
Attorney Docket Number:	276900US90
Receipt Date:	30-JAN-2009
Filing Date:	17-AUG-2005
Time Stamp:	17:41:01
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
------------------------	----

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		276900USamendment.pdf	252977 <small>4f937d582cb159f9f510c0835f2c8c6c4798 4a4</small>	yes	9

Multipart Description/PDF files in .zip description		
Document Description	Start	End
Miscellaneous Incoming Letter	1	1
Amendment/Req. Reconsideration-After Non-Final Reject	2	2
Specification	3	3
Claims	4	6
Applicant Arguments/Remarks Made in an Amendment	7	9

Warnings:

Information:

Total Files Size (in bytes):

252977

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

Docket No. 276900US8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Jun HAISHIMA

SERIAL NO: 11/205,121

ART UNIT: 2114

FILING DATE: August 17, 2005

EXAMINER: P. F. CONTINO

FOR: GAMING APPARATUS HAVING MEMORY FAULT DETECTION

FEE TRANSMITTAL

- No additional fee is required
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.

The Fee has been calculated as shown below:

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	7 - 20 =	0	x \$52 =	\$0.00
INDEPENDENT CLAIMS	3 - 3 =	0	x \$220 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$390 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
BASIC FEES				\$0.00
TOTAL OF ABOVE CALCULATIONS				\$0.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
TOTAL				\$0.00

- Please charge Deposit Account No. **15-0030** in the amount of _____
- Credit card payment is being made online (if electronically filed), or is attached hereto (if paper filed), in the amount of _____
- The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no payment is enclosed herewith, or credit any overpayment to Deposit Account No. **15-0030**.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030.

Submitted by: _____

Bradley D. Lytle
Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 10/08)

Scott A. McKeown
Registration No. 42,866

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875					Application or Docket Number 11/205,121		Filing Date 08/17/2005		<input type="checkbox"/> To be Mailed			
APPLICATION AS FILED – PART I												
(Column 1)			(Column 2)			SMALL ENTITY <input type="checkbox"/>		OR			OTHER THAN SMALL ENTITY	
FOR		NUMBER FILED	NUMBER EXTRA		RATE (\$)	FEE (\$)	OR		RATE (\$)	FEE (\$)		
<input type="checkbox"/> BASIC FEE <small>(37 CFR 1.16(a), (b), or (c))</small>		N/A	N/A		N/A				N/A			
<input type="checkbox"/> SEARCH FEE <small>(37 CFR 1.16(k), (l), or (m))</small>		N/A	N/A		N/A		N/A					
<input type="checkbox"/> EXAMINATION FEE <small>(37 CFR 1.16(o), (p), or (q))</small>		N/A	N/A		N/A		N/A					
TOTAL CLAIMS <small>(37 CFR 1.16(i))</small>		minus 20 =	*		X \$ =		OR		X \$ =			
INDEPENDENT CLAIMS <small>(37 CFR 1.16(h))</small>		minus 3 =	*		X \$ =		OR		X \$ =			
<input type="checkbox"/> APPLICATION SIZE FEE <small>(37 CFR 1.16(s))</small>		If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).										
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENT <small>(37 CFR 1.16(j))</small>												
* If the difference in column 1 is less than zero, enter "0" in column 2.												
APPLICATION AS AMENDED – PART II					SMALL ENTITY		OR		OTHER THAN SMALL ENTITY			
(Column 1)		(Column 2)		(Column 3)		RATE (\$)	ADDITIONAL FEE (\$)	OR		RATE (\$)	ADDITIONAL FEE (\$)	
01/30/2009		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA							
Total <small>(37 CFR 1.16(i))</small>		* 7	Minus	** 20	= 0	X \$ =		OR		X \$52=	0	
Independent <small>(37 CFR 1.16(h))</small>		* 3	Minus	***3	= 0	X \$ =		OR		X \$220=	0	
<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>												
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>												
						TOTAL ADD'L FEE	OR		TOTAL ADD'L FEE	0		
(Column 1)		(Column 2)		(Column 3)		RATE (\$)	ADDITIONAL FEE (\$)	OR		RATE (\$)	ADDITIONAL FEE (\$)	
AMENDMENT		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA							
Total <small>(37 CFR 1.16(i))</small>		*	Minus	**	=	X \$ =		OR		X \$ =		
Independent <small>(37 CFR 1.16(h))</small>		*	Minus	***	=	X \$ =		OR		X \$ =		
<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>												
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>												
						TOTAL ADD'L FEE	OR		TOTAL ADD'L FEE			
* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.												
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".												
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".												
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.												
Legal Instrument Examiner: /Fennell A. Pearlie/												

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with 5 columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO. Includes sub-tables for EXAMINER, ART UNIT, PAPER NUMBER, NOTIFICATION DATE, and DELIVERY MODE.

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

patentdocket@oblon.com
oblonpat@oblon.com
jgardner@oblon.com

DETAILED ACTION: Final Rejection

Response to Arguments

1. Applicant's arguments with respect to claims 1-6 have been considered but are moot in view of the new grounds of rejection.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bizzarri (U.S. Patent No. 5,732,268) in view of Alexander (U.S. Patent No. 6,393,559).

As in claim 1, Bizzarri teaches of an information process device (*Fig. 1 #11*) comprising:
a first memory device for storing a boot program executed when the information process device is started to operate (*column 5 lines 9-20, E-BIOS 17 boot program inherently stored in a first memory device*);

a second memory device for storing an application program (*column 5 lines 35-36, application program code on a hard disk*);

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device and the application program stored therein (*column 6 lines 6-11*);

wherein the fault inspection program is stored in the first memory device (*column 5 lines 9-20 and column 6 lines 6-11, E-BIOS*); and

wherein the control device executes the fault inspection program when the information process device is started to operate (*column 5 lines 9-20 and column 6 lines 6-11, fault inspection upon booting of information processing device 11*).

However, Bizzarri fails to expressly teach of a mother board or a second memory device connected to a mother board. Alexander teaches of a motherboard with a connected second memory device (*Fig. 2 #s18,26,28; column 2 lines 58-68, hard drive 26, disk drive 28, and BIOS memory 18*).

It would have been obvious to a person skilled in the art at the time the invention was made to have included the motherboard and memory connection as taught by Alexander in the invention of Bizzarri. This would have been obvious because the computer layout as taught by Alexander is well-known to those skilled in the art as a conventional means of laying out a computer system (*column 2 lines 61-63*).

As in claim 2, the combined invention of Bizzarri and Alexander teaches the first memory device is a ROM provided on the mother board (*Bizzarri: column 3 lines 66-67; Alexander: Fig. 2 #18, column 2 lines 64-68*),

wherein the second memory device is a hard disk which is independent from the mother board (*Alexander: Fig. 2 #26; column 2 line 58*), and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk (*Bizzarri: column 5 lines 30-42 and column 6 lines 5-13*).

* * *

3. Claims 3-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bizzarri in view of Alexander, further in view of Yamato (U.S. Patent No. 6,115,036).

As in claim 3, the combined invention of Bizzarri and Alexander teaches of an information processing device. However, the combined invention of Bizzarri and Alexander fails to teach of a gaming machine. Yamato teaches of a gaming machine for business use (*abstract, game fabrication system*).

It would have been obvious to a person skilled in the art at the time the invention was made to have included the gaming components as taught by Yamato in the combined invention of Bizzarri and Alexander because the combined invention of Bizzarri and Alexander offers a

further level of fault tolerance while booting a computer system using a BIOS in the invention of Yamato.

As in claim 4, Bizzarri teaches of a machine utilizing an information processing device (*Fig. 1 #11*), the information process device comprising:

a first memory device for storing a boot program executed when the information process device is started to operate (*column 5 lines 9-20, E-BIOS 17 boot program inherently stored in a first memory device*);

a second memory device configured to store an application program (*column 5 lines 35-36, application program code on a hard disk*);

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device and the application program stored therein (*column 6 lines 6-11*);

wherein the fault inspection program is stored in the first memory device (*column 5 lines 9-20 and column 6 lines 6-11, E-BIOS*); and

wherein the control device executes the fault inspection program every time the information process device is started to operate (*column 5 lines 9-20 and column 6 lines 6-11, fault inspection upon booting of information processing device 11*); and

wherein when the fault does not occur in the second memory device the application program is started to execute and when the fault occurs in the second memory device an error display is conducted on a display device (*column 6 lines 10-14, normal application program loading; column 6 lines 50-53, on-screen message upon failure*).

However, Bizzarri fails to expressly teach of a gaming machine, a game application program, a mother board or a second memory device connected to a mother board. Alexander teaches of a motherboard with a connected second memory device (*Fig. 2 #s18,26,28; column 2 lines 58-68, hard drive 26, disk drive 28, and BIOS memory 18*). Yamato teaches of a gaming machine and a game application program (*Fig. 1A; column 26 lines 1-14 [game application program] and 54-57*).

It would have been obvious to a person skilled in the art at the time the invention was made to have included the motherboard and memory connection as taught by Alexander in the invention of Bizzarri. This would have been obvious because the computer layout as taught by Alexander is well-known to those skilled in the art as a conventional means of laying out a computer system (*column 2 lines 61-63*).

It would have been obvious to a person skilled in the art at the time the invention was made to have included the gaming components as taught by Yamato in the combined invention of Bizzarri and Alexander because the combined invention of Bizzarri and Alexander offers a further level of fault tolerance while booting a computer system using a BIOS in the invention of Yamato.

As in claim 5, the combined invention of Bizzarri and Alexander teaches the first memory device is a ROM provided on the mother board (*Bizzarri: column 3 lines 66-67; Alexander: Fig. 2 #18, column 2 lines 64-68*),

wherein the second memory device is a hard disk which is independent from the mother board (*Alexander: Fig. 2 #26; column 2 line 58*), and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk (*Bizzarri: column 5 lines 30-42 and column 6 lines 5-13*).

As in claim 6, Yamato teaches of a RAM provided on the mother board (*Fig. 2A*);
wherein the game application program is loaded from the hard disk to the RAM and executed by the control device when the fault does not occur in the hard disk (*Chang: paragraph [0002] hard disk; Yamoto: column 5 lines 36-37, program from disk 8 to RAM 4*).

* * *

4. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bizzarri in view of Alexander, further in view of Krau (U.S. Patent No. 5,864,698).

As in claim 7, Bizzarri teaches of an information process device (*Fig. 1 #11*) comprising:
a first memory device for storing a boot program executed when the information process device is started to operate (*column 5 lines 9-20, E-BIOS 17 boot program inherently stored in a first memory device*);
a second memory device for storing an application program (*column 5 lines 35-36, application program code on a hard disk*);

a control device for executing a fault inspection program to inspect whether or not a fault occurs in the second memory device and the application program stored therein (*column 6 lines 6-11*);

wherein the fault inspection program is stored in the first memory device (*column 5 lines 9-20 and column 6 lines 6-11, E-BIOS*); and

wherein the control device executes the fault inspection program when the information process device is started to operate (*column 5 lines 9-20 and column 6 lines 6-11, fault inspection upon booting of information processing device 11*).

However, Bizzarri fails to expressly teach of a mother board or a second memory device connected to a mother board, or execution of a BIOS on a second memory device. Alexander teaches of a motherboard with a connected second memory device (*Fig. 2 #s18,26,28; column 2 lines 58-68, hard drive 26, disk drive 28, and BIOS memory 18*). Krau teaches of a BIOS initialized on a second memory device upon execution of a boot program (*column 1 lines 55-62, balance of BIOS on second memory hard disk*).

It would have been obvious to a person skilled in the art at the time the invention was made to have included the motherboard and memory connection as taught by Alexander in the invention of Bizzarri. This would have been obvious because the computer layout as taught by Alexander is well-known to those skilled in the art as a conventional means of laying out a computer system (*column 2 lines 61-63*).

It would have been obvious to a person skilled in the art at the time the invention was made to have included the BIOS layout as taught by Krau in the combined invention of Bizzarri and Alexander. This would have been obvious because Krau teaches of storing portions of a

BIOS in both a ROM and a hard disk in order to reduce the space available for applications (*column 1 lines 55-66*), which, when coupled with the BIOS execution as taught by Bizzarri, would execute the BIOS before fault inspection.

Conclusion

5. Applicant's amendment necessitated the new grounds of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to PAUL F. CONTINO whose telephone number is (571) 272-3657. The examiner can normally be reached on Monday-Friday 9:00 am - 6:00 pm.

Application/Control Number: 11/205,121
Art Unit: 2114

Page 10

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Scott Baderman can be reached on (571) 272-3644. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Scott T Baderman/
Supervisory Patent Examiner, Art Unit 2114

PFC
20 April 2009

Notice of References Cited	Application/Control No. 11/205,121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN	
	Examiner PAUL F. CONTINO	Art Unit 2114	Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A US-5,732,268	03-1998	Bizzarri, Maurice W.	713/2
*	B US-5,864,698	01-1999	Krau et al.	713/2
*	C US-6,393,559	05-2002	Alexander, Marc D.	713/2
	D US-			
	E US-			
	F US-			
	G US-			
	H US-			
	I US-			
	J US-			
	K US-			
	L US-			
	M US-			


FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N				
	O				
	P				
	Q				
	R				
	S				
	T				

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	
V	
W	
X	


*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

<i>Index of Claims</i> 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner PAUL F. CONTINO	Art Unit 2114

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

Claims renumbered in the same order as presented by applicant
 CPA
 T.D.
 R.1.47

CLAIM		DATE							
Final	Original	03/25/2008	10/28/2008	04/20/2009					
	1	✓	✓	✓					
	2	✓	✓	✓					
	3	✓	✓	✓					
	4		✓	✓					
	5		✓	✓					
	6		✓	✓					
	7			✓					

Search Notes 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner PAUL F. CONTINO	Art Unit 2114

SEARCHED			
Class	Subclass	Date	Examiner
714	36	10/28/2008	PFC
710	104	10/28/2008	PFC

SEARCH NOTES		
Search Notes	Date	Examiner
Previous Examiner's prosecution history	10/28/2008	PFC
714/2,3,5 - text search - see included search history	10/28/2008	PFC
Inventor search	10/28/2008	PFC
SEARCHED subclasses updated	4/16/2009	PFC

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner

--	--

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S20	105	(bios) with (hard adj (disk disc drive) harddisk harddrive harddisc) with (fault\$3 corrupt\$4 error erroneous fail \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 10:11
S19	259	(bios) with (hard adj (disk disc drive) harddisk harddrive harddisc) same (fault\$3 corrupt\$4 error erroneous fail \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 10:10
S18	1669	714/36.ccls. 710/104.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 10:04
S17	2916	713/2.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 10:04
S16	319	714/27.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 10:03

4/ 20/ 09 10:52:59 AM

C:\ Documents and Settings\ pcontinio\ My Documents\ EAST\ Workspaces

\ 11_205121.wsp



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with 5 columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO. Includes fields for EXAMINER (CONTINO, PAUL F), ART UNIT (2114), and DELIVERY MODE (ELECTRONIC).

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

patentdocket@oblon.com
oblonpat@oblon.com
jgardner@oblon.com

Interview Summary	Application No.	Applicant(s)	
		11/205,121	HAISHIMA, JUN
	Examiner	Art Unit	
	PAUL F. CONTINO	2114	
<p>All participants (applicant, applicant's representative, PTO personnel):</p> <p>(1) <u>Paul F. Contino (Examiner)</u>. (3)_____.</p> <p>(2) <u>Derek P. Benke (Attorney)</u>. (4)_____.</p> <p>Date of Interview: <u>16 July 2009</u>.</p> <p>Type: a) <input type="checkbox"/> Telephonic b) <input type="checkbox"/> Video Conference c) <input checked="" type="checkbox"/> Personal [copy given to: 1) <input type="checkbox"/> applicant 2) <input checked="" type="checkbox"/> applicant's representative]</p> <p>Exhibit shown or demonstration conducted: d) <input type="checkbox"/> Yes e) <input checked="" type="checkbox"/> No. If Yes, brief description: _____.</p> <p>Claim(s) discussed: <u>All</u>.</p> <p>Identification of prior art discussed: <u>Bizarri (U.S. Patent 5,732,268)</u>.</p> <p>Agreement with respect to the claims f) <input type="checkbox"/> was reached. g) <input type="checkbox"/> was not reached. h) <input checked="" type="checkbox"/> N/A.</p> <p>Substance of Interview including description of the general nature of what was agreed to if an agreement was reached, or any other comments: <u>The Examiner and the Applicant discussed the 103 rejection and applied prior art references. No agreement was reached with respect to the claims.</u></p> <p>(A fuller description, if necessary, and a copy of the amendments which the examiner agreed would render the claims allowable, if available, must be attached. Also, where no copy of the amendments that would render the claims allowable is available, a summary thereof must be attached.)</p> <p>THE FORMAL WRITTEN REPLY TO THE LAST OFFICE ACTION MUST INCLUDE THE SUBSTANCE OF THE INTERVIEW. (See MPEP Section 713.04). If a reply to the last Office action has already been filed, APPLICANT IS GIVEN A NON-EXTENDABLE PERIOD OF THE LONGER OF ONE MONTH OR THIRTY DAYS FROM THIS INTERVIEW DATE, OR THE MAILING DATE OF THIS INTERVIEW SUMMARY FORM, WHICHEVER IS LATER, TO FILE A STATEMENT OF THE SUBSTANCE OF THE INTERVIEW. See Summary of Record of Interview requirements on reverse side or on attached sheet.</p>			
/Paul F. Contino/ Patent Examiner		16 July 2009	

DOCKET NO: 276900US8

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
JUN HAISHIMA : EXAMINER: CONTINO, P.
SERIAL NO: 11/205,121 :
FILED: AUGUST 17, 2005 : GROUP ART UNIT: 2114
FOR: INFORMATION PROCESS DEVICE :

AMENDMENT UNDER 37 C.F.R. §1.116

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated April 30, 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Currently Amended): [[An]] A gaming device configured to execute a game, the gaming information process device comprising:

a first memory device for storing a boot program executed when the gaming information process device is started to operate;

a mother board on which the first memory device is provided;

a second memory device for storing [[an]] a game application program for the game, the second memory device being connected to the mother board; and

a control device for executing a fault inspection program for the gaming device to inspect whether or not a fault occurs in the second memory device and the game application program stored therein,

wherein the fault inspection program is stored in the first memory device~~[[;]]~~, and the control device executes the fault inspection program when the gaming device is started to operate and completes the execution of the fault inspection program before the game is started.

Claim 2 (Currently Amended): The gaming information process device according to claim 1, wherein the first memory device is a ROM provided on the mother board,

wherein the second memory device is a hard disk which is independent from the mother board, and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk.

Claim 3 (Currently Amended): The ~~information process~~ gaming device according to claim 1, wherein the ~~information process device is utilized as a gaming machine is configured~~ for business use.

Claim 4 (Currently Amended): A gaming ~~machine utilizing an information process~~ device configured to execute a game, the ~~information process~~ gaming device comprising:

a first memory device configured to store a boot program executed when the information process device of the gaming machine is started to operate;

a mother board on which the first memory device is provided;

a second memory device configured to store a game application program, the second memory device being connected to the mother board and electrically rewritable;

a control device configured to execute a fault inspection program to inspect whether or not a fault occurs in the second memory device and the game application program stored therein;

wherein the fault inspection program is stored in the first memory device~~[[;]], and~~ ~~wherein~~ the control device executes the fault inspection program every time the information process device is started to operate and completes the execution of the fault inspection program before the game is started, and

wherein when the fault does not occur in the second memory device the game application program is started to execute and when the fault occurs in the second memory device an error is displayed ~~display is conducted~~ on a display device located on the exterior of the gaming machine.

Claim 5 (Currently Amended): The gaming device ~~machine~~ according to Claim 4, wherein the first memory device is a ROM provided on the mother board,

wherein the second memory device is a hard disk which is independent from the mother board, and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk.

Claim 6 (Currently Amended): The gaming device ~~machine~~ according to Claim 5, further comprising:

a RAM provided on the mother board;

wherein the game application program is loaded from the hard disk to the RAM and executed by the control device when the fault does not occur in the hard disk.

Claim 7 (Currently Amended): ~~[[An]]~~ A gaming device configured to execute a game, the gaming information process device comprising:

a first memory device for storing a boot program executed when the gaming information process device is started to operate;

a mother board on which the first memory device is provided;

a second memory device for storing ~~[[an]]~~ a game application program for the game and a BIOS, the second memory device being connected to the mother board; and

a control device for executing a fault inspection program for the gaming device to inspect whether or not a fault occurs in the second memory device and the game application program stored therein;

wherein the fault inspection program is stored in the first memory device, ~~[[;]]~~ and ~~wherein~~ the control device executes the boot program to initialize the BIOS stored in the second memory device before executing the fault inspection program when the ~~information~~

process gaming device is started to operate, and completes the execution of the fault inspection program before the game is started.

Claim 8 (New): The gaming device according to claim 1, wherein the first memory device and the mother board are located in the information process device.

Claim 9 (New): The gaming device according to claim 1, wherein the second memory device is a flash memory.

Claim 10 (New): The gaming device according to Claim 4, wherein the display device is a liquid crystal display.

REMARKS

Favorable reconsideration of this application as presently amended, and in light of the following discussion, is respectfully requested.

Claims 1-10 are pending in this application, Claims 8-10 having been added; and Claims 1-7 having been currently amended. Amended Claims 1-7 and new Claims 8-10 find non-limiting support in the disclosure as originally filed, for example at page 6, lines 1-11 and 27-30, and Fig. 1. No new matter has been added.

In the outstanding Office Action, Claims 1 and 2 were rejected under 35 U.S.C. §103(a) as unpatentable over Bizzari (U.S. Patent No. 5,732,268) in view of Alexander (U.S. Patent No. 6,393,559); Claims 3-6 were rejected under 35 U.S.C. §103(a) as unpatentable over Bizzari in view of Alexander and Yamamoto (U.S. Patent No. 6,115,036); and Claim 7 was rejected under 35 U.S.C. §103(a) as unpatentable over Bizzari in view of Alexander and Krau (U.S. Patent No. 5,864,698).

Applicant acknowledges with appreciation the courtesy of Examiner Contino in granting an interview in this case with Applicant's representative on July 16, 2009, during which time the issues in the outstanding Office Action were discussed as substantially summarized hereinafter and also on the Interview Summary Sheet. No agreement was reached during the interview pending a formal response to the outstanding Office Action.

In response to the rejections under 35 U.S.C. §103(a), Applicant respectfully submits that amended independent Claim 1 recites novel features clearly not taught or rendered obvious by the applied references.

Independent Claim 1 is directed to a gaming device including, *inter alia*:

... a first memory device for storing a boot program executed when the gaming device is started to operate;

a mother board on which the first memory device is provided;

a second memory device for storing a game application program for the game, the second memory device being connected to the mother board; and

a control device for executing a fault inspection program for the gaming device to inspect whether or not a fault occurs in the second memory device and the game application program stored therein,

wherein the fault inspection program is stored in the first memory device, and the control device executes the fault inspection program when the gaming device is started to operate and completes the execution of the fault inspection program before the game is started.

Page 3 of the outstanding Office Action asserts that column 5, lines 35-36 of Bizzari describes a “second memory device for storing an application program, the second memory device being connected to the mother board,” as recited in Applicant’s Claim 1. Column 5, lines 29-42 of Bizzari states:

There are a number of possible reasons why an E-BIOS PC might fail to boot. Perhaps chief among these, without regard to importance, are (1) a physical failure of the boot drive, (2) code corruption in the boot sector of the boot drive, (3) corruption of the file allocation table (FAT), (4) BIOS setup corrupted (usually in real-time clock, and often called CMOS), and (5) a virus infection affecting code and/or data in CMOS or on a hard disk. In case (1) the necessary correction cannot be made from the remote location, and it is necessary to send a technician to the failed computer to physically repair or change out the offending drive. In the cases of (2), (3), (4), and (5) diagnosis and repair can be made by methods of the present invention.

Thus, the above portion of Bizzari merely describes possible reasons for why an E-BIOS PC might fail to boot, and describes that a virus infection affecting code and/or data on a hard disk could be a possible reason for failure. However, Bizzari does not describe that the hard disk stores a game application program and that the hard disk is connected to a mother board. Bizzari merely describes that the hard disk contains code and/or data, but does not describe that the code or data is an application program for a game.

In addition, pages 2 and 3 of the outstanding Office Action assert that the E-BIOS in Bizzari is equivalent to both of Applicant's claimed fault inspection program and boot program. However, in Applicant's Claim 1, the boot program and the fault inspection program are distinct. Further, Bizzari fails to suggest "a control device for executing a fault inspection program for the gaming device to inspect whether or not a fault occurs in the second memory device and the game application program stored therein," as recited in Claim 1. As described above, Bizzari does not describe a game application program stored in the hard disk of Bizzari. In addition, Bizzari merely describes that there are a number of possible reasons why the E-BIOS might fail to boot, but does not describe a device which executes a fault inspection program to inspect whether or not a fault occurs in the hard disk and a game application program stored in the hard disk.

Turning now to Yamamoto, Yamamoto is directed to a videographics/video game fabricating system including a multiprocessor based game processor console. Column 26, lines 6-14 of Yamamoto states:

The main CPU 228 executes various utility programs, operating system, peripheral driver programs, and BIOS and IPL software. The utility software operated by the main CPU 228 includes game editing tools, network software, word processing software, disk management software, etc.

However, Yamamoto also fails to teach or suggest "a second memory device for storing a *game application program* for the game, the second memory device being connected to the mother board; and a control device for executing *a fault inspection program for the gaming device to inspect whether or not a fault occurs in the second memory device and the game application program* stored therein." Lastly, Applicant respectfully submits that Yamamoto does not describe that a "control device executes the fault inspection program when the gaming device is started to operate and completes the execution of the fault inspection program *before the game is started*," as recited in Claim 1.

Accordingly, Applicant respectfully submits that independent Claim 1 (and all claims depending thereon) patentably distinguishes over Bizzari in view of Yamamoto. Further, Applicant submits that Alexander and Krau fail to cure any of the above-noted deficiencies of Bizzari and Yamamoto.

Independent Claim 4 recites that “the control device executes the fault inspection program every time the information process device is started to operate and completes the execution of the fault inspection program before the game is started,” and is believed to be patentable for at least the reasons discussed above. Accordingly, Applicant respectfully submits that independent Claim 4 (and all claims depending thereon) patentably distinguishes over the above cited references.

Independent Claim 7 recites that “the control device executes the boot program to initialize the BIOS stored in the second memory device before executing the fault inspection program when the gaming device is started to operate, and completes the execution of the fault inspection program before the game is started,” and is also believed to be patentable for at least the reasons discussed above.

Accordingly, Applicant respectfully requests that the rejections under 35 U.S.C. §103 be withdrawn.

Application No. 11/205,121
Reply to Office Action of April 30, 2009

Consequently, in view of the present amendment, and in light of the above discussion, the pending claims as presented herewith are believed to be in condition for formal allowance, and an early and favorable action to that effect is respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



Bradley D. Lytle
Attorney of Record
Registration No. 40,073

Customer Number
22850

Tel: (703) 413-3000
Fax: (703) 413 -2220
(OSMMN 08/07)

Derek P. Benke
Registration No. 56,944

1265081_2.DOC

Electronic Acknowledgement Receipt

EFS ID:	5802077
Application Number:	11205121
International Application Number:	
Confirmation Number:	2910
Title of Invention:	Information process device
First Named Inventor/Applicant Name:	Jun Haishima
Customer Number:	22850
Filer:	Marvin Jay Spivak/Kim Rouse
Filer Authorized By:	Marvin Jay Spivak
Attorney Docket Number:	276900US90
Receipt Date:	30-JUL-2009
Filing Date:	17-AUG-2005
Time Stamp:	16:22:10
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
------------------------	----

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		276900USAMENDMENT.PDF	402873 <small>1f09494611b47064c6ac274e8fa246b69b620902</small>	yes	11

Multipart Description/PDF files in .zip description			
Document Description		Start	End
Miscellaneous Incoming Letter		1	1
Amendment After Final		2	2
Claims		3	6
Applicant Arguments/Remarks Made in an Amendment		7	11

Warnings:

Information:

Total Files Size (in bytes):

402873

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

Docket No. 276900US8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Jun HAISHIMA

SERIAL NO: 11/205,121

ART UNIT: 2114

FILING DATE: August 17, 2005

EXAMINER: CONTINO, P.

FOR: INFORMATION PROCESS DEVICE

FEE TRANSMITTAL

- No additional fee is required
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.

The Fee has been calculated as shown below:

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	10 - 20 =	0	x \$52 =	\$0.00
INDEPENDENT CLAIMS	3 - 3 =	0	x \$220 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$390 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
BASIC FEES				\$0.00
TOTAL OF ABOVE CALCULATIONS				\$0.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
TOTAL				\$0.00

- Please charge Deposit Account No. **15-0030** in the amount of **\$0.00**
- Credit card payment is being made online (if electronically filed), or is attached hereto (if paper filed), in the amount of **\$0.00**.
- The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no payment is enclosed herewith, or credit any overpayment to Deposit Account No. **15-0030**.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030.

Submitted by:



Bradley D. Lytle
Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 10/08)

Derek P. Benke
Registration No. 56,944

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875					Application or Docket Number 11/205,121		Filing Date 08/17/2005		<input type="checkbox"/> To be Mailed					
APPLICATION AS FILED – PART I														
(Column 1)			(Column 2)			SMALL ENTITY <input type="checkbox"/>		OR			OTHER THAN SMALL ENTITY			
FOR		NUMBER FILED	NUMBER EXTRA		RATE (\$)	FEE (\$)	OR		RATE (\$)	FEE (\$)				
<input checked="" type="checkbox"/> BASIC FEE <small>(37 CFR 1.16(a), (b), or (c))</small>		N/A	N/A		N/A				N/A	300				
<input type="checkbox"/> SEARCH FEE <small>(37 CFR 1.16(k), (l), or (m))</small>		N/A	N/A		N/A		N/A							
<input type="checkbox"/> EXAMINATION FEE <small>(37 CFR 1.16(o), (p), or (q))</small>		N/A	N/A		N/A		N/A							
TOTAL CLAIMS <small>(37 CFR 1.16(i))</small>		minus 20 =	*		X \$ =		OR	X \$ =						
INDEPENDENT CLAIMS <small>(37 CFR 1.16(h))</small>		minus 3 =	*		X \$ =		OR	X \$ =						
<input type="checkbox"/> APPLICATION SIZE FEE <small>(37 CFR 1.16(s))</small>		If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).												
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENT <small>(37 CFR 1.16(j))</small>														
					TOTAL				TOTAL		300			
* If the difference in column 1 is less than zero, enter "0" in column 2.														
APPLICATION AS AMENDED – PART II														
(Column 1)			(Column 2)			(Column 3)			SMALL ENTITY		OR		OTHER THAN SMALL ENTITY	
AMENDMENT	07/30/2009		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE (\$)	ADDITIONAL FEE (\$)	OR		RATE (\$)	ADDITIONAL FEE (\$)		
	Total <small>(37 CFR 1.16(i))</small>		* 10	Minus	** 20	= 0	X \$ =				OR	X \$2=	0	
	Independent <small>(37 CFR 1.16(h))</small>		* 3	Minus	***3	= 0	X \$ =		OR	X \$220=	0			
	<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>													
	<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>													
					TOTAL ADD'L FEE				OR		TOTAL ADD'L FEE		0	
AMENDMENT			CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE (\$)	ADDITIONAL FEE (\$)	OR		RATE (\$)	ADDITIONAL FEE (\$)		
	Total <small>(37 CFR 1.16(i))</small>		*	Minus	**	=	X \$ =				OR	X \$ =		
	Independent <small>(37 CFR 1.16(h))</small>		*	Minus	***	=	X \$ =		OR	X \$ =				
	<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>													
	<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>													
					TOTAL ADD'L FEE				OR		TOTAL ADD'L FEE			
* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.														
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".														
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".														
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.														

Legal Instrument Examiner:
/TAMMY MCBETH BROWN/

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

DOCKET NO: 276900US8

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
JUN HAISHIMA : EXAMINER: CONTINO, P.
SERIAL NO: 11/205,121 :
FILED: AUGUST 17, 2005 : GROUP ART UNIT: 2114
FOR: INFORMATION PROCESS DEVICE :

Please DO NOT ENTER

/dml/

8/15/09

AMENDMENT UNDER 37 C.F.R. §1.116

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated April 30, 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with 5 columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO. Includes sub-tables for EXAMINER, ART UNIT, PAPER NUMBER, NOTIFICATION DATE, and DELIVERY MODE.

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

patentdocket@oblon.com
oblonpat@oblon.com
jgardner@oblon.com

**Advisory Action
Before the Filing of an Appeal Brief**

Application No.

11/205,121

Applicant(s)

HAISHIMA, JUN

Examiner

Dieu-Minh Le

Art Unit

2114

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 30 July 2009 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

a) The period for reply expires 3 months from the mailing date of the final rejection.

b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because

(a) They raise new issues that would require further consideration and/or search (see NOTE below);

(b) They raise the issue of new matter (see NOTE below);

(c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or

(d) They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: The newly proposed limitations that "completes the execution of the fault inspection program before the game is started" [added in claims 1.4 & 7] have not been previously claimed. These limitations would require further consideration/search. (See 37 CFR 1.116 and 41.33(a)).

4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).

5. Applicant's reply has overcome the following rejection(s): _____.

6. Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

7. For purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: none.

Claim(s) objected to: none.

Claim(s) rejected: 1-7.

Claim(s) withdrawn from consideration: none.

AFFIDAVIT OR OTHER EVIDENCE

8. The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).

9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).

10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because: _____.

12. Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). _____

13. Other: _____.

/Dieu-Minh Le/
Primary Examiner, Art Unit 2114

Docket No.: 276900US8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Jun HAISHIMA

SERIAL NO: 11/205,121

GAU: 2114

FILED: August 17, 2005

EXAMINER: LE, D.M.

FOR: INFORMATION PROCESS DEVICE

REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

This is a Request for Continued Examination (RCE) under 37 C.F.R. §1.114 of the above-identified application.

Submission required under 37 C.F.R. §1.114

Previously Submitted:

- Consider the amendment(s)/reply under 37 C.F.R. §1.116 previously filed on July 30, 2009
- Consider the arguments in the Appeal Brief or Reply Brief previously filed on

Enclosed:

- Amendment/Reply
- Information Disclosure Statement (IDS)
- Other: Request for Extension of Time (1 month)

FEES	RATE	CALCULATIONS
<input type="checkbox"/> Suspension of action on the above-identified application is requested under 37 C.F.R. §1.103(c) for a period of _____ months.	\$130.00	\$0.00
<input checked="" type="checkbox"/> RCE Fee required under 37 C.F.R. §1.17(e)	\$810.00	\$810.00
<input checked="" type="checkbox"/> A ONE MONTH EXTENSION OF TIME IS REQUESTED		\$130.00
<input type="checkbox"/>		\$0.00
TOTAL OF ABOVE CALCULATIONS:		\$940.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING AS SMALL ENTITY		\$0.00
TOTAL:		\$940.00

- Credit card payment is being made online (if electronically filed), or is attached hereto (if paper filed), in the amount of **\$940.00**.

- Please charge any additional Fees for the papers being filed herewith and for which no payment is enclosed herewith, or credit any overpayment to Deposit Account No. **15-0030**.

- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 CFR 1.136, and any additional fees required under 37 CFR 1.136 for any necessary extension of time may be charged to Deposit Account No. **15-0030**.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, L.L.P.



Bradley D. Lytle

Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 07/09)

Derek P. Benke

Registration No. 56,944

Docket No. 276900US8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Jun HAISHIMA

SERIAL NO: 11/205,121

GAU: 2114

FILED: August 17, 2005

EXAMINER: LE, D.M.

FOR: INFORMATION PROCESS DEVICE

REQUEST FOR EXTENSION OF TIME
UNDER 37 C.F.R. 1.136

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

It is hereby requested that a **one** month extension of time be granted to August 30, 2009 for

- filing a response to the Official Action dated:
- responding to the requirements in the Notice of Allowability dated:
- filing the Formal Drawings. The Issue Fee due has been timely filed.
- responding to the Notice to File Missing Parts of Application dated:
- filing a Request for Continued Examination (RCE). A response to the final rejection was filed on July 30, 2009.
- filing an Appeal Brief. A Notice of Appeal was filed on:
- Applicant claims small entity status. See 37 CFR 1.27.

The required fee of \$130.00 is being made by credit card payment online (if electronically filed), or is attached hereto (if paper filed), and any further charges may be made against the Attorney of Record's Deposit Account No. **15-0030**.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, L.L.P.



Bradley D. Lytle

Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 07/09)

Derek P. Benke

Registration No. 56,944

Electronic Patent Application Fee Transmittal

Application Number:	11205121			
Filing Date:	17-Aug-2005			
Title of Invention:	Information process device			
First Named Inventor/Applicant Name:	Jun Haishima			
Filer:	Marvin Jay Spivak/Kim Rouse			
Attorney Docket Number:	276900US90			
Filed as Large Entity				
Utility under 35 USC 111(a) Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 1 month with \$0 paid	1251	1	130	130

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Total in USD (\$)				940

Electronic Acknowledgement Receipt

EFS ID:	5959566
Application Number:	11205121
International Application Number:	
Confirmation Number:	2910
Title of Invention:	Information process device
First Named Inventor/Applicant Name:	Jun Haishima
Customer Number:	22850
Filer:	Marvin Jay Spivak/Kim Rouse
Filer Authorized By:	Marvin Jay Spivak
Attorney Docket Number:	276900US90
Receipt Date:	26-AUG-2009
Filing Date:	17-AUG-2005
Time Stamp:	17:08:26
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$940
RAM confirmation Number	3476
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
-----------------	----------------------	-----------	-------------------------------------	------------------	------------------

1		276900USRCE.PDF	82403 f38be8b90a9fcb6a722cad20ace24c8530b a229	yes	2
Multipart Description/PDF files in .zip description					
		Document Description	Start	End	
		Request for Continued Examination (RCE)	1	1	
		Extension of Time	2	2	
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	31862 979737fb1c38774e7816bc9620f6389a057 b9171	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			114265		
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875					Application or Docket Number 11/205,121		Filing Date 08/17/2005		<input type="checkbox"/> To be Mailed			
APPLICATION AS FILED – PART I												
(Column 1)			(Column 2)			SMALL ENTITY <input type="checkbox"/>		OR			OTHER THAN SMALL ENTITY	
FOR		NUMBER FILED	NUMBER EXTRA		RATE (\$)	FEE (\$)	OR		RATE (\$)	FEE (\$)		
<input type="checkbox"/> BASIC FEE <small>(37 CFR 1.16(a), (b), or (c))</small>		N/A	N/A		N/A				N/A			
<input type="checkbox"/> SEARCH FEE <small>(37 CFR 1.16(k), (l), or (m))</small>		N/A	N/A		N/A		N/A					
<input type="checkbox"/> EXAMINATION FEE <small>(37 CFR 1.16(o), (p), or (q))</small>		N/A	N/A		N/A		N/A					
TOTAL CLAIMS <small>(37 CFR 1.16(i))</small>		minus 20 =	*		X \$ =		OR		X \$ =			
INDEPENDENT CLAIMS <small>(37 CFR 1.16(h))</small>		minus 3 =	*		X \$ =		OR		X \$ =			
<input type="checkbox"/> APPLICATION SIZE FEE <small>(37 CFR 1.16(s))</small>		If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).										
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENT <small>(37 CFR 1.16(j))</small>												
* If the difference in column 1 is less than zero, enter "0" in column 2.												
APPLICATION AS AMENDED – PART II					SMALL ENTITY		OR			OTHER THAN SMALL ENTITY		
(Column 1)		(Column 2)		(Column 3)		RATE (\$)	ADDITIONAL FEE (\$)	OR		RATE (\$)	ADDITIONAL FEE (\$)	
08/26/2009		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR								
Total <small>(37 CFR 1.16(i))</small>		* 10		Minus ** 20		= 0		OR		X \$20= 0		
Independent <small>(37 CFR 1.16(h))</small>		* 3		Minus *** 3		= 0		OR		X \$220= 0		
<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>												
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>												
					TOTAL ADD'L FEE		OR			TOTAL ADD'L FEE		
										0		
(Column 1)		(Column 2)		(Column 3)		RATE (\$)	ADDITIONAL FEE (\$)	OR		RATE (\$)	ADDITIONAL FEE (\$)	
		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR								
Total <small>(37 CFR 1.16(i))</small>		*		Minus **		=		OR		X \$ =		
Independent <small>(37 CFR 1.16(h))</small>		*		Minus ***		=		OR		X \$ =		
<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>												
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>												
					TOTAL ADD'L FEE		OR			TOTAL ADD'L FEE		
* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.												
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".												
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".												
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.												
Legal Instrument Examiner: /Debra R. Wyatt/												

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Entered/WRCE
8-26-09
Dwyatt
8-28-09

DOCKET NO: 276900US8

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
JUN HAISHIMA : EXAMINER: CONTINO, P.
SERIAL NO: 11/205,121 :
FILED: AUGUST 17, 2005 : GROUP ART UNIT: 2114
FOR: INFORMATION PROCESS DEVICE :

~~Please DO NOT ENTER~~

/dm1/
8/15/09

AMENDMENT UNDER 37 C.F.R. §1.116

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated April 30, 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

NOTICE OF ALLOWANCE AND FEE(S) DUE

22850 7590 09/25/2009

OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L.P.
1940 DUKE STREET
ALEXANDRIA, VA 22314

EXAMINER

LE, DIEU MINH T

ART UNIT PAPER NUMBER

2114

DATE MAILED: 09/25/2009

Table with 5 columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO.
11/205,121 08/17/2005 Jun Haishima 276900US90 2910

TITLE OF INVENTION: INFORMATION PROCESS DEVICE

Table with 7 columns: APPLN. TYPE, SMALL ENTITY, ISSUE FEE DUE, PUBLICATION FEE DUE, PREV. PAID ISSUE FEE, TOTAL FEE(S) DUE, DATE DUE
nonprovisional NO \$1510 \$300 \$0 \$1810 12/28/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

- A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.
B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

- A. Pay TOTAL FEE(S) DUE shown above, or
B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

**Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 or Fax (571)-273-2885**

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

22850 7590 09/25/2009

OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L.P.
 1940 DUKE STREET
 ALEXANDRIA, VA 22314

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

_____ (Depositor's name)
_____ (Signature)
_____ (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/205,121	08/17/2005	Jun Haishima	276900US90	2910

TITLE OF INVENTION: INFORMATION PROCESS DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	12/28/2009

EXAMINER	ART UNIT	CLASS-SUBCLASS
LE, DIEU MINH T	2114	714-036000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). <input type="checkbox"/> Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. <input type="checkbox"/> "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.	2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, 1 _____ (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. 2 _____ 3 _____
--	--

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE _____ (B) RESIDENCE: (CITY and STATE OR COUNTRY) _____

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are submitted: <input type="checkbox"/> Issue Fee <input type="checkbox"/> Publication Fee (No small entity discount permitted) <input type="checkbox"/> Advance Order - # of Copies _____	4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) <input type="checkbox"/> A check is enclosed. <input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached. <input type="checkbox"/> The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).
--	---

5. Change in Entity Status (from status indicated above)

a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature _____ Date _____
 Typed or printed name _____ Registration No. _____

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with 5 columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO.

22850 7590 09/25/2009
OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L.P.
1940 DUKE STREET
ALEXANDRIA, VA 22314

EXAMINER

LE, DIEU MINH T

ART UNIT PAPER NUMBER

2114
DATE MAILED: 09/25/2009

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 510 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 510 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Notice of Allowability	Application No.	Applicant(s)	
	11/205,121	HAISHIMA, JUN	
	Examiner	Art Unit	
	Dieu-Minh Le	2114	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to the communication filed 8/26/09.
2. The allowed claim(s) is/are 1-10.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____ .
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|--|
| <ol style="list-style-type: none"> 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ . 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____. |
|--|--|

/Dieu-Minh Le/
 Primary Examiner, Art Unit 2114

Notice of References Cited	Application/Control No. 11/205,121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN	
	Examiner Dieu-Minh Le	Art Unit 2114	Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A US-6,011,564	01-2000	Furuhashi et al.	345/501
*	B US-5,701,478	12-1997	Chen, Yi-Rong	713/2
	C US-			
	D US-			
	E US-			
	F US-			
	G US-			
	H US-			
	I US-			
	J US-			
	K US-			
	L US-			
	M US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N				
	O				
	P				
	Q				
	R				
	S				
	T				

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	
V	
W	
X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

EAST Search History

EAST Search History (I nterference)


Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L20	837	(boot\$3 bios initializ\$5) with (program\$5 software application) with game	USPAT; UPAD	OR	ON	2009/09/22 11:56
L21	3168	(fault\$3 error\$3 fail\$3 corrupt\$3 problem malfunction defect \$3 bug) with inspect\$3 with (program application)	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 11:56
L22	349466	(inspect\$3 check \$3 monitor\$3 track\$3) with (program application)	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:01
L23	68574	(rom eprom eeprom harddisk harddrive harddisc hard-disk hard-drive hard-disc "hard disc" "hard drive" "hard disk" "mother board") same (fault\$3 error\$5 fail\$3 corrupt\$3 problem malfunction defect \$3 bug\$4)	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:01
L24	432	(714/36).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:02

L25	372	(714/27).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:02
L26	382	(714/3).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:02
L27	1517	(714/5).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:02
L28	432	(714/36).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:03
L29	1702	(714/47).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:03
L30	1566	(714/48).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:03
L31	3413	(713/2).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:03
L32	1363	(710/104).ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:03
L33	0	20 and 21	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:04
L34	322	21 and 22 and 23	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:05
L35	0	34 and 20 and (24 25 26 27 28)	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:05

L36	0	34 and 20 and (29 30 31 32)	US-PGPUB; USPAT; UPAD	OR	ON	2009/09/22 12:06
L37	0	((boot\$3 bios initializ\$5) same (program\$5 software application) same (gam\$3 vediogam \$3)) and 34 and ("714"/\$.cls.)	USPAT; UPAD	OR	ON	2009/09/22 12:07
L38	0	((boot\$3 bios initializ\$5) same (program\$5 software application) same (gam\$3 vediogam \$3)) and 34 and ("710"/\$.cls.)	USPAT; UPAD	OR	ON	2009/09/22 12:08
L39	0	((boot\$3 bios initializ\$5) same (program\$5 software application) same (gam\$3 vediogam \$3)) and 34 and ("713"/\$.cls.)	USPAT; UPAD	OR	ON	2009/09/22 12:08

9/ 22/ 09 12:08:31 PM


C:\ Documents and Settings\ MLe\ My Documents\ EAST\ Workspaces\ 11205121.wsp

Index of Claims 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner PAUL F. CONTINO	Art Unit 2114

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

Claims renumbered in the same order as presented by applicant
 CPA
 T.D.
 R.1.47

CLAIM		DATE							
Final	Original	03/25/2008	10/28/2008	04/20/2009	09/22/2009				
1	1	✓	✓	✓	=				
2	2	✓	✓	✓	=				
3	3	✓	✓	✓	=				
6	4		✓	✓	=				
7	5		✓	✓	=				
8	6		✓	✓	=				
10	7			✓	=				
4	8				=				
5	9				=				
9	10				=				

Search Notes 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner PAUL F. CONTINO	Art Unit 2114

SEARCHED			
Class	Subclass	Date	Examiner
714	36,27,3,5,47	9/22/09	dml
710	104	9/22/09	dml
713	2	9/22/09	dml

SEARCH NOTES		
Search Notes	Date	Examiner
EAST search all files USPAT, USPGPUBM USOCR, FPRS, EPO, JPO, DERWENT, IBM-TDB updated	9/22/09	dml
NPL search [IEEE, Google Advanced Search]	9/22/09	dml
Inventor search	9/22/09	dml
SEARCHED subclasses updated	9/22/09	dml

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner
714	36,27,3,5,47	9/22/09	dml
710	104	9/22/09	dml
713	2	9/22/09	dml

	/Dieu-Minh Le/ Primary Examiner.Art Unit 2114
--	--

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	41083	(inspect\$3 check\$3 monitor\$3 track\$3) adj (program application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:37
L8	1893	(boot\$3 bios initializ\$5) with (program\$5 software application) with game	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:41
L9	102850	(rom eprom eeprom harddisk harddrive harddisc hard-disk hard-drive hard-disc "hard disc" "hard drive" "hard disk" "mother board") same (fault\$3 error\$5 fail\$3 corrupt\$3 problem malfunction defect\$3 bug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:41
L10	25	L9 same L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:41
L11	2	7 and L10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:41
L12	12060	(before ahead prior previous) with (gam\$3 vediogam\$3) with (start\$3 begin\$4 operat\$3 execut\$3 run\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:46
L14	7	(haishima-jun\$).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:50

L15	3149	(713/2).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:51
L16	1336	(710/104).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:52
L17	1242	(710/36).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:52
L18	169	(710/27).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:52
L19	3718	(714/27,36,47,48).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 11:53
S1	20	("20040078697" "20050246586" "20070168738" "5732268" "5860122" "5864698" "5971851" "6115036" "6393559" "6449735").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/21 14:32
S2	180	(fault\$3 error\$3 fail\$3 problem malfunction defect \$3 bug) near2 inspect\$3 near (program application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/21 14:34
S3	20235	(complet\$3 finish\$3 done) same game same (start\$3 operat\$3 execut\$3 operat \$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/21 14:35

S4	0	S2 same S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/21 14:35
S5	0	S2 and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/21 14:36
S6	419	(714/36).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/21 14:36
S7	358	(714/27).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/21 14:36
S8	7130	(714/3,5,38,47,48).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/21 14:38
S9	1893	(boot\$3 bios initializ\$5) with (program\$5 software application) with game	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:28
S10	102850	(rom eprom eeprom harddisk harddrive harddisc hard-disk hard-drive hard- disc "hard disc" "hard drive" "hard disk" "mother board") same (fault\$3 error\$5 fail\$3 corrupt\$3 problem malfunction defect\$3 bug \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:41
S11	25	S10 same S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:44

S12	180	(fault\$3 error\$3 fail\$3 problem malfunction defect\$3 bug) near2 inspect\$3 near (program application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:45
S13	0	S11 and S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:45
S14	4385	(fault\$3 error\$3 fail\$3 corrupt\$3 problem malfunction defect\$3 bug) with inspect\$3 with (program application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:46
S15	0	S11 and S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:46
S16	48801	inspect\$3 with (program application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:47
S17	0	S11 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:47
S18	409730	(inspect\$3 check\$3 monitor\$3 track\$3) with (program application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:47
S19	22	S11 and S18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:55
S20	22	S19 and (gam\$3 videogam\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/22 10:56

9/ 22/ 09 11:55:27 AM

C:\ Documents and Settings\ MLe\ My Documents\ EAST\ Workspaces\ 11205121.wsp




UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIB DATA SHEET

CONFIRMATION NO. 2910

SERIAL NUMBER 11/205,121	FILING or 371(c) DATE 08/17/2005 RULE	CLASS 714	GROUP ART UNIT 2114	ATTORNEY DOCKET NO. 276900US90	
APPLICANTS Jun Haishima, Tokyo, JAPAN;					
** CONTINUING DATA ***** na dml					
** FOREIGN APPLICATIONS ***** JAPAN 2004-245337 08/25/2004 yes dml					
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/02/2005					
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged /DIEU-MINH T LE/ Examiner's Signature	<input type="checkbox"/> Met after Allowance dml Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 3	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 3
ADDRESS OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L.P. 1940 DUKE STREET ALEXANDRIA, VA 22314 UNITED STATES					
TITLE Information process device					
FILING FEE RECEIVED 1000	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		

Issue Classification 	Application/Control No. 11205121	Applicant(s)/Patent Under Reexamination HAISHIMA, JUN
	Examiner Dieu-Minh Le	Art Unit 2114

ORIGINAL						INTERNATIONAL CLASSIFICATION											
CLASS			SUBCLASS			CLAIMED				NON-CLAIMED							
714			36			G	O	6	F	11 / 00 (2006.01.01)							
CROSS REFERENCE(S)																	
CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)															
713	2																

<input type="checkbox"/> Claims renumbered in the same order as presented by applicant <input type="checkbox"/> CPA <input type="checkbox"/> T.D. <input type="checkbox"/> R.1.47															
Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original
1	1														
2	2														
3	3														
6	4														
7	5														
8	6														
10	7														
4	8														
5	9														
9	10														

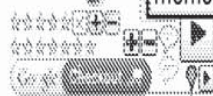
NONE	Total Claims Allowed:	
(Assistant Examiner)	(Date)	10
/Dieu-Minh Le/ Primary Examiner. Art Unit 2114	09/22/2009	O.G. Print Claim(s) O.G. Print Figure
(Primary Examiner)	(Date)	1 1



memory stored boot program control device completed executing fault inspectio

Search

Advanced Search



Web Results 11 - 20 of about 26,300 for memory stored boot program control device completed executing fault inspection program before game started. (0.38 seconds)



Show options...

1. [PDF] [Malicious Code Detection for Open Firmware](#)

File Format: PDF/Adobe Acrobat - [View](#)

started. Boot firmware is the **program** that controls this pro- ... eral devices. The **boot program** runs **before** the operating that represents this device. That foode **program** is **stored** in ensure the basic properties of **memory**, **control** flow, and Towards **fault-tolerant** and secure agen- try. In Proc. ...

www.cs.cornell.edu/~kozen/papers/acsac.pdf - [Similar](#)

by F Adelman - 2002 - Cited by 13 - [Related articles](#)

2. [Embedded Control Systems Design/Operating systems - Wikibooks ...](#)

Apr 11, 2009 ... An operating system is a computer **program** that manages the hard and ... as we first **start** with a more general approach of operating systems. appear are perhaps a divide by zero, a **memory** segmentation **fault**, etc. Namespace:

before writing any code it is important to name your **device**. **Memory** ...

en.wikibooks.org/wiki/...Control.../Operating_systems - [Cached](#) - [Similar](#)

3. [PDF] [Fault-Tolerant Systems in A Space Environment: The CRC ARGOS ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

(**executing** pieces of code two or more times), assigned signature **control** flow ... that stores the **boot program** and the operating system in a compressed format. This **complete** information will be given to the Collector to be **stored** and modern operating systems take advantage of that for **memory** protection. ...

ftp://reports.stanford.edu/pub/ctrl/reports/csl/.../CSL-TR-96-774.pdf - [Similar](#)

by PP Shirvani - [Related articles](#) - [All 11 versions](#)

4. [Backup Encyclopedia.: V](#)

Program code and data are **stored** in random access **memory** (RAM) chips - also known ... copy the least recently used pages of **memory** to the hard disk drive, even **before** an ... to reflect the virtual address space of the currently **executing** processes, ... The Volume **Boot** Record (also known as the Volume **Boot** Sector or ...

www.backupencyclopedia.com/glossary/v/v.html - [Cached](#) - [Similar](#)

5. [PDF] [BodySnatcher: Towards reliable volatile memory acquisition by software](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

liably acquiring host **memory** by snatching **complete control** ... configured to be used as a **memory acquisition device**. ... contents of **memory** are **stored** in a file. Such **memory** tion OS executor into **memory**, then **executing** the acquisition ...

Switching **program** flow from the host OS kernel to the acqui- ...

www.dirws.org/2007/proceedings/p126-schatz.pdf - [Similar](#)

by B Schatz - [Cited by 10](#) - [Related articles](#) - [All 6 versions](#)

6. [\[PDF\] TransmetaTM CrusoeTM Hardware, Software, and Development Abstract ...](#)

File Format: PDF/Adobe Acrobat - [View](#)

der software **control**, allowing an individual operation to **memory** is **stored** in a target-only part of host RAM. Sec- ... dled by **executing** instructions one-at-a-time until the **fault** grade requires **boot** in a minute or less. Crusoe is tuned to ... was during **program start** when exceptions were frequent. ...

amas-bt.cs.virginia.edu/2008proceedings/3-keppel-paper.pdf - [Similar](#)

7. [\[PDF\] /tardir/tiffs/a416770.tiff](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

safe **memory** management options. **Before**, we had to restrict programmers ... to **execute**, and this decision is beyond the programmers **control**. Nonde- ... is called **boot** firmware and is typically **stored** in ROM or other non-volatile **memory**. ... typically **device** drivers, will be loaded into the **boot program** and must ...

handle.dtic.mil/100.2/ADA416770 - [Similar](#)

by FB Schneider - 2003 - [Cited by 2](#) - [Related articles](#) - [All 7 versions](#)

8. [\[PDF\] INSTALLATION GUIDE System Console Software 2.0](#)

File Format: PDF/Adobe Acrobat - [View](#)

The unit of the **program** in which the system is composed is called the package. ... information for system **fault** analysis to a backup tape **device**. 0.7 Using the **memory** error **inspection** function. (Solaris 7/Solaris 8 **Execute** the following command to **start** the daemon. ... All uninstallation **completed**. ...

sysdoc.doors.ch/FUJITSUSIEMENS/scs_2_0_inst.pdf - [Similar](#)

9. [\[PDF\] The Parallel Board Tester Combining Parallel Boundary-Scan and ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

and test software **executing** on the test objects. The test setup ... **boot program** via boundary- scan. This **boot** is an in- ... The EBoot is **stored** in flash **memory** and the DEST in RAM. advanced quality **control**, it would be possible to skip the ICT test. ... ports to a presentation and storing system saves **fault**- ...

[www.mcoisystems.com/.../Paralle%20Board%20Tester%20\(3.2\).pdf](http://www.mcoisystems.com/.../Paralle%20Board%20Tester%20(3.2).pdf) - [Similar](#)

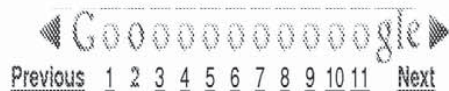
10. [Computer Programming Software Terms, Glossary and Dictionary - D ...](#)

The Daemon **program**, often **started** at the time the system boots and runs

memory stored boot program control device completed executing fault inspection program before game started - Google Search

continuously without ... as bits and bytes **stored** in electronic **memory**, or as facts **stored** in a person's mind. ... Data elements usage can be discovered by **inspection** of software create and remove breakpoints, and **execute** your **program**. ...
www.networkdictionary.com/software/d.php - [Cached](#) - [Similar](#)

Tip: These results **do not include** the word "game". [Show results that include "game"](#).



memory stored boot program control device completed executing fault inspectio

[Search within results](#) - [Language Tools](#) - [Search Help](#) - [Dissatisfied? Help us improve](#) - [Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)

Tue, 22 Sep 2009, 1:48:46 PM EST

Edit an existing query or compose a new query in the Search Query Display.

Search Query Display

Select a search number (#) to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

Results

#	Query	Results
#1	((boot program executed)<in>metadata) <and> ((fault occurs second memory)<in>metadata) <and> ((before game started)<in>metadata)	0
#2	((boot program executed)<in>metadata) <and> ((fault occurs second memory)<in>metadata) <and> ((before game started)<in>metadata)	0
#3	((fault inspection program)<in>metadata) <and> ((error display)<in>metadata) <and> ((exterior gaming machine)<in>metadata)	0
#4	((fault inspection program)<in>metadata) <and> ((stored mother board)<in>metadata) <and> ((executed before game started)<in>metadata)	0
#5	((boot program)<in>metadata) <and> ((control device)<in>metadata) <and> ((fault inspection program)<in>metadata)	0
#6	((bios harddisk)<in>metadata) <and> ((fault inspection program)<in>metadata) <and> ((before game started)<in>metadata)	0





UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2910

SERIAL NUMBER 11/205,121	FILING OR 371(c) DATE 08/17/2005 RULE	CLASS 714	GROUP ART UNIT 2114	ATTORNEY DOCKET NO. 276900US90		
APPLICANTS Jun Haishima, Tokyo, JAPAN;						
** CONTINUING DATA *****						
** FOREIGN APPLICATIONS ***** JAPAN 2004-245337 08/25/2004						
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/02/2005						
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged Examiner's Signature _____ Initials _____	STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 3	INDEPENDENT CLAIMS 1
ADDRESS 22850						
TITLE Gaming apparatus having memory fault detection						
FILING FEE RECEIVED 1000	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit			

us
10/13/09

Docket No.: 276900US8/jkl

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Jun HAISHIMA

SERIAL NUMBER: 11/205,121

GROUP: 2114

FILED: August 17, 2005

EXAMINER: LE, DIEU MINH T.

FOR: **GAMING APPARATUS HAVING MEMORY FAULT DETECTION**

AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. § 1.312

MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

SIR:

In response to the Notice of Allowability dated September 25, 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Previously Presented): A gaming device configured to execute a game, the gaming device comprising:

a first memory device for storing a boot program executed when the gaming device is started to operate;

a mother board on which the first memory device is provided;

a second memory device for storing a game application program for the game, the second memory device being connected to the mother board; and

a control device for executing a fault inspection program for the gaming device to inspect whether or not a fault occurs in the second memory device and the game application program stored therein,

wherein the fault inspection program is stored in the first memory device, and the control device executes the fault inspection program when the gaming device is started to operate and completes the execution of the fault inspection program before the game is started.

Claim 2 (Previously Presented): The gaming device according to claim 1, wherein the first memory device is a ROM provided on the mother board,

wherein the second memory device is a hard disk which is independent from the mother board, and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk.

Claim 3 (Currently Amended): The gaming device according to claim 1, wherein the gaming ~~machine~~ device is configured for business use.

Claim 4 (Currently Amended): A gaming device configured to execute a game, the gaming device comprising:

a first memory device configured to store a boot program executed when ~~the information process device~~ of the gaming ~~machine~~ device is started to operate;

a mother board on which the first memory device is provided;

a second memory device configured to store a game application program, the second memory device being connected to the mother board and electrically rewritable;

a control device configured to execute a fault inspection program to inspect whether or not a fault occurs in the second memory device and the game application program stored therein;

wherein the fault inspection program is stored in the first memory device, and the control device executes the fault inspection program every time the ~~information process~~ gaming device is started to operate and completes the execution of the fault inspection program before the game is started, and

wherein when the fault does not occur in the second memory device the game application program is started to execute and when the fault occurs in the second memory device an error is displayed on a display device located on ~~the~~ an exterior of the gaming ~~machine~~ device.

Claim 5 (Previously Presented): The gaming device according to Claim 4, wherein the first memory device is a ROM provided on the mother board,

wherein the second memory device is a hard disk which is independent from the mother board, and

wherein the control device executes the fault inspection program stored in the ROM to inspect whether or not the fault occurs in the hard disk.

Claim 6 (Previously Presented): The gaming device according to Claim 5, further comprising:

a RAM provided on the mother board;

wherein the game application program is loaded from the hard disk to the RAM and executed by the control device when the fault does not occur in the hard disk.

Claim 7 (Previously Presented): A gaming device configured to execute a game, the gaming device comprising:

a first memory device for storing a boot program executed when the gaming device is started to operate;

a mother board on which the first memory device is provided;

a second memory device for storing a game application program for the game and a BIOS, the second memory device being connected to the mother board; and

a control device for executing a fault inspection program for the gaming device to inspect whether or not a fault occurs in the second memory device and the game application program stored therein;

wherein the fault inspection program is stored in the first memory device, and the control device executes the boot program to initialize the BIOS stored in the second memory device before executing the fault inspection program when the gaming device is started to

operate, and completes the execution of the fault inspection program before the game is started.

Claim 8 (Currently Amended): The gaming device according to claim 1, wherein the first memory device and the mother board are located in the ~~information process~~ gaming device.

Claim 9 (Previously Presented): The gaming device according to claim 1, wherein the second memory device is a flash memory.

Claim 10 (Previously Presented): The gaming device according to Claim 4, wherein the display device is a liquid crystal display.

REMARKS/ARGUMENTS

Entry of this Amendment After Allowance under 37 C.F.R. § 1.312 is respectfully requested since it does not add any new matter. More specifically, the amendment to claims 3, 4 and 8 corrects antecedent errors. These antecedent errors in the claims were discovered by Applicant's attorney during a post-allowance review of the claims, and the attorney has been diligent in correcting any informalities that he has become aware of.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, L.L.P.



Bradley D. Lytle
Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 07/09)

Craig R. Feinberg
Registration No. 62,116

Electronic Acknowledgement Receipt

EFS ID:	6596434
Application Number:	11205121
International Application Number:	
Confirmation Number:	2910
Title of Invention:	Gaming apparatus having memory fault detection
First Named Inventor/Applicant Name:	Jun Haishima
Customer Number:	22850
Filer:	Marvin Jay Spivak/Jerri Klatecki
Filer Authorized By:	Marvin Jay Spivak
Attorney Docket Number:	276900US90
Receipt Date:	08-DEC-2009
Filing Date:	17-AUG-2005
Time Stamp:	16:58:59
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
------------------------	----

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		276900USAMN312.pdf	197106 <small>09b624ad6a9dde84eece4812d4be1c85dc75952f4</small>	yes	7

Multipart Description/PDF files in .zip description		
Document Description	Start	End
Miscellaneous Incoming Letter	1	1
Amendment after Notice of Allowance (Rule 312)	2	2
Claims	3	6
Applicant Arguments/Remarks Made in an Amendment	7	7
Warnings:		
Information:		
Total Files Size (in bytes):	197106	
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>		

Docket No. 276900US8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Jun HAISHIMA

SERIAL NO: 11/205,121

ART UNIT: 2114

FILING DATE: August 17, 2005

EXAMINER: LE, DIEU MINH T.

FOR: **GAMING APPARATUS HAVING MEMORY FAULT DETECTION**

FEE TRANSMITTAL

- No additional fee is required
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.

The Fee has been calculated as shown below:

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	10 - 20 =	0	x \$52 =	\$0.00
INDEPENDENT CLAIMS	3 - 3 =	3	x \$220 =	\$660.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$390 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
BASIC FEES				\$0.00
TOTAL OF ABOVE CALCULATIONS				\$660.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
TOTAL				\$660.00

- Please charge Deposit Account No. **15-0030** in the amount of _____
- Credit card payment is being made online (if electronically filed), or is attached hereto (if paper filed), in the amount of _____
- The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no payment is enclosed herewith, or credit any overpayment to Deposit Account No. **15-0030**, with the **EXCEPTION** of deficiencies in fees for multiple dependent claims in new applications.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030.

Submitted by: _____



Bradley D. Lytle
Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 09/09)

Craig R. Feinberg
Registration No. 62,116

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875					Application or Docket Number 11/205,121		Filing Date 08/17/2005		<input type="checkbox"/> To be Mailed					
APPLICATION AS FILED – PART I														
(Column 1)			(Column 2)			SMALL ENTITY <input type="checkbox"/>		OR		OTHER THAN SMALL ENTITY				
FOR		NUMBER FILED	NUMBER EXTRA		RATE (\$)	FEE (\$)	OR		RATE (\$)	FEE (\$)				
<input type="checkbox"/> BASIC FEE <small>(37 CFR 1.16(a), (b), or (c))</small>		N/A	N/A		N/A				N/A					
<input type="checkbox"/> SEARCH FEE <small>(37 CFR 1.16(k), (l), or (m))</small>		N/A	N/A		N/A		N/A							
<input type="checkbox"/> EXAMINATION FEE <small>(37 CFR 1.16(o), (p), or (q))</small>		N/A	N/A		N/A		N/A							
TOTAL CLAIMS <small>(37 CFR 1.16(i))</small>		minus 20 =	*		X \$ =		OR		X \$ =					
INDEPENDENT CLAIMS <small>(37 CFR 1.16(h))</small>		minus 3 =	*		X \$ =		OR		X \$ =					
<input type="checkbox"/> APPLICATION SIZE FEE <small>(37 CFR 1.16(s))</small>		If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).												
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENT <small>(37 CFR 1.16(j))</small>														
					TOTAL				TOTAL					
* If the difference in column 1 is less than zero, enter "0" in column 2.														
APPLICATION AS AMENDED – PART II														
(Column 1)			(Column 2)			(Column 3)			SMALL ENTITY		OR		OTHER THAN SMALL ENTITY	
AMENDMENT	12/08/2009		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE (\$)	ADDITIONAL FEE (\$)		RATE (\$)	ADDITIONAL FEE (\$)			
	Total <small>(37 CFR 1.16(i))</small>		* 10	Minus	** 20	= 0	X \$ =		OR	X \$52=	0			
	Independent <small>(37 CFR 1.16(h))</small>		* 3	Minus	***3	= 0	X \$ =		OR	X \$220=	0			
	<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>													
	<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>													
					TOTAL ADD'L FEE				OR		TOTAL ADD'L FEE		0	
AMENDMENT			CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE (\$)	ADDITIONAL FEE (\$)		RATE (\$)	ADDITIONAL FEE (\$)			
	Total <small>(37 CFR 1.16(i))</small>		*	Minus	**	=	X \$ =		OR	X \$ =				
	Independent <small>(37 CFR 1.16(h))</small>		*	Minus	***	=	X \$ =		OR	X \$ =				
	<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>													
	<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>													
					TOTAL ADD'L FEE				OR		TOTAL ADD'L FEE			
* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.														
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".														
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".														
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.														
Legal Instrument Examiner: /DARRYL FORTE/														

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail** Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

22850 7590 09/25/2009

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

CUSTOMER NUMBER
22850

APPLICATION NO	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/205,121	08/17/2005	Jun Haishima	276900US90	2910

TITLE OF INVENTION: GAMING APPARATUS HAVING MEMORY FAULT DETECTION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	12/28/2009

EXAMINER	ART UNIT	CLASS-SUBCLASS
I.E. DIEU MINH T	2114	714-036000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
 Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
 "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list
 (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Oblon, Spivak.
 2 McClelland, Maier
 3 & Neustadt, L.L.P.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)
 PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE UNIVERSAL ENTERTAINMENT CORPORATION
 (B) RESIDENCE: (CITY and STATE OR COUNTRY) Tokyo, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are submitted:
 Issue Fee
 Publication Fee (No small entity discount permitted)
 Advance Order - # of Copies _____

4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above)
 A check is enclosed.
 Payment by credit card. Transmitted via EFS-Web.
 The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number 15-0030 (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)
 a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature  Date DEC 24 2009
 Typed or printed name Paul J. Killos Registration No. Division No. 50,011

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

DOCKET NO.: 276900US8/ssa

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Jun HAISHIMA

SERIAL NUMBER: 11/205,121

GROUP: 2114

FILED: August 17, 2005

EXAMINER: LE, DIEU MINH T

FOR: GAMING APPARATUS HAVING MEMORY FAULT DETECTION

REQUEST TO CORRECT TITLE OF INVENTION

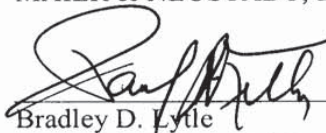
MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

SIR:

In the matter of the above-identified application for patent, we hereby request correction of your records to reflect the correct title of the invention. The title of the invention should read as follows: **GAMING APPARATUS HAVING MEMORY FAULT DETECTION.**

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, L.L.P.


Bradley D. Lytle
Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 07/09)

Derek P. Benke

Registration No. 56,944

Paul J. Killos
Registration No. 58,014

Electronic Patent Application Fee Transmittal

Application Number:	11205121			
Filing Date:	17-Aug-2005			
Title of Invention:	Gaming apparatus having memory fault detection			
First Named Inventor/Applicant Name:	Jun Haishima			
Filer:	Marvin Jay Spivak/Mimi Chanthaphone			
Attorney Docket Number:	276900US90			
Filed as Large Entity				
Utility under 35 USC 111(a) Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1510	1510
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1810

Electronic Acknowledgement Receipt

EFS ID:	6706651
Application Number:	11205121
International Application Number:	
Confirmation Number:	2910
Title of Invention:	Gaming apparatus having memory fault detection
First Named Inventor/Applicant Name:	Jun Haishima
Customer Number:	22850
Filer:	Marvin Jay Spivak/Mimi Chanthaphone
Filer Authorized By:	Marvin Jay Spivak
Attorney Docket Number:	276900US90
Receipt Date:	24-DEC-2009
Filing Date:	17-AUG-2005
Time Stamp:	10:42:41
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$1810
RAM confirmation Number	10031
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
-----------------	----------------------	-----------	-------------------------------------	------------------	------------------

1		276900USISSUEFEE.pdf	117762	yes	2
			3a873b2a4278c6116944b2093818713d93d3ec8f		
Multipart Description/PDF files in .zip description					
		Document Description	Start	End	
		Issue Fee Payment (PTO-85B)	1	1	
		Miscellaneous Incoming Letter	2	2	
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	31492	no	2
			b28549c35a5386003193041266d4a79bc98ce1be		
Warnings:					
Information:					
Total Files Size (in bytes):			149254		
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with 7 columns: APPLICATION NUMBER, FILING or 371(c) DATE, GRP ART UNIT, FIL FEE REC'D, ATTY DOCKET NO, TOT CLAIMS, IND CLAIMS. Row 1: 11/205,121, 08/17/2005, 2114, 1300, 276900US90, 3, 1

CONFIRMATION NO. 2910

CORRECTED FILING RECEIPT

22850
OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L.P.
1940 DUKE STREET
ALEXANDRIA, VA 22314



Date Mailed: 12/30/2009

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections

Applicant(s)

Jun Haishima, Tokyo, JAPAN;

Assignment For Published Patent Application

Aruze Corp., Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 022850

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2004-245337 08/25/2004

If Required, Foreign Filing License Granted: 09/02/2005

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US 11/205,121

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

GAMING APPARATUS HAVING MEMORY FAULT DETECTION

Preliminary Class

714

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

Applicants also are advised that in the case of inventions made in the United States, the Director of the USPTO must issue a license before applicants can apply for a patent in a foreign country. The filing of a U.S. patent application serves as a request for a foreign filing license. The application's filing receipt contains further information and guidance as to the status of applicant's license for foreign filing.

Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at <http://www.uspto.gov/web/offices/pac/doc/general/index.html>.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, <http://www.stopfakes.gov>. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4158).

LICENSE FOR FOREIGN FILING UNDER**Title 35, United States Code, Section 184****Title 37, Code of Federal Regulations, 5.11 & 5.15****GRANTED**

The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as

set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Bureau of Industry and Security, Department of Commerce (15 CFR parts 730-774); the Office of Foreign Assets Control, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

NOT GRANTED

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2910

SERIAL NUMBER 11/205,121	FILING OR 371(c) DATE 08/17/2005 RULE	CLASS 714	GROUP ART UNIT 2114	ATTORNEY DOCKET NO. 276900US90
------------------------------------	---	---------------------	-------------------------------	--

APPLICANTS
 Jun Haishima, Tokyo, JAPAN;

**** CONTINUING DATA *******

**** FOREIGN APPLICATIONS *******
 JAPAN 2004-245337 08/25/2004

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
**** 09/02/2005**

Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 3	INDEPENDENT CLAIMS 1	
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance					
Verified and Acknowledged	Examiner's Signature _____	Initials _____			

ADDRESS
 22850

TITLE
 GAMING APPARATUS HAVING MEMORY FAULT DETECTION

FILING FEE RECEIVED 1300	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)
		<input type="checkbox"/> Other _____
		<input type="checkbox"/> Credit

Docket No.: 276900US8/jkl

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Jun HAISHIMA

SERIAL NUMBER: 11/205,121

GROUP: 2114

FILED: August 17, 2005

EXAMINER: LE, DIEU MINH T.

FOR: **GAMING APPARATUS HAVING MEMORY FAULT DETECTION**

O.K. to enter

/dml/

01/04/10

AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. § 1.312

MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

SIR:

In response to the Notice of Allowability dated September 25, 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with 5 columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO. Includes fields for EXAMINER (LE, DIEU MINH T), ART UNIT (2114), PAPER NUMBER, NOTIFICATION DATE (01/12/2010), and DELIVERY MODE (ELECTRONIC).

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

patentdocket@oblon.com
oblonpat@oblon.com
jgardner@oblon.com

Response to Rule 312 Communication	Application No.	Applicant(s)
	11/205,121	HAISHIMA, JUN
	Examiner	Art Unit
	Dieu-Minh Le	2114

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

1. The amendment filed on 08 December 2009 under 37 CFR 1.312 has been considered, and has been:
- a) entered.
 - b) entered as directed to matters of form not affecting the scope of the invention.
 - c) disapproved because the amendment was filed after the payment of the issue fee.
Any amendment filed after the date the issue fee is paid must be accompanied by a petition under 37 CFR 1.313(c)(1) and the required fee to withdraw the application from issue.
 - d) disapproved. See explanation below.
 - e) entered in part. See explanation below.

The request to correct title of invention filed on 12/24/2009 also has been entered.

/Scott T Baderman/
Supervisory Patent Examiner, Art Unit 2114



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P. O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	ISSUE DATE	PATENT NO.	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/205,121	02/16/2010	7664988	276900US90	2910

22850 7590 01/27/2010
OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, L.L.P.
1940 DUKE STREET
ALEXANDRIA, VA 22314

ISSUE NOTIFICATION

The projected patent number and issue date are specified above.

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(application filed on or after May 29, 2000)

The Patent Term Adjustment is 439 day(s). Any patent to issue from the above-identified application will include an indication of the adjustment on the front page.

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Application Assistance Unit (AAU) of the Office of Data Management (ODM) at (571)-272-4200.

APPLICANT(s) (Please see PAIR WEB site <http://pair.uspto.gov> for additional applicants):

Jun Haishima, Tokyo, JAPAN;

Under the Paperwork Reduction Act of 1995 no persons are required to respond to a collection of information unless it displays a valid OMB control number

PATENT - POWER OF ATTORNEY OR REVOCATION OF POWER OF ATTORNEY WITH A NEW POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS	Patent Number	7,664,988
	Issue Date	February 16, 2010
	First Named Inventor	Jun Haishima
	Title	Gaming Apparatus Having Memory Fault Detection
	Attorney Docket No.	070059-2

I hereby revoke all previous powers of attorney given in the above-identified patent.

A Power of Attorney is submitted herewith.

OR
 I hereby appoint Practitioner(s) associated with the Customer Number identified in the box at right as my/our attorney(s) or agent(s) with respect to the patent identified above, and to transact all business in the United States Patent and Trademark Office connected therewith:

31013

OR
 I hereby appoint Practitioner(s) named below as my/our attorney(s) or agent(s) with respect to the patent identified above, and to transact all business in the United States Patent and Trademark Office connected therewith:

Practitioner(s) Name	Registration Number

Please recognize or change the correspondence address for the above-identified patent to:

The address associated with the above-identified Customer Number.

OR
 The address associated with the Customer Number identified in the box at right:

Firm or Individual Name

Address

City

State

Zip

Country

Telephone

Email

I am the:

Applicant.

OR

Patent owner.

Statement under 37 CFR 3.73(c) (Form PTO/AIA/96) submitted herewith or filed on _____.

SIGNATURE of Applicant or Patent Owner

Signature

Joseph Ward

Date

JUNE 1, 2020

Name

Joseph Ward

Telephone

Title and Company

President, Bot M8

NOTE: Signatures of all the applicants or patent owners of the entire interest or their representative(s) are required. If more than one signature is required, submit multiple forms, check the box below, and identify the total number of forms submitted in the blank below.

A total of 1 forms are submitted.

This collection of information is required by 37 CFR 1.31, 1.32, and 1.33. The information is required to obtain or retain a benefit by the public, which is to update (and by the USPTO to process) the file of a patent or reexamination proceeding. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Privacy Act Statement

The **Privacy Act of 1974 (P.L. 93-579)** requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (*i.e.*, GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

STATEMENT UNDER 37 CFR 3.73(c)

Applicant/Patent Owner: BOT M8 LLC
 Application No./Patent No.: 7,664,988 Filed/Issue Date: February 16, 2010
 Titled: Gaming Apparatus Having Memory Fault Detection
BOT M8 LLC, a corporation
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that, for the patent application/patent identified above, it is (choose **one** of options 1, 2, 3 or 4 below):

1. The assignee of the entire right, title, and interest.
2. An assignee of less than the entire right, title, and interest (check applicable box):
- The extent (by percentage) of its ownership interest is _____%. Additional Statement(s) by the owners holding the balance of the interest must be submitted to account for 100% of the ownership interest.
- There are unspecified percentages of ownership. The other parties, including inventors, who together own the entire right, title and interest are:

Additional Statement(s) by the owner(s) holding the balance of the interest must be submitted to account for the entire right, title, and interest.

3. The assignee of an undivided interest in the entirety (a complete assignment from one of the joint inventors was made). The other parties, including inventors, who together own the entire right, title, and interest are:

Additional Statement(s) by the owner(s) holding the balance of the interest must be submitted to account for the entire right, title, and interest.

4. The recipient, via a court proceeding or the like (e.g., bankruptcy, probate), of an undivided interest in the entirety (a complete transfer of ownership interest was made). The certified document(s) showing the transfer is attached.

The interest identified in option 1, 2 or 3 above (not option 4) is evidenced by either (choose **one** of options A or B below):

- A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

- B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: Jun Haishima To: Aruze Corp.

The document was recorded in the United States Patent and Trademark Office at
 Reel 016869, Frame 0474, or for which a copy thereof is attached.

2. From: Aruze Corp. To: Universal Entertainment Corporation

The document was recorded in the United States Patent and Trademark Office at
 Reel 023610, Frame 0363, or for which a copy thereof is attached.

[Page 1 of 2]

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

STATEMENT UNDER 37 CFR 3.73(c)

3. From: Universal Entertainment Corporation To: BOT M8 LLC

The document was recorded in the United States Patent and Trademark Office at
Reel 040823, Frame 0741, or for which a copy thereof is attached.

4. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

5. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

6. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

Additional documents in the chain of title are listed on a supplemental sheet(s).

As required by 37 CFR 3.73(c)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

/Jeffrey H. Price/

June 3, 2020

Signature

Date

Jeffrey H. Price

69,141

Printed or Typed Name

Title or Registration Number

Privacy Act Statement

The **Privacy Act of 1974 (P.L. 93-579)** requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (*i.e.*, GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Electronic Acknowledgement Receipt

EFS ID:	39620904
Application Number:	11205121
International Application Number:	
Confirmation Number:	2910
Title of Invention:	GAMING APPARATUS HAVING MEMORY FAULT DETECTION
First Named Inventor/Applicant Name:	Jun Haishima
Customer Number:	22850
Filer:	Jeffrey Hobish Price/Diane Torniali
Filer Authorized By:	Jeffrey Hobish Price
Attorney Docket Number:	276900US90
Receipt Date:	03-JUN-2020
Filing Date:	17-AUG-2005
Time Stamp:	22:33:25
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
------------------------	----

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Power of Attorney	Patent_POA_USP_7664988.pdf	72615 b7911a2e7282b4b569c8a575e0b37efd946ec4f5	no	2

Warnings:

Information:					
2	Assignee showing of ownership per 37 CFR 3.73	Statement_Under_3_73c_USP-7664988.pdf	89363	no	3
			3c963279999cb0fc23eebef3d10fe871cc806810		
Warnings:					
Information:					
Total Files Size (in bytes):				161978	
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NUMBER	FILING OR 371(C) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
11/205,121	08/17/2005	Jun Haishima	070059-2

CONFIRMATION NO. 2910

POA ACCEPTANCE LETTER

31013
KRAMER LEVIN NAFTALIS & FRANKEL LLP
INTELLECTUAL PROPERTY DEPARTMENT
1177 AVENUE OF THE AMERICAS
NEW YORK, NY 10036



Date Mailed: 06/12/2020

NOTICE OF ACCEPTANCE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 06/03/2020.

The Power of Attorney in this application is accepted. Correspondence in this application will be mailed to the above address as provided by 37 CFR 1.33.

Questions about the contents of this notice and the requirements it sets forth should be directed to the Office of Data Management, Application Assistance Unit, at (571) 272-4000 or (571) 272-4200 or 1-888-786-0101.

/snguyen/



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NUMBER	FILING OR 371(C) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE
11/205,121	08/17/2005	Jun Haishima	276900US90

22850
OBLON, MCCLELLAND, MAIER & NEUSTADT, L.L.P.
1940 DUKE STREET
ALEXANDRIA, VA 22314

CONFIRMATION NO. 2910
POWER OF ATTORNEY NOTICE



Date Mailed: 06/12/2020

NOTICE REGARDING CHANGE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 06/03/2020.

- The Power of Attorney to you in this application has been revoked by the assignee who has intervenered as provided by 37 CFR 3.71. Future correspondence will be mailed to the new address of record(37 CFR 1.33).

Questions about the contents of this notice and the requirements it sets forth should be directed to the Office of Data Management, Application Assistance Unit, at (571) 272-4000 or (571) 272-4200 or 1-888-786-0101.

/snguyen/