US005522076A

United States Patent [19]

Dewa et al.

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[45] Date of Patent: May 28, 1996

[54] **COMPUTER SYSTEM HAVING BIOS (BASIC INPUT/OUTPUT SYSTEM)-ROM (READ ONLY MEMORY) WRITING FUNCTION**

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[73] Assignee: **Kabushiki Kaisha Toshiba**, Tokyo, Japan

[21] Appl. No.: **234,475**

[22] Filed: **Apr. 28, 1994**

[30] **Foreign Application Priority Data**

May 13, 1993 [JP] Japan 5-111738
Aug. 30, 1993 [JP] Japan 5-213887

[51] **Int. Cl.**[6] **G06F 9/44**

[52] **U.S. Cl.** **395/700**; 364/DIG. 1; 364/280.2

[58] **Field of Search** ..... 395/700; 364/DIG. 1 MS File

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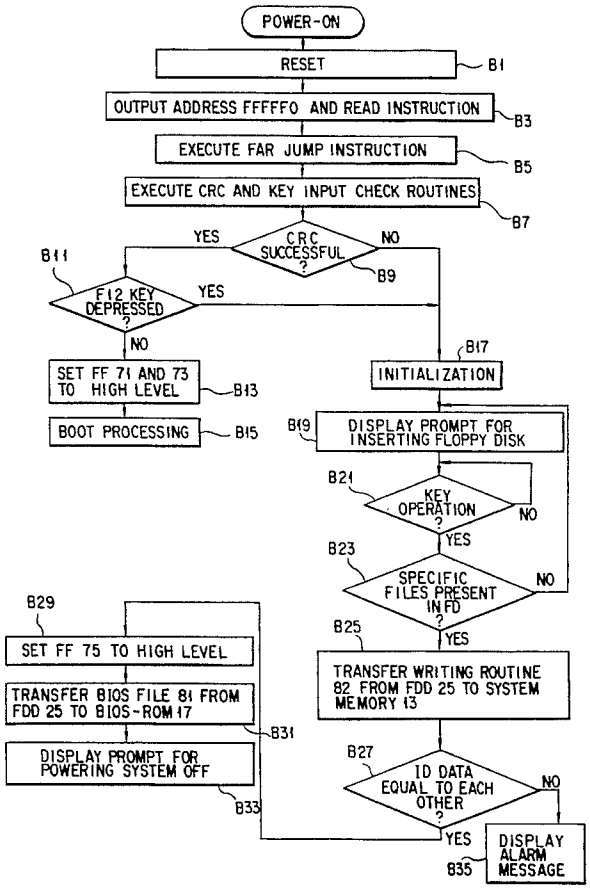
Primary Examiner—Thomas M. Heckler
Attorney, Agent, or Firm—Limbach & Limbach; Alan S. Hodes

[57] **ABSTRACT**

A flash memory used as a BIOS-ROM has a main block storing a BIOS and a boot block storing minimum programs executed in initializing the system. Upon a power-on operation, when a rewriting unit is connected to an expansion bus connector, a new boot block stored in a ROM of the rewriting unit is written in the flash memory. Upon the power-on operation, a key depression detecting routine in the boot block is executed to check whether a predetermined key is depressed. If the predetermined key is determined to be depressed, a rewriting program in a floppy disk is loaded in the system and executed, thereby rewriting the content of the main block into the BIOS file stored in the floppy disk.

16 Claims, 13 Drawing Sheets



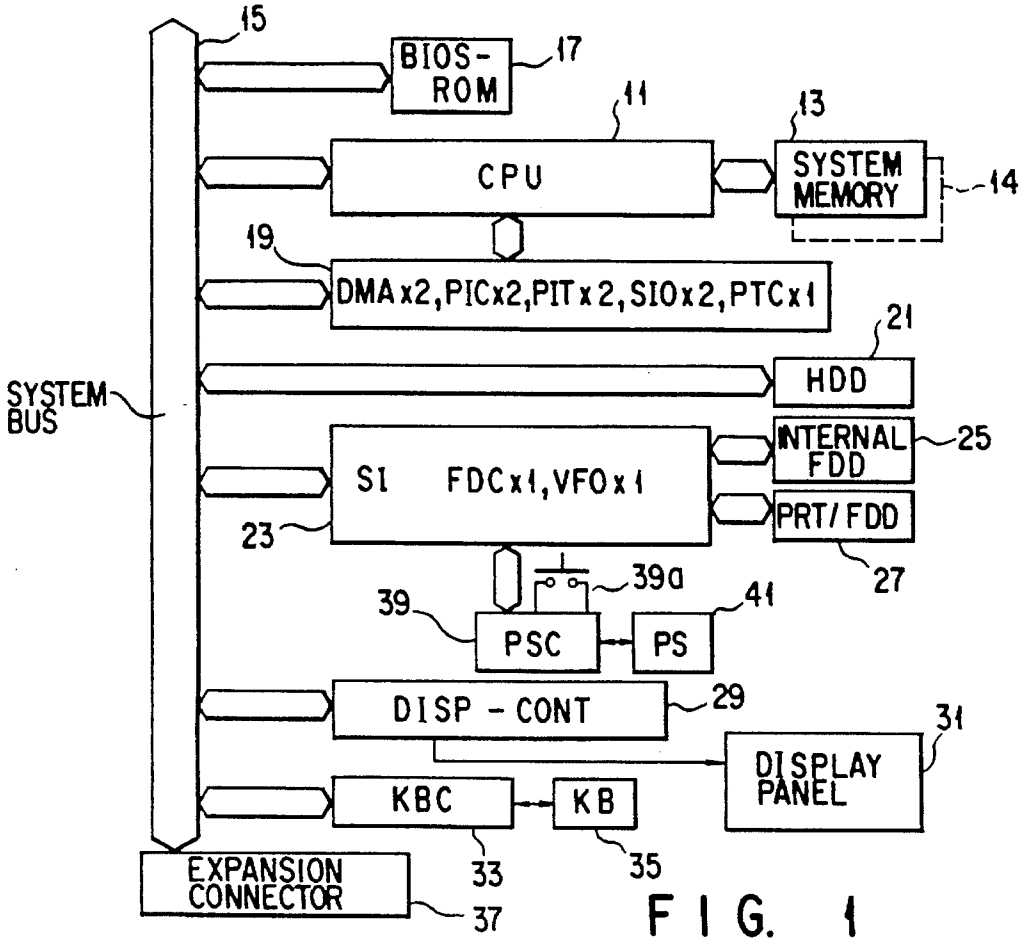


FIG. 1

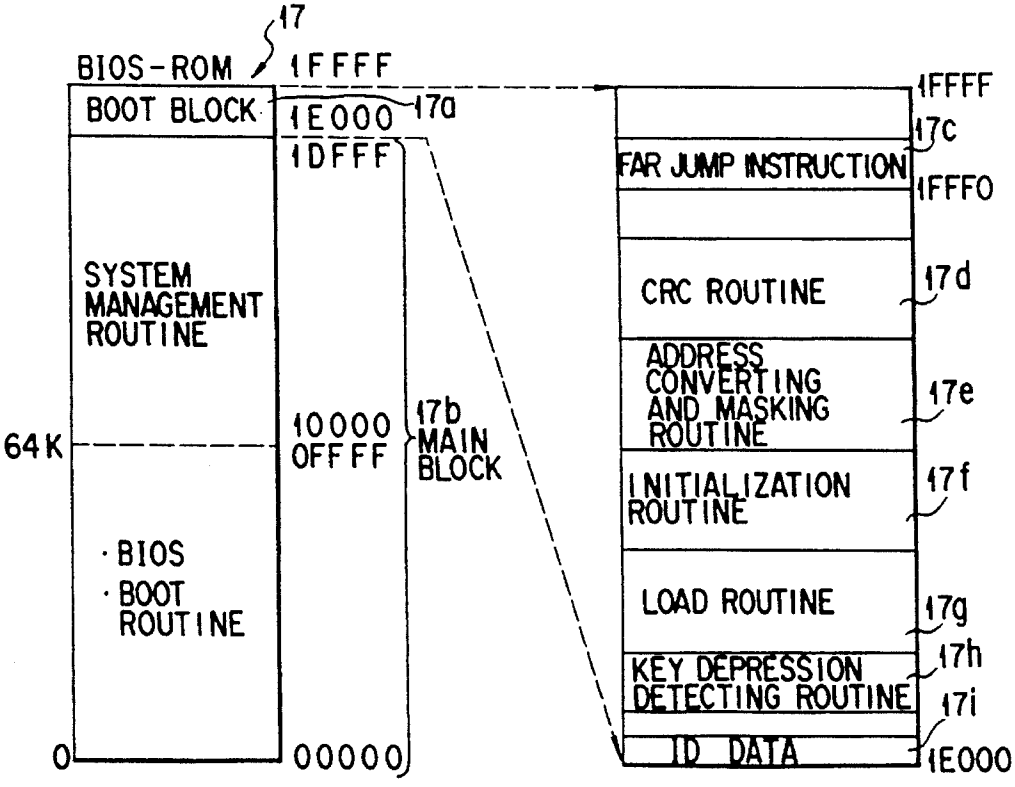


FIG. 2

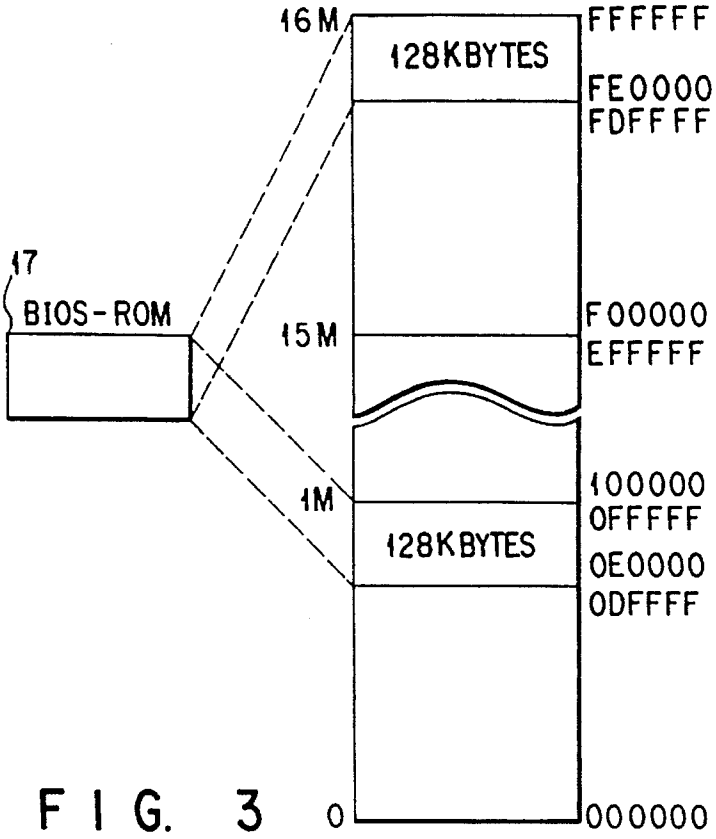


FIG. 3

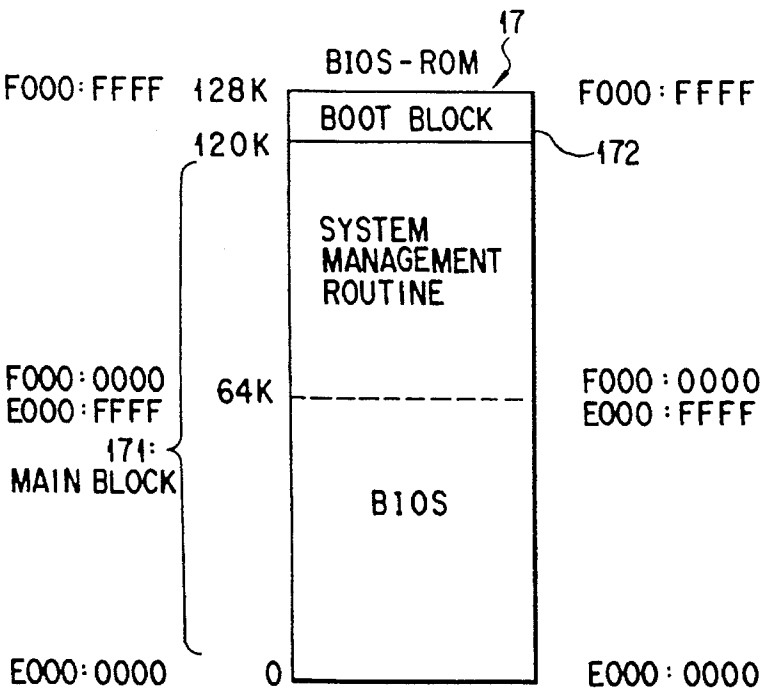


FIG. 4A

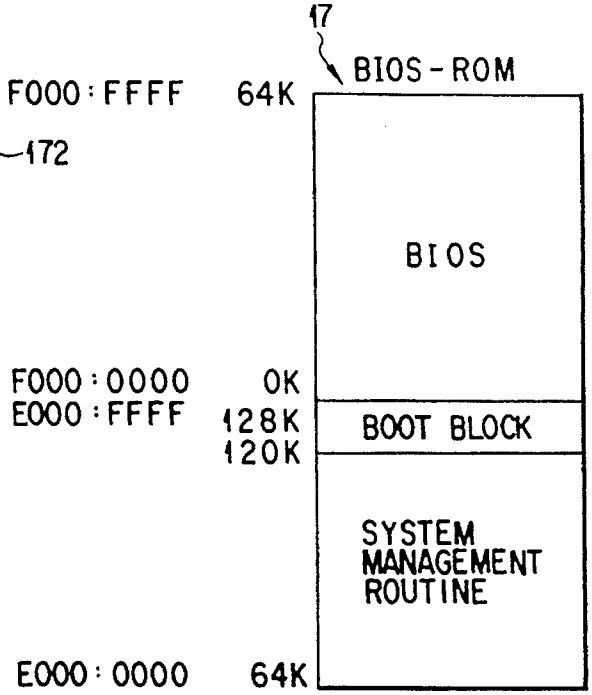


FIG. 4B

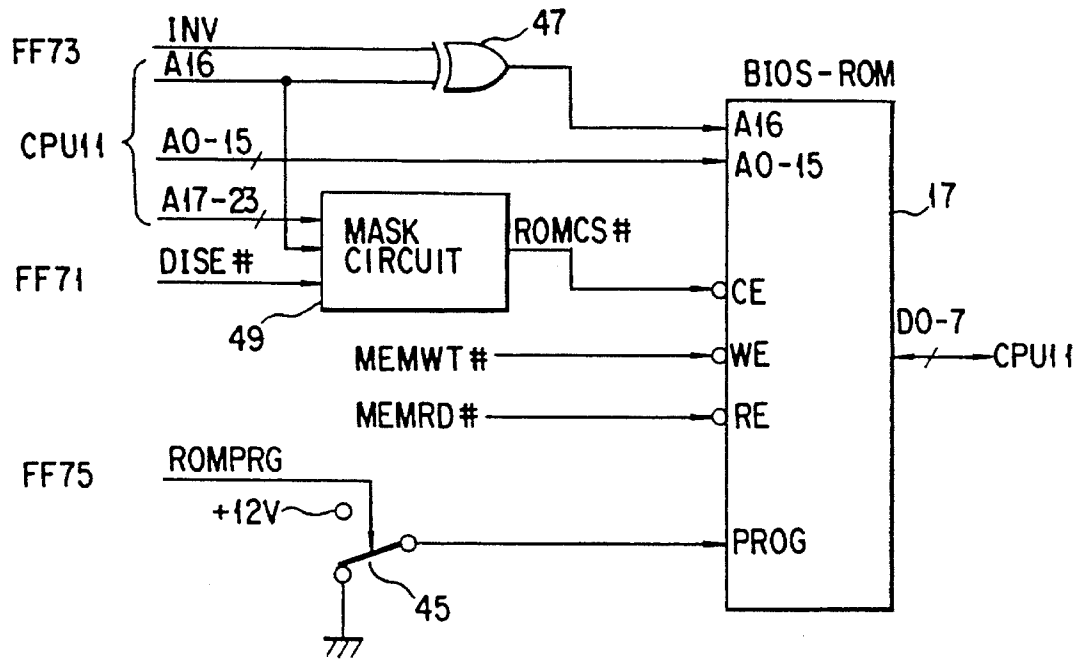


FIG. 5

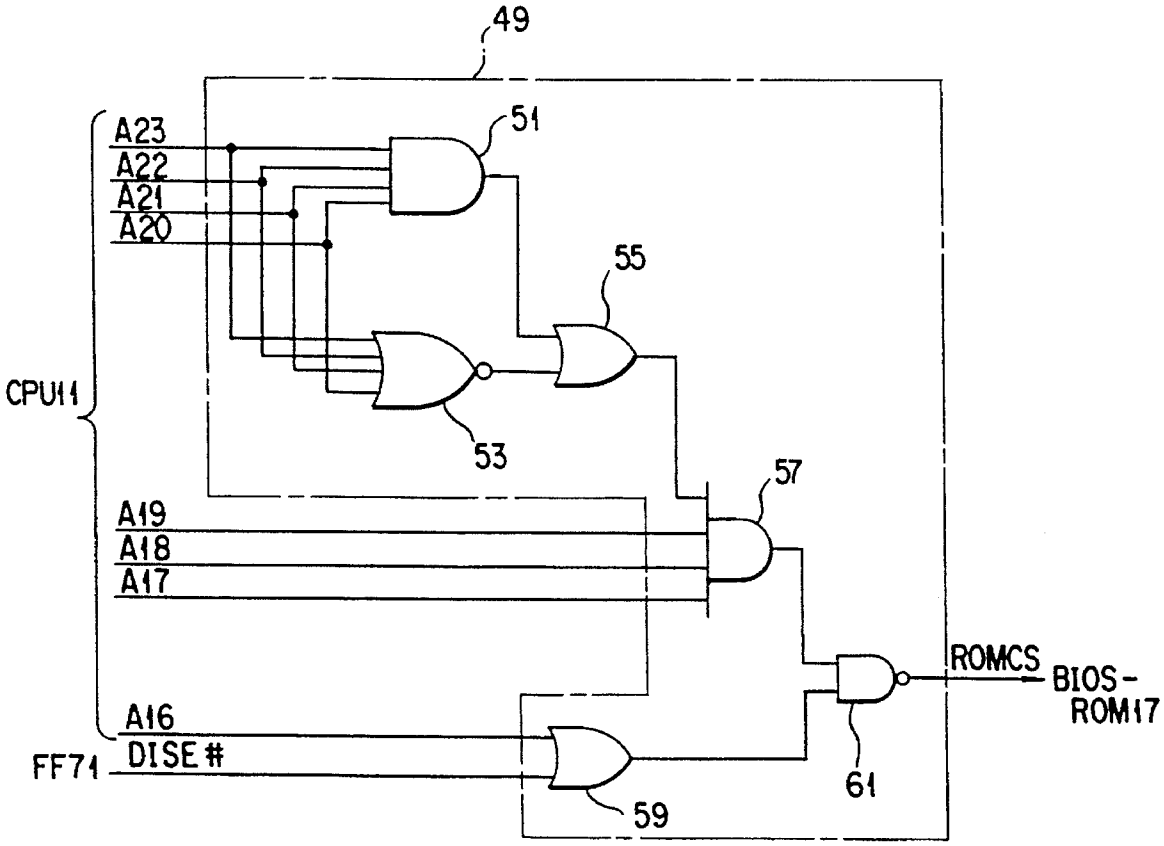


FIG. 6

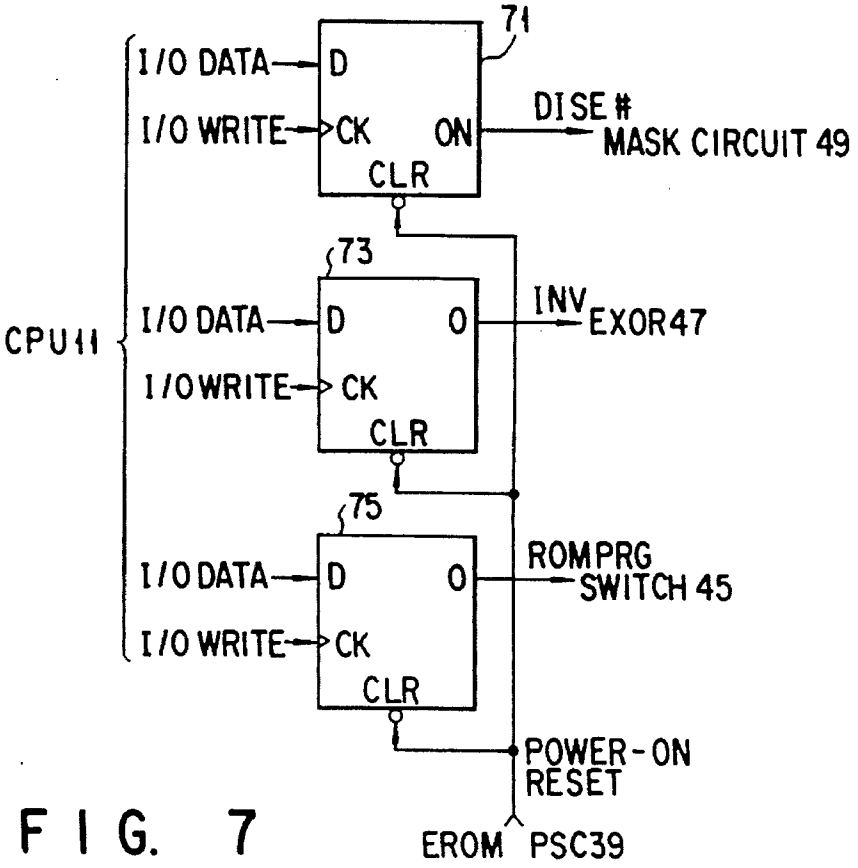


FIG. 7

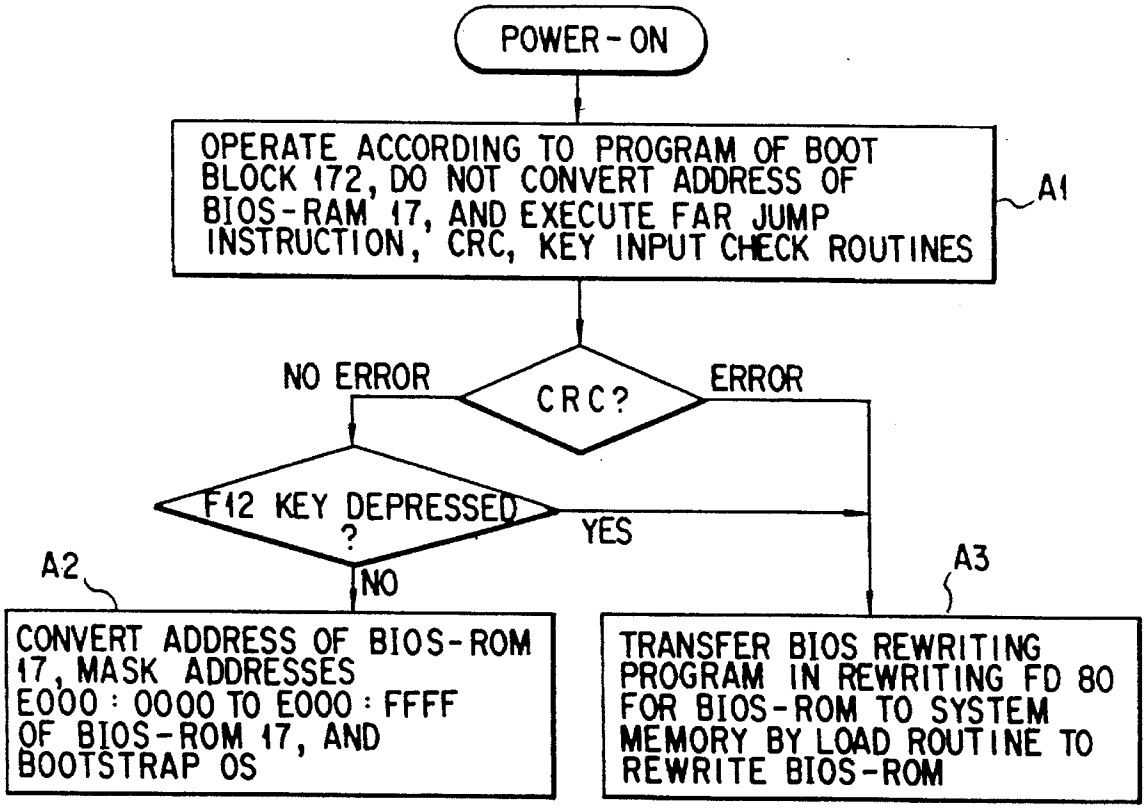


FIG. 8

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